# Steps:

1. Check text file format.
   1. Make sure every register definition in text file was separate with two “\n”.
   2. Make sure ‘:’ follows register name in “SFR Definition” row
   3. Fix mistake in text file from “3-2” to “3:2
   4. Fix irregular format “0000: P0.0 1000: P2.0”
2. Manual requirement
   1. Add top and bottom line in “Version” sheet.
   2. Name new value for all cell with “???”
   3. Move multi bits field to correct row and bit field value.
   4. Adjust row width for beautiful requirement.
   5. Deal with some two conditions issue which makes extra rows.
   6. Take care about special column with ‘v’ requirement.
   7. Any other unusual value in cell
3. Need to compare final excel file with PDF version datasheet.

# Result:

|  |  |  |
| --- | --- | --- |
| F93x-92x Peripheral | Manual fix | Compare with PDF |
| UART | **DONE** | **DONE** |
| VREG | **DONE** | **DONE** |
| VREF | **DONE** | **DONE** |
| TIMER\_OVERVIEW | **DONE** | **DONE** |
| TIMERS\_0\_1 | **DONE** | **DONE** |
| TIMER\_2 | **DONE** | **DONE** |
| TIMER\_3 | **DONE** | **DONE** |
| TEMPSENSOR | **DONE** | **DONE** |
| SPI | **DONE** | **DONE** |
| SMBUS | **DONE** | **DONE** |
| SMARTCLOCK | **DONE** | **DONE** |
| SAR10 | **DONE** | **DONE** |
| RESET | **DONE** | **DONE** |
| POWER | **DONE** | **DONE** |
| PORTIO | **DONE** | **DONE** |
| PCA | **DONE** | **DONE** |
| IREF | **DONE** | **DONE** |
| FLASH | **DONE** | **DONE** |
| EMIF\_12BIT | **DONE** | **DONE** |
| DCDC | **DONE** | **DONE** |
| COMPARATORS | **DONE** | **DONE** |
| COMPARATORS\_MUX | **DONE** | **DONE** |
| CLOCKING | **DONE** | **DONE** |
| CIP8051 | **DONE** | **DONE** |
| C2 | **DONE** | **DONE** |
| ADC\_MUX | **DONE** | **DONE** |

# Reference manual error list:

C8051F93x-92x.PDF, Rev 1.3 5/2

|  |
| --- |
| 1. SFR Definition 25.12. TMR2H Timer 2 High Byte, there is “Timer 2 Low Byte.” Should be “Timer 2 High Byte” |

1. SFR Definition 21.18. P2: Port2, “Read” and “Write” should be switched
2. SFR Definition 7.4. CPT1MD, bit 6, “read=00b “, should be “read=0b”
3. SFR Definition 19.3. OSCICL, bit 6, type “R”, should be “R/W”
4. SFR Definition 19.4. OSCXCN, bit 6, type “R”, should be “R/W”
5. C2 Register Definition 27.2. DEVICEID, in Reset cell, it is 0x14; in function description, it is 0x16.