

Mark Gilliland  
2019

TITLE: microBackpack

Document Number:

REV:  
0.9

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1

2

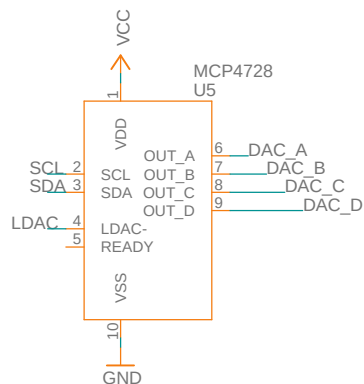
3

4

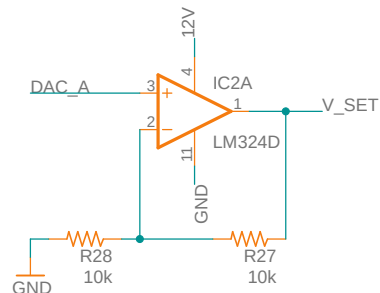
5

6

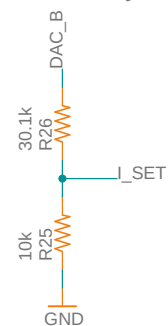
A



VSET mult by 2.6

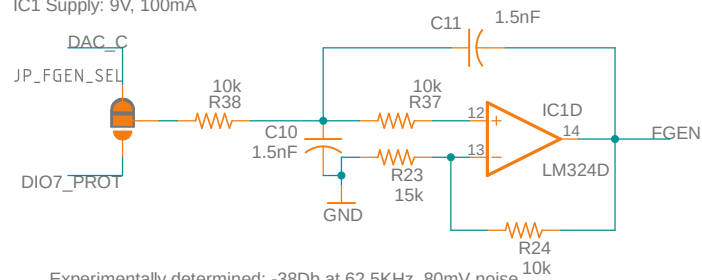


ISET div by 4



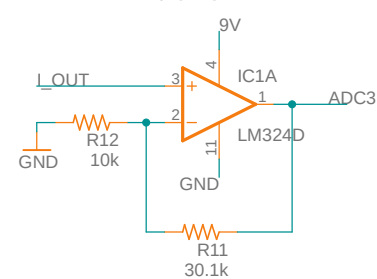
FGEN Multiply by 1.66  
Filter at  $F_c = 110\text{kHz}$   
IC1 Supply: 9V, 100mA

Change: C10 was moved to the other side of R37.  
R38 was changed to 47k  
 $F_c$  is now 5.1kHz

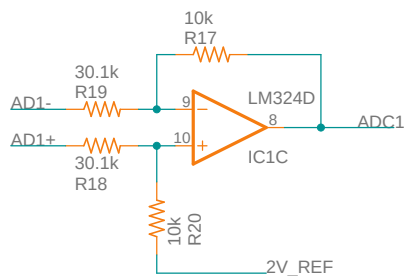
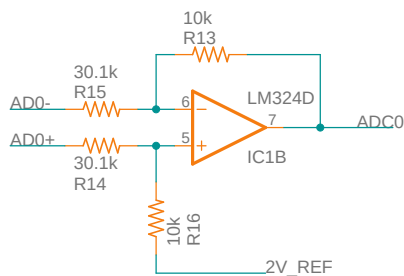


Experimentally determined: -38Db at 62.5KHz, 80mV noise

IOUT multiply by 4



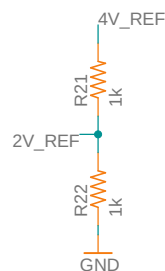
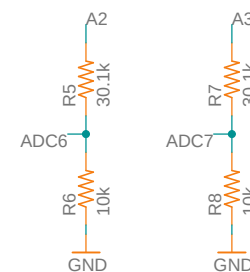
Analog Input Differential Amp  
2.048V centered  
1/3 ratio, max of ~12V



VOUT div by 2.4



Analog Input div by 4



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D

E

A

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C

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