				Op (Code		Operands												
Command	Description	Туре	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
ADD	ADD Register and Register, Store in Register	ALU	0	0	0	1	D	Dest Reg			rc Reg	1	0	0	0	Sr	c Reg	2	
ADD	ADD Register and Immediate Value, Store in Register	ALU	0	0	0	1	D	est Re	eg		Src Reg 1				Imm Val 5				
AND	AND and Store 2 values	ALU	0	1	0	1	Dest Reg			Src Reg 1			0	0	0	Src Reg 2			
AND	AND Register and Immediate Value, Store in Register	ALU	0	1	0	1	Dest Reg			Src Reg 1			1		lm	Imm Val 5			
NOT	Store Bitwise NOT of Register in Register	ALU	1	0	0	1	Dest Reg				Src Re	g	0	0	0	0	0	0	
BR	Branch to Label based on CC register	Control	0	0	0	0	n	z p PC Offset 9											
JMP	Jump based on Register	Control	1	1	0	0	0	0	0	В	ase Re	eg	0	0	0	0	0	0	
JSR	Jump to Label and store return on R7	Control	0	1	0	0	1	0	0	PC Offset 9									
JSRR	Jump based on Register and store return on R7	Control	0	1	0	0	0	0	0	В	ase Re	eg	0	0	0	0	0	0	
RET	Return to address in R7	Control	1	1	0	0	0	0	0	1	1	1	0	0	0	0	0	0	
LD	Load Label value into Register	Memory Access	0	0	1	0	D	est Re	eg	PC Offset 9									
LDI	Load address in Label into Register	Memory Access	1	0	1	0	D	est Re	eg	PC Offset 9									
LDR	Load value from Register + Offset into Register	Memory Access	0	1	1	0	D	est Re	eg	Base Reg PC Offset 6									
LEA	Load address of Label into Register	Memory Access	1	1	1	0	D	est Re	eg	PC Offset 9									
ST	Store Register into Label address	Memory Access	0	0	1	1	S	Src Re	g	PC Offset 9									
STI	Store Register into Label address	Memory Access	1	0	1	1	S	Src Re	g	PC Offset 9									
STR	Store value in Register from address in Register + Offset	Memory Access	0	1	1	1	Src Reg			Base Reg				PC Offset 6					
TRAP	Perform trap routine of vector	System	1	1	1	1	0	0	0	0			Trap Vector 8						
NOP	No Operation	System	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
reserved	Illegal Instruction	System	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	