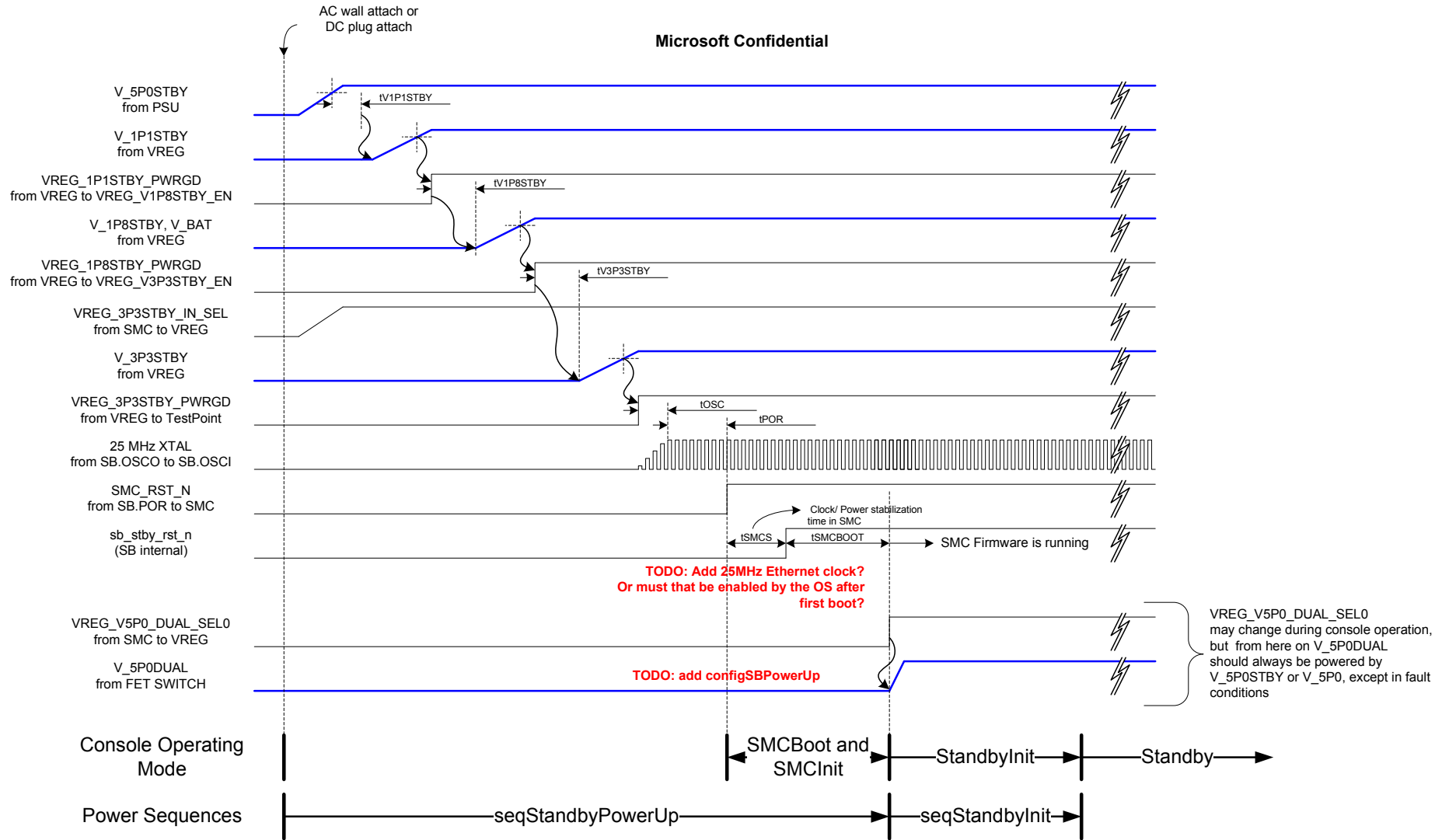
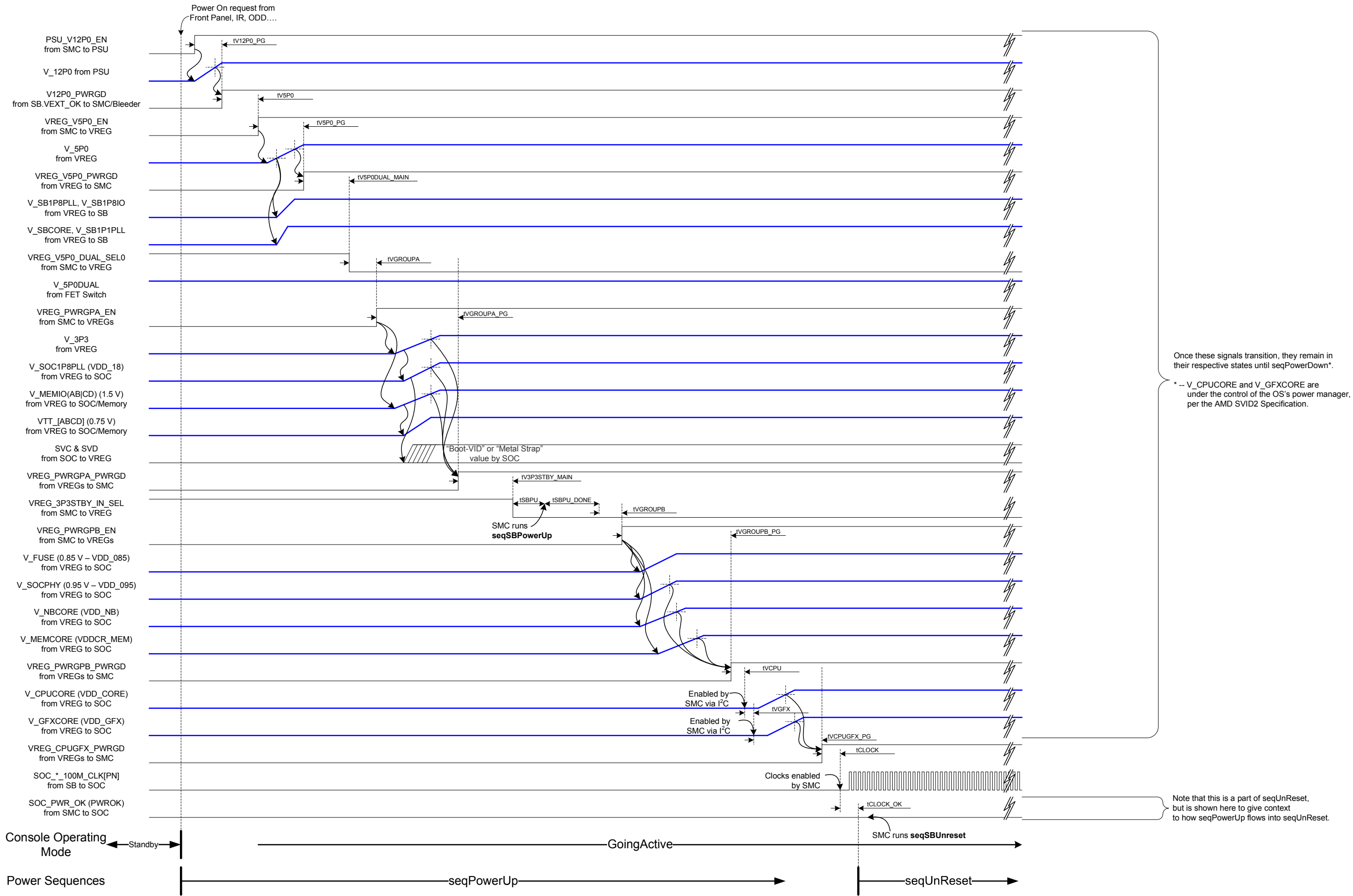


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Once these signals transition, they remain in their respective states until seqPowerDown*.

* -- V_CPUCORE and V_GFXCORE are under the control of the OS's power manager, per the AMD SVID2 Specification.

Note that this is a part of seqUnReset, but is shown here to give context to how seqPowerUp flows into seqUnReset.