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THIS IS THE README FILE FOR LAB 5.

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When answering the questions in this file, make a point to take a look at whether the most significant bit (remembering it can be bit 7, 15, 31 or 63 depending upon what size value we are working with) to see if the results you see change based on whether it is a 0 or a 1.

```
.file "lab5.s"
.globl main
.type    main, @function
```

```
.text
main:
    pushq %rbp                #stack housekeeping
    movq %rsp, %rbp
```

```
Label1:
    movq $0x8877665544332211, %rax    #as you go through this program note the changes to %rip starts with 0x55555555168 then 1a4
    movb $-1, %al                    # the value of %rax is: 0x8877665544332211
    movw $-1, %ax                    # the value of %rax is: 0x88776655443322ff
    movl $-1, %eax                   # the value of %rax is: 0x887766554433ffff
    movq $-1, %rax                   # the value of %rax is: 0xffffffff
    movl $-1, %eax                   # the value of %rax is: 0xffffffff
    cltq                             # the value of %rax is: 0xffffffff
    movl $0x7fffffff, %eax           # the value of %rax is: 0x7fffff
    cltq                             # the value of %rax is: 0x7fffff
    movl $0x8fffffff, %eax           # the value of %rax is: 0x8fffff
    cltq                             # the value of %rax is: 0xffffffff
    # what do you think the cltq instruction does?
    # Extends the sign value or the registers' value
    movq $0x8877665544332211, %rax    # the value of %rax is: 0x8877665544332211
    # the value of %rdx *before* movb $0xAA, %dl executes is: 0x7fffffff028
    movb $0xAA, %dl                 # the value of %rdx is: 0xffffffff0aa
    movb %dl, %al                   # the value of %rax is: 0x88776655443322aa
    movsbw %dl, %eax                # the value of %rax is: 0x887766554433ffaa
    movzbw %dl, %eax                # the value of %rax is: 0x88776655443300aa
    movq $0x8877665544332211, %rax    # the value of %rax is: 0x8877665544332211
    movb %dl, %al                   # the value of %rax is: 0x88776655443322aa
    movsbl %dl, %eax                # the value of %rax is: 0xffffffffaa
    movzbl %dl, %eax                # the value of %rax is: 0xaa
    movq $0x8877665544332211, %rax    # the value of %rax is: 0x8877665544332211
    movb %dl, %al                   # the value of %rax is: 0x88776655443322aa
    movsbq %dl, %rax                # the value of %rax is: 0xffffffffffffaa
    movzbq %dl, %rax                # the value of %rax is: 0xaa
    movq $0x8877665544332211, %rax    # the value of %rax is: 0x8877665544332211
    # the value of %rdx *before* movb $0x55, %dl executes is: 0x7fffffff0aa
    movb $0x55, %dl                 # the value of %rdx is: 0xffffffff055
    movb %dl, %al                   # the value of %rax is: 0x8877665544332255
    movsbw %dl, %eax                # the value of %rax is: 0x8877665544330055
    movzbw %dl, %eax                # the value of %rax is: 0x8877665544330055
    movq $0x8877665544332211, %rax    # the value of %rax is: 0x8877665544332211
    movb %dl, %al                   # the value of %rax is: 0x8877665544332255
    movsbl %dl, %eax                # the value of %rax is: 0x55
    movzbl %dl, %eax                # the value of %rax is: 0x55
    movq $0x8877665544332211, %rax    # the value of %rax is: 0x8877665544332211
    movb %dl, %al                   # the value of %rax is: 0x8877665544332255
    movsbq %dl, %rax                # the value of %rax is: 0x55
    movzbq %dl, %rax                # the value of %rax is: 0x55
```

answer questions below when included

# movq \$0x8877665544332211, %rax	# in executable	
# pushb %al	# the value of %rax is:	0x8877665544332211
# movq \$0, %rax		
# popb %al	# the value of %rax is:	0x11
movq \$0x8877665544332211, %rax	# the value of %rax is:	0x8877665544332211 the value of %rsp is: 0x7fffffffdf00
pushw %ax	# the value of %rsp is:	0x7fffffffdefe
	# the difference between the two values of %rsp is:	f00 – efe = 002
movq \$0, %rax	# the value of %rax is:	0x0
popw %ax	# the value of %rax is:	0x2211 How did the value of %rsp change? it went back to 0x7fffffffdf00
movq \$0x8877665544332211, %rax	# the value of %rax is:	0x8877665544332211 the value of %rsp is: 0x7fffffffdf00
pushw %ax	# the value of %rsp is:	0x7fffffffdefe
	# the difference between the two values of %rsp is:	2
movq \$-1, %rax	# the value of %rax is:	0xffffffffffff
popw %ax	# the value of %rax is:	0xfffffffff2211 How did the value of %rsp change? it went back to 0x7fffffffdf00
	# answer questions below when included	
# movq \$0x8877665544332211, %rax	# in executable	
# pushl %eax	# the value of %rax is:	0x8877665544332211
# movq \$0, %rax		
# popl %eax	# the value of %rax is:	0x44332211
movq \$0x8877665544332211, %rax	# the value of %rax is:	0x8877665544332211 the value of %rsp is: 0x7fffffffdf00
pushq %rax	# the value of %rsp is:	0x7fffffffdef8
	# the difference between the two values of %rsp is:	8
movq \$0, %rax	# the value of %rax is:	0x0
popq %rax	# the value of %rax is:	0x8877665544332211 How did the value of %rsp change? 0x7fffffffdf00
	# what eflags are set?	
movq \$0x500, %rax	# the value of %rax is:	0x500
movq \$0x123, %rcx	# the value of %rcx is:	0x123
# 0x123 - 0x500		
subq %rax, %rcx	# the value of %rax is:	0x500
	# the value of %rcx is:	0xffffffffffc23
	# what eflags are set?	[CF SF IF]
movq \$0x500, %rax	# the value of %rax is:	0x500
movq \$0x123, %rcx	# the value of %rcx is:	0x123
# 0x500 - 0x123		
subq %rcx, %rax	# the value of %rax is:	0x3dd
	# what eflags are set?	[PF AF IF]
movq \$0x500, %rax	# the value of %rax is:	0x500
movq \$0x500, %rcx	# the value of %rcx is:	0x500
# 0x500 - 0x500		
subq %rcx, %rax	# the value of %rax is:	0x0
	# what eflags are set?	[PF ZF IF]
movb \$0xff, %al	# the value of %rax is:	0xff
# 0xff +=1 (1 byte)		
incb %al	# the value of %rax is:	0x0 what eflags are set? [pf af zf if]
movb \$0xff, %al	# the value of %rax is:	0xff
# 0xff +=1 (4 bytes)		
incl %eax	# the value of %rax is:	0x100 what eflags are set? [PF AF IF]
movq \$-1, %rax	# the value of %rax is:	0xffffffffffff
# 0xff +=1 (8 bytes)		
incq %rax	# the value of %rax is:	0x0 what eflags are set? [PF AF ZF IF]
movq \$0x8877665544332211, %rax	# the value of %rax is:	0x8877665544332211
movq \$0x8877665544332211, %rcx	# the value of %rax is:	0x8877665544332211 what eflags are set? [PF AF ZF IF]
addq %rcx, %rax	# the value of %rax is:	0x10eccc88664422 what eflags are set? [CF PF IF OF]
movq \$0x8877665544332211, %rax	# the value of %rax is:	0x8877665544332211
andq \$0x1, %rax	# the value of %rax is:	0x1
movq \$0x8877665544332211, %rax	# the value of %rax is:	0x8877665544332211 explain why the values for AND/OR/XOR are
andq %rax, %rax	# the value of %rax is:	0x8877665544332211 what they are

[illegible]

```
.size    main, .-main
```

5. What did you observe happened to the condition code values as instructions that process within the ALU executed? What instructions caused changes? Were the changes what you expected? Why or why not?

The instructions did change whenever any line of code would execute all the MovXX commands changed the second operand after the call and cltq also changed the value if the most significant bit was 1 but did not change anything when it was 0. Push and Pop changed the values of many registers at the same time.

6. There were some instructions that caused bitwise AND/OR/XOR data manipulation. What did you observe?

The and or xor bitwise was working on the binary level which was nice to decode and understand.

7. There were some instructions that executed left or right bit shifting. What did you observe with respect to the register data? Did the size of the data being shifted change the result in the register? How?

Sometimes the shift to the right would depending on the call you make some functions would extend the msb and some would shift zeros instead and some would keep all the ones on the left and shift 0 after all the ones to the right. Shift to the left was not as fun but it had the same idea; the msb would get dumped unless commanded not to.

8. What did you observe happening to the value in register %rip over the course the program? Did it always change by the same amount as each instruction executed?

It changed depending on how much info was pushed in and it would equally change back to what it was before the push and therefore after the pop instruction would be called, the value it would change by is the same in reverse order.

9. What did you observe when you took the comments away from the two different instruction sets and tried to reassemble the program? There were questions in item L and M in the Lab 5 Description; include your answers to those questions here.

The code did bug out but I did not understand why because the instructions that were called made sense to me but maybe the fact that the registers were of different sizes caused the code to bug.

10. Any other comments about what you observed?

Fun project I loved going in depth about the CPU and registers values and how things get actually processed on a registers level.