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2  -- Name: Capt Jeff Falkinburg
3  -- Date: Spring 2016
4  -- Course: ECE 281
5  -- File: Lsn20_andTwo.vhd
6  -- HW:   Lecture 20
7  -- Purp: Structural implementation of a 2 input AND Gate
8  -- Doc:  None
9  -- Academic Integrity Statement: I certify that, while others may have
10 -- assisted me in brain storming, debugging and validating this program,
11 -- the program itself is my own work. I understand that submitting code
12 -- which is the work of other individuals is a violation of the honor
13 -- code. I also understand that if I knowingly give my original work to
14 -- another individual is also a violation of the honor code.
15 -----
16 library IEEE;
17 use IEEE.STD_LOGIC_1164.ALL;
18
19 entity andTwo is
20     Port ( A : in  STD_LOGIC;
21           B : in  STD_LOGIC;
22           Y : out STD_LOGIC);
23 end andTwo;
24
25 architecture Behavioral of andTwo is
26
27 begin
28
29     Y <= A and B;
30
31 end Behavioral;
32
33
```