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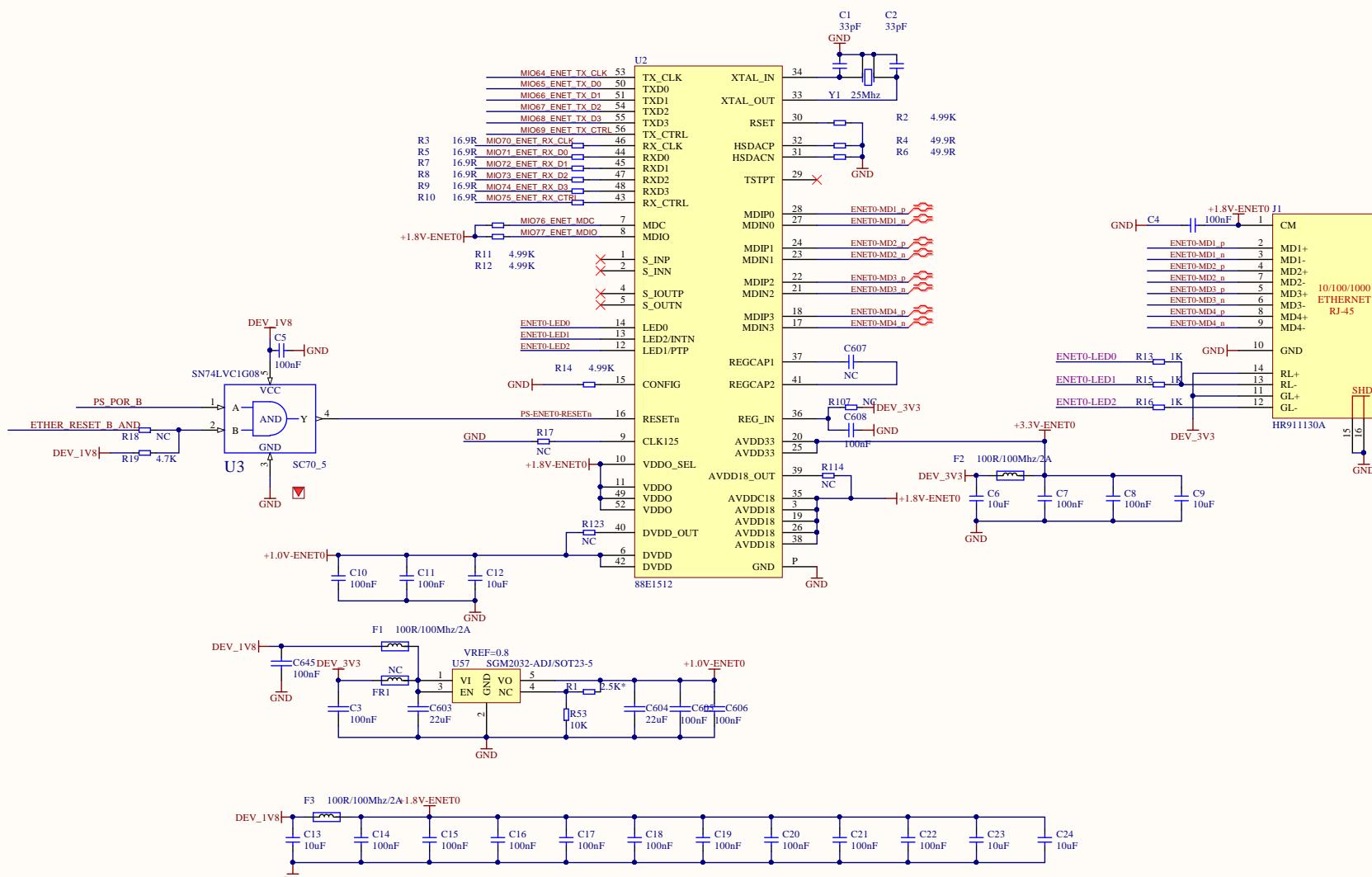
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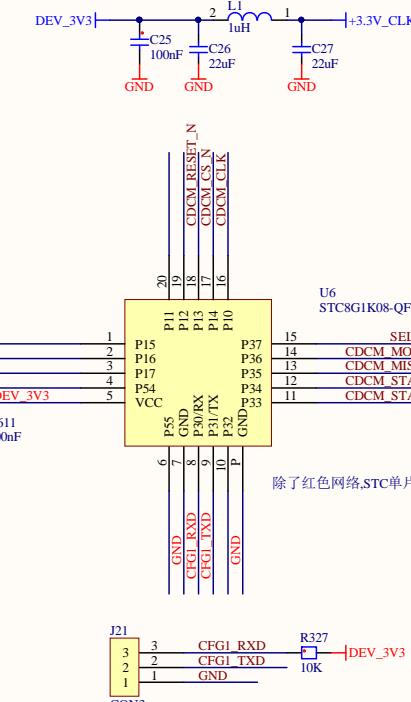
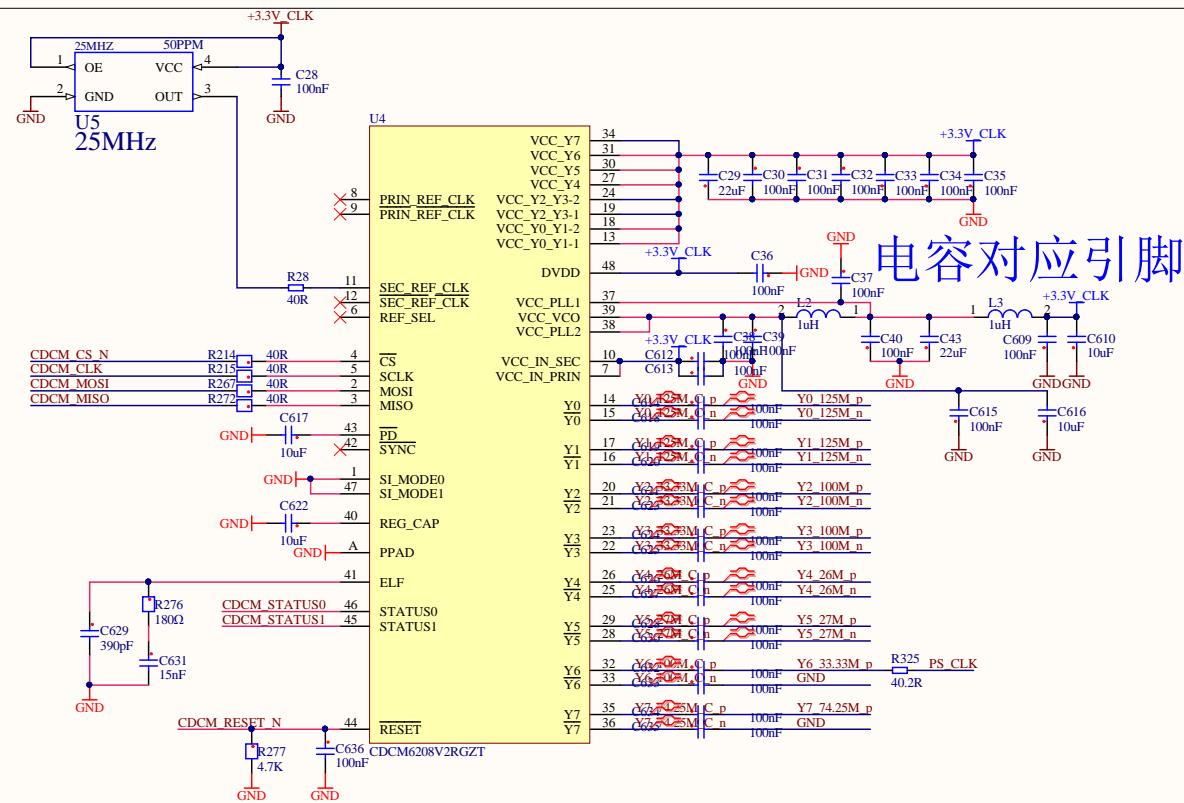
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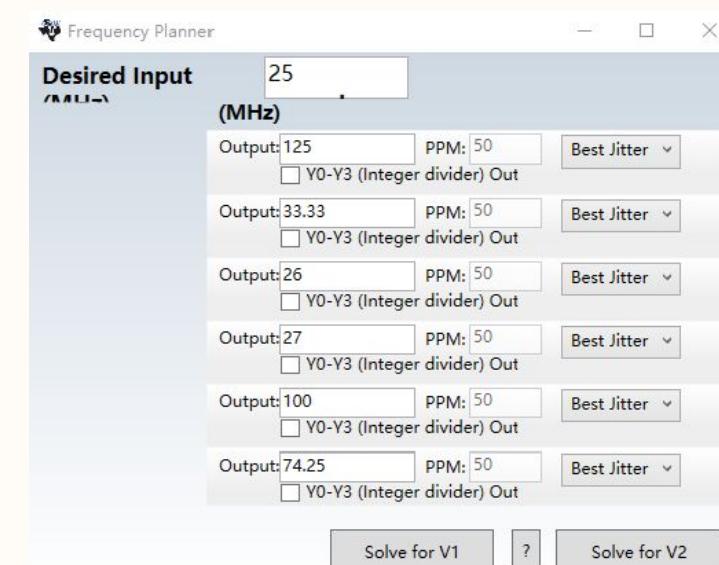
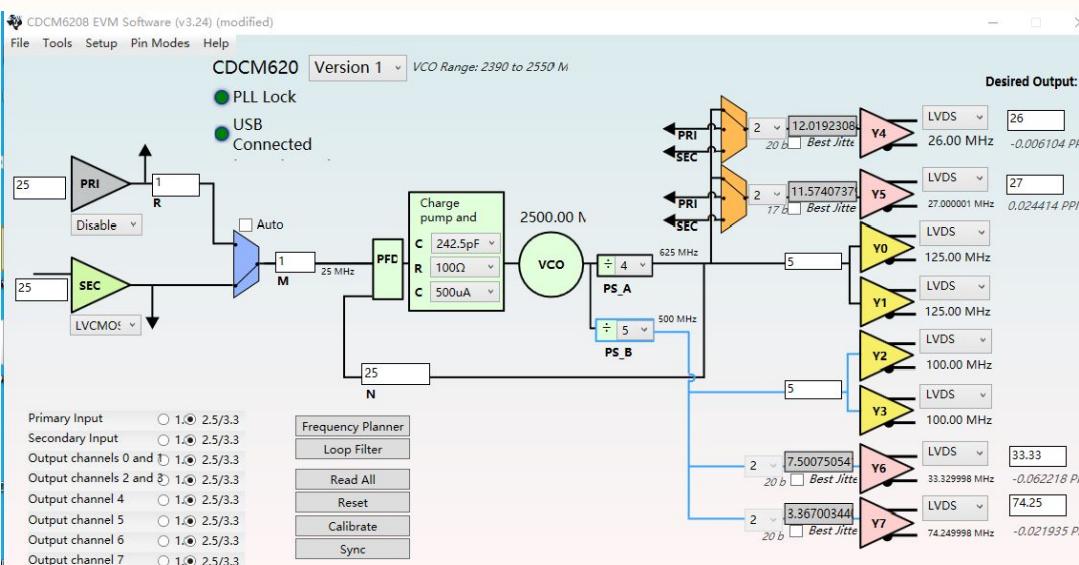


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除了红色网络,STC单片机引脚互换可以

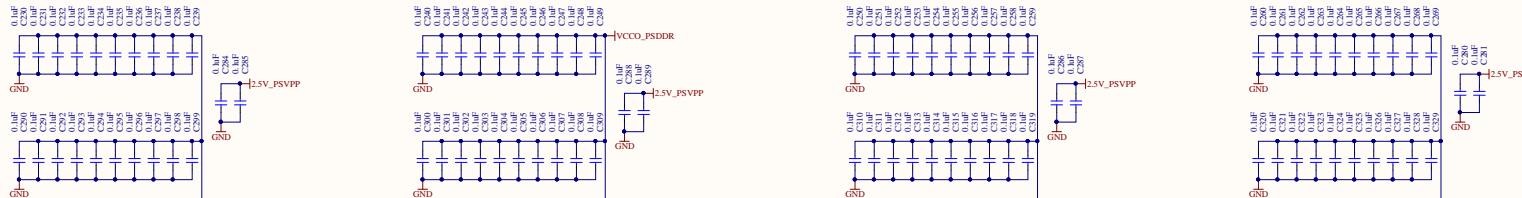
电容对应引脚



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Register 1: 0000
Register 2: 0018
Register 3: 00F4
Register 4: 30AB
Register 5: 0023
Register 6: 0004
Register 7: 0023
Register 8: 0004
Register 9: 0023
Register 10: 00B0
Register 11: 4ECS
Register 12: 0203
Register 13: 00A9
Register 14: 2F68
Register 15: 0203
Register 16: 0068
Register 17: 0313
Register 18: 0203
Register 19: 0025
Register 20: DF3F
Register 21: 0000
Register 40: 0000
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Secondary Input Frequency: 25
Version 1
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C2: 0
C3: 0
R3: 0
C3: 0
Charge Pump: 0

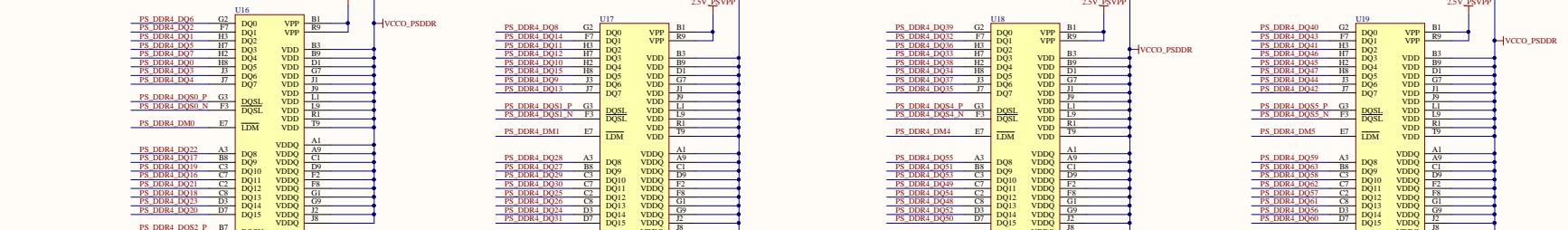
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A



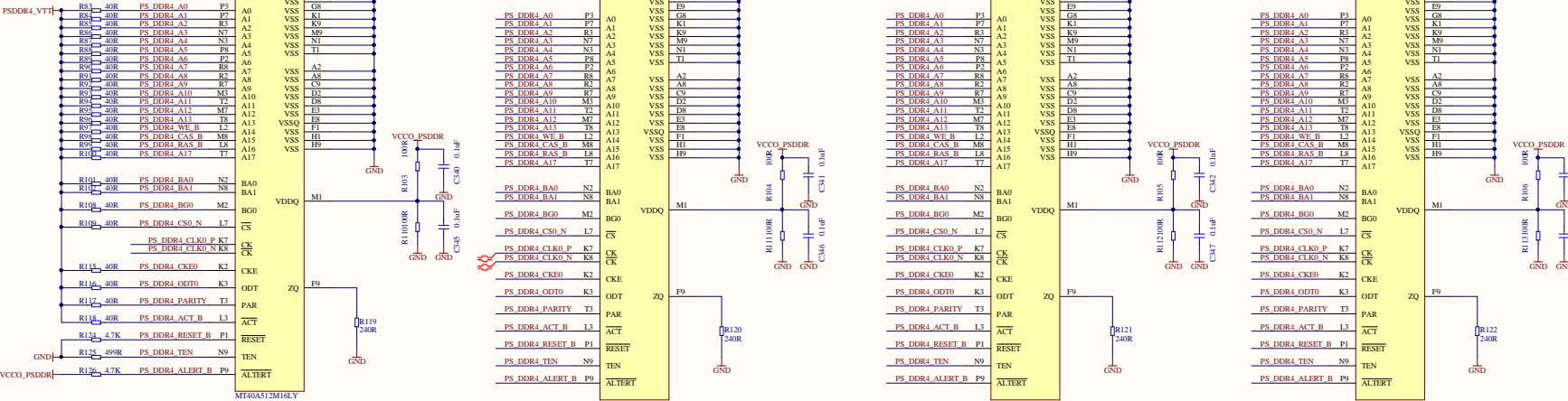
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B



C

C



D

D



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A

A

B

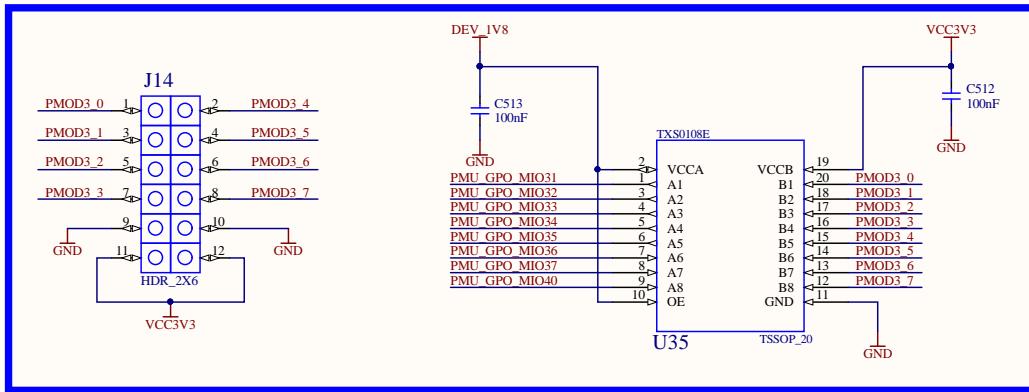
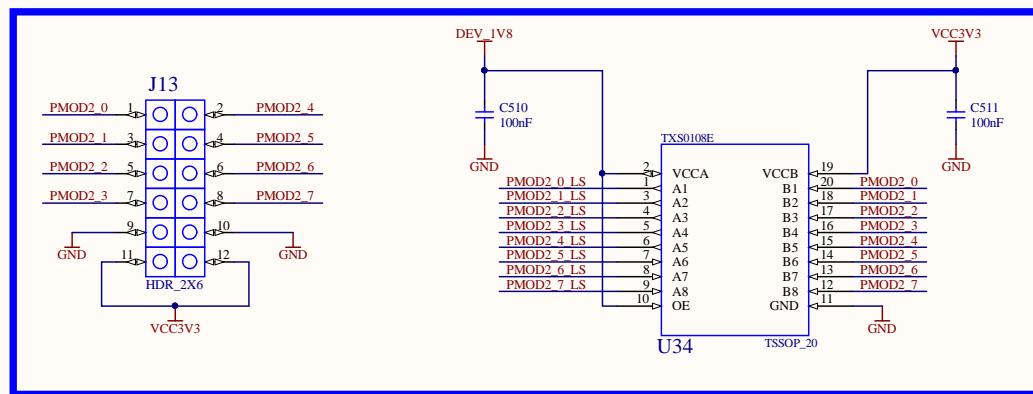
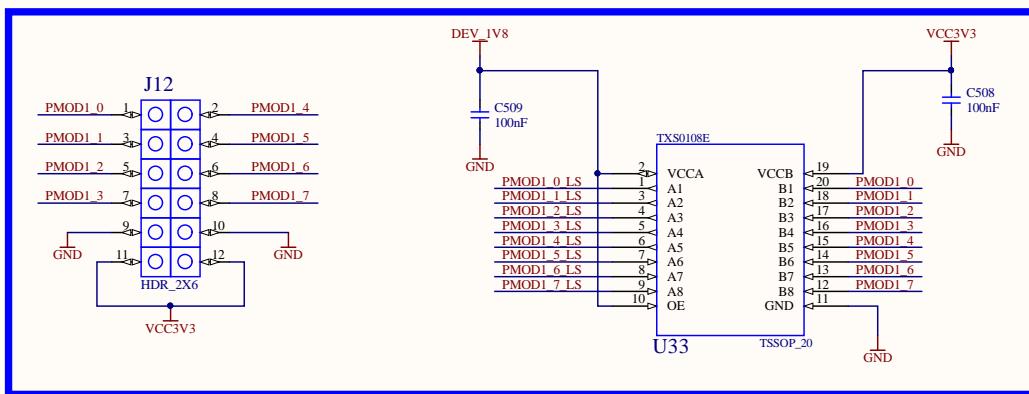
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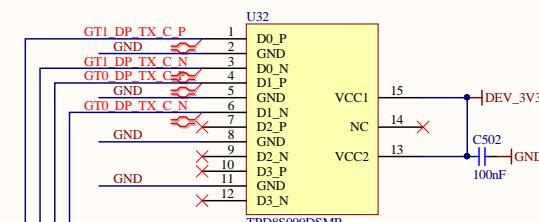


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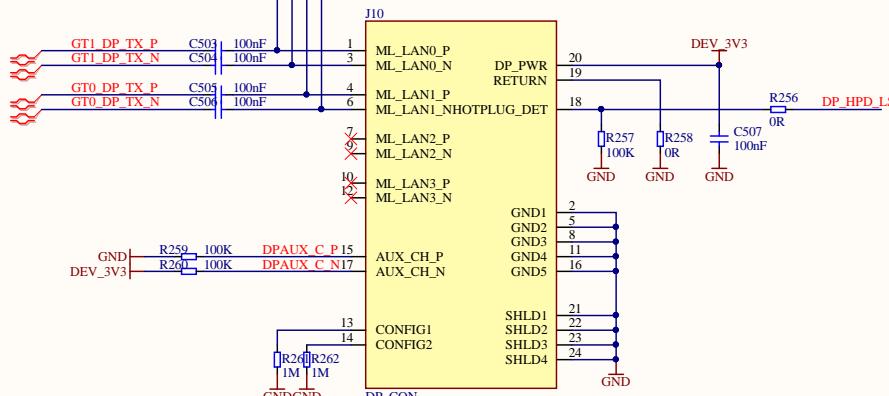
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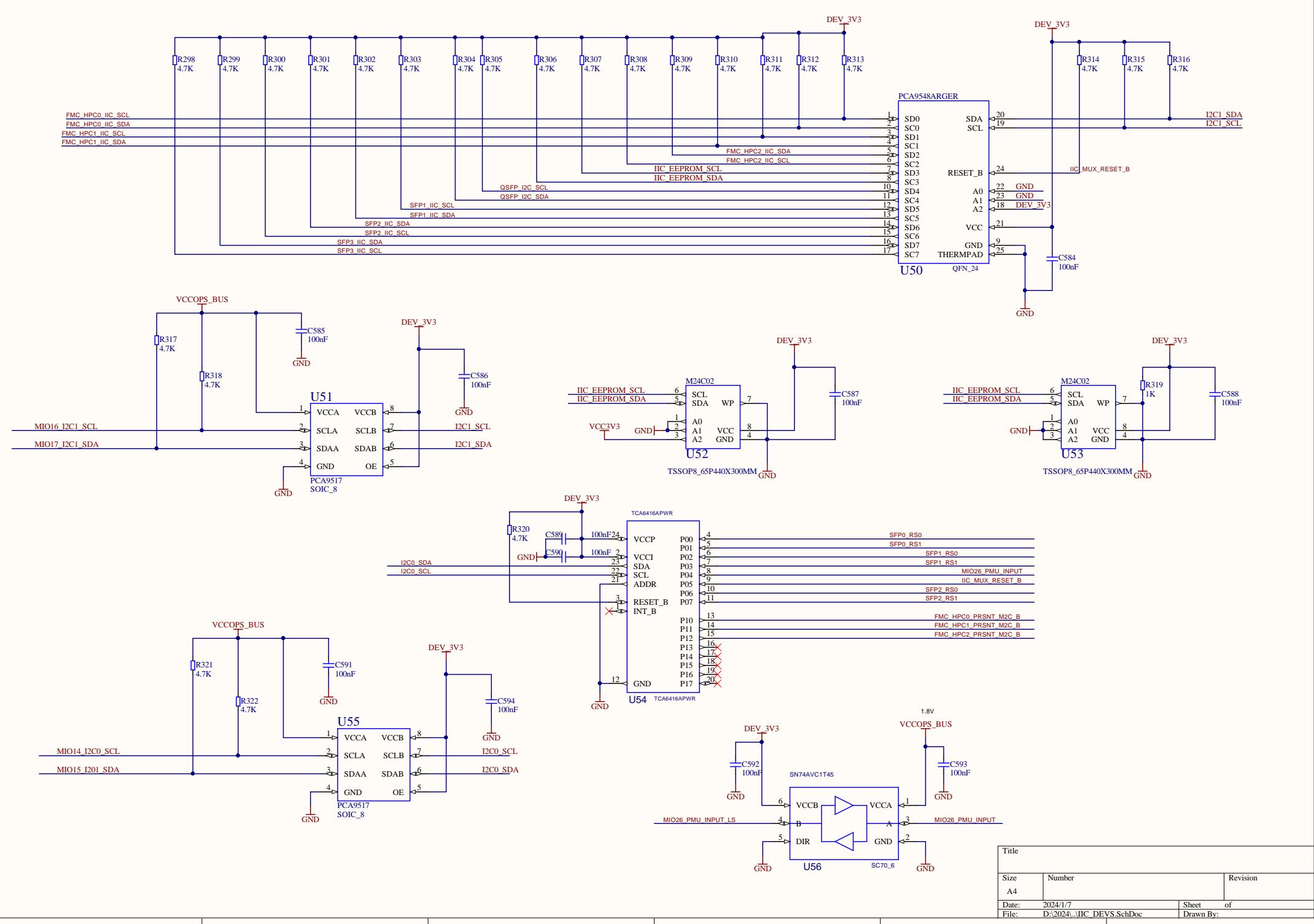
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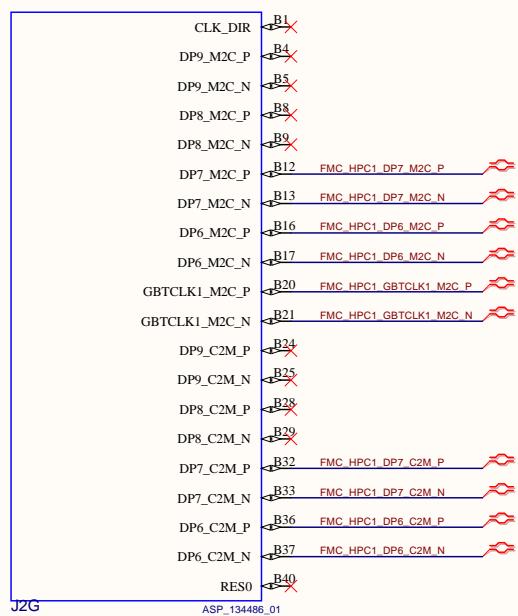
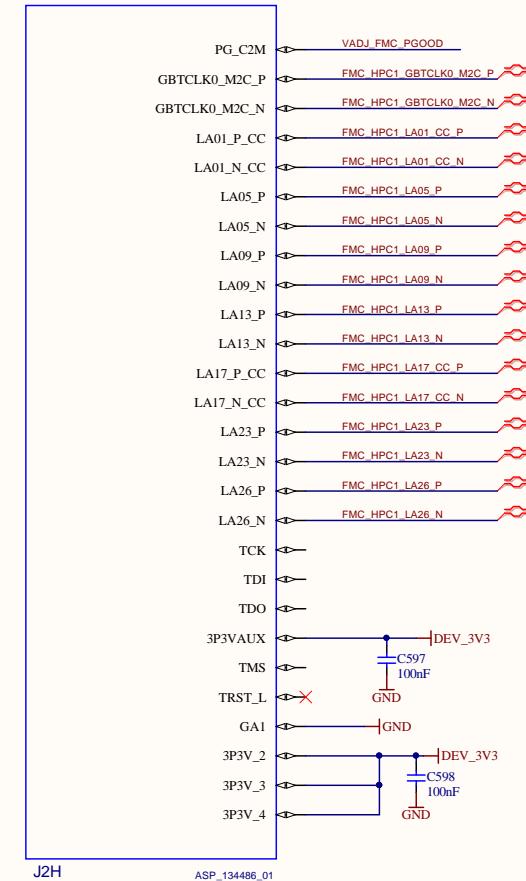
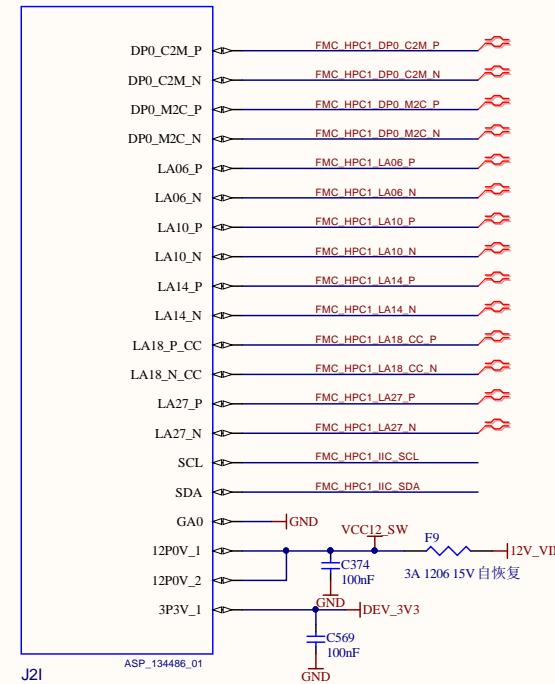
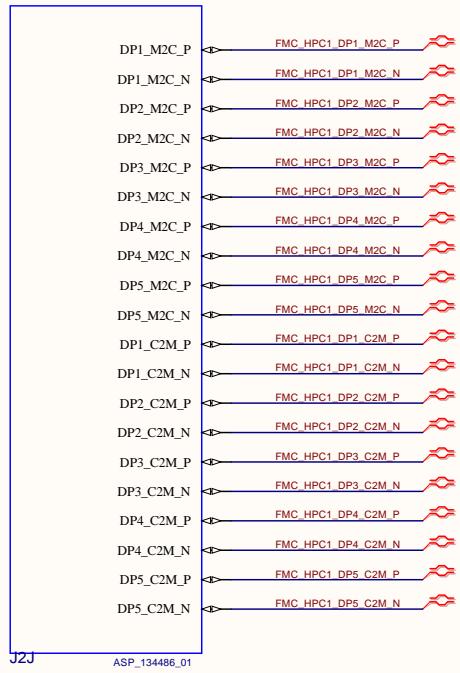


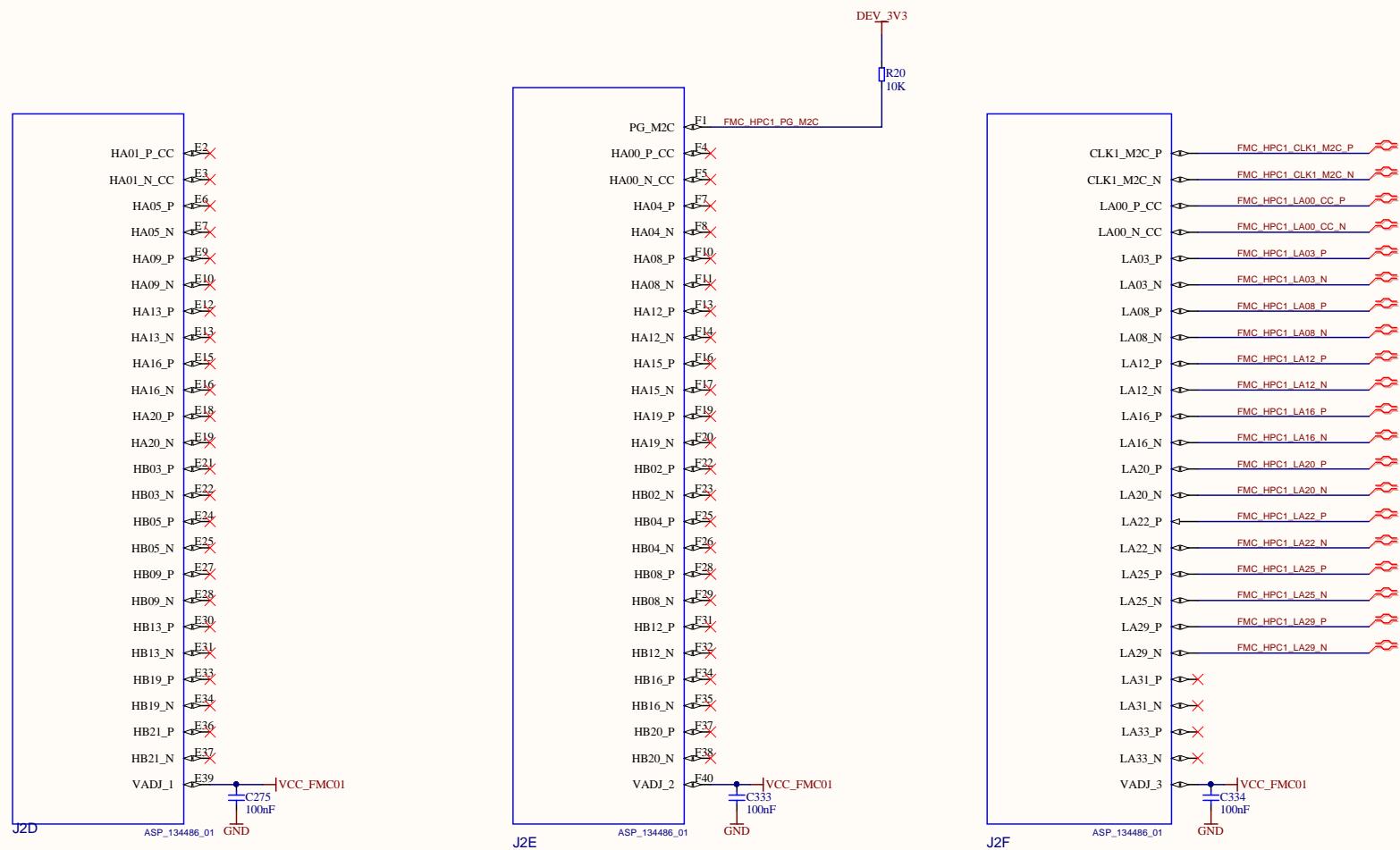
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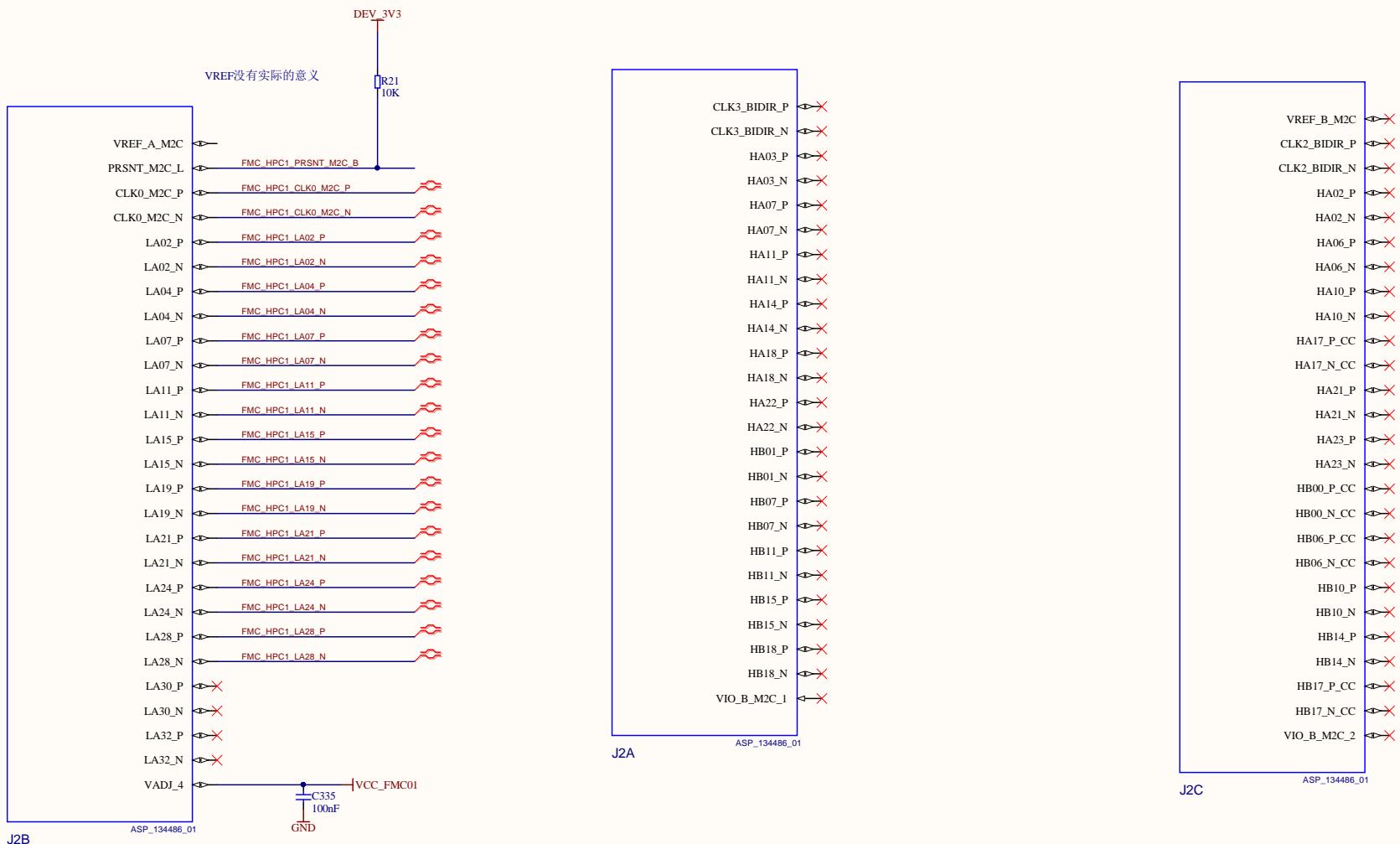
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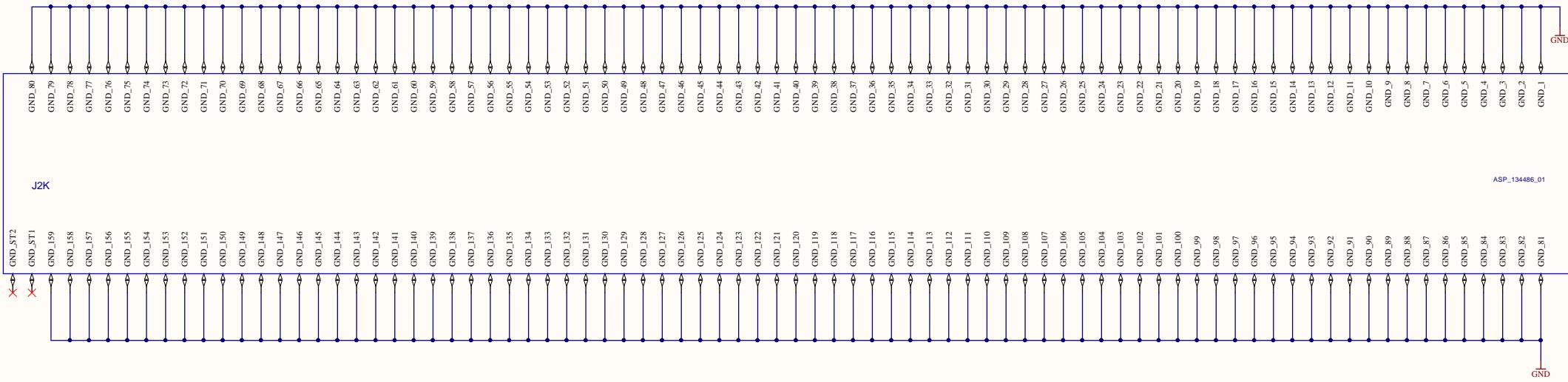




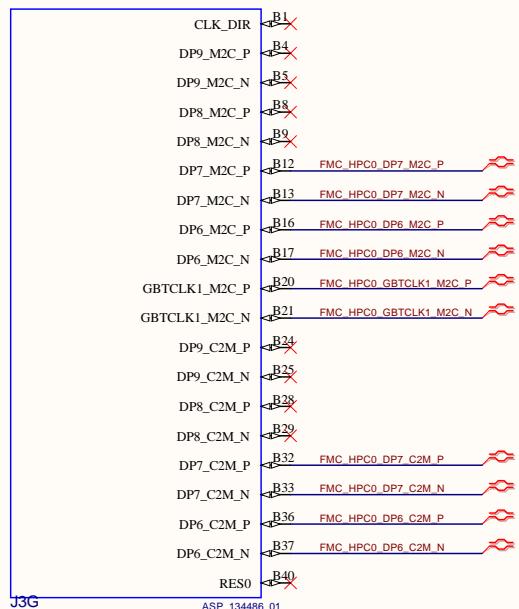
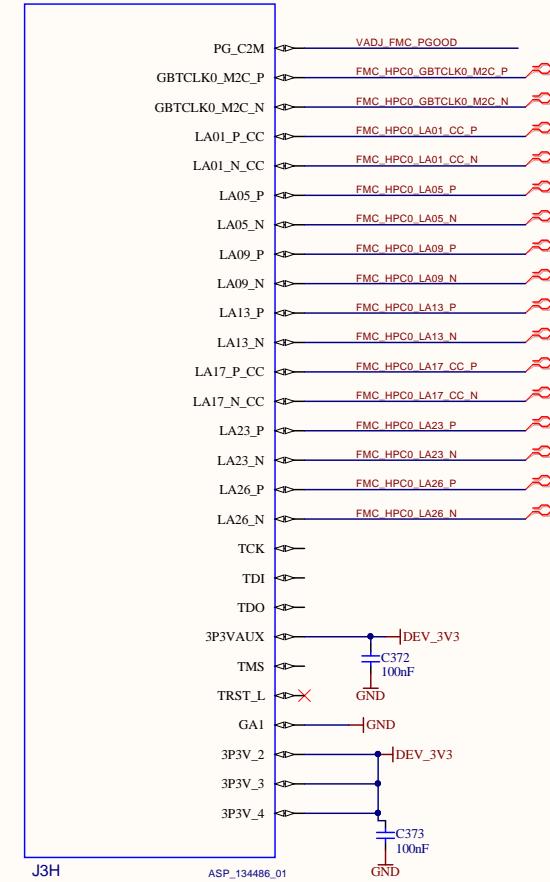
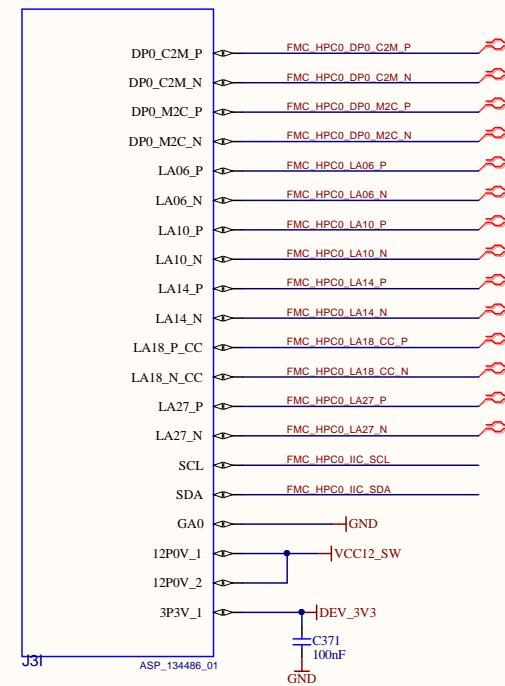
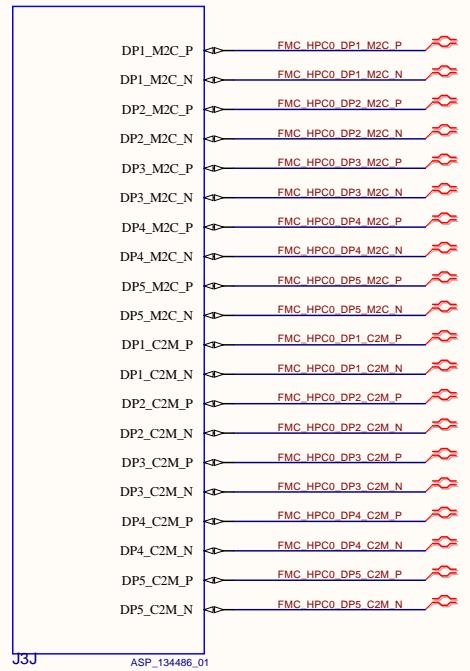
ANSI/VITA 57.1 - Revised 2010
 PL FMC HPC1 Header Rows E F G



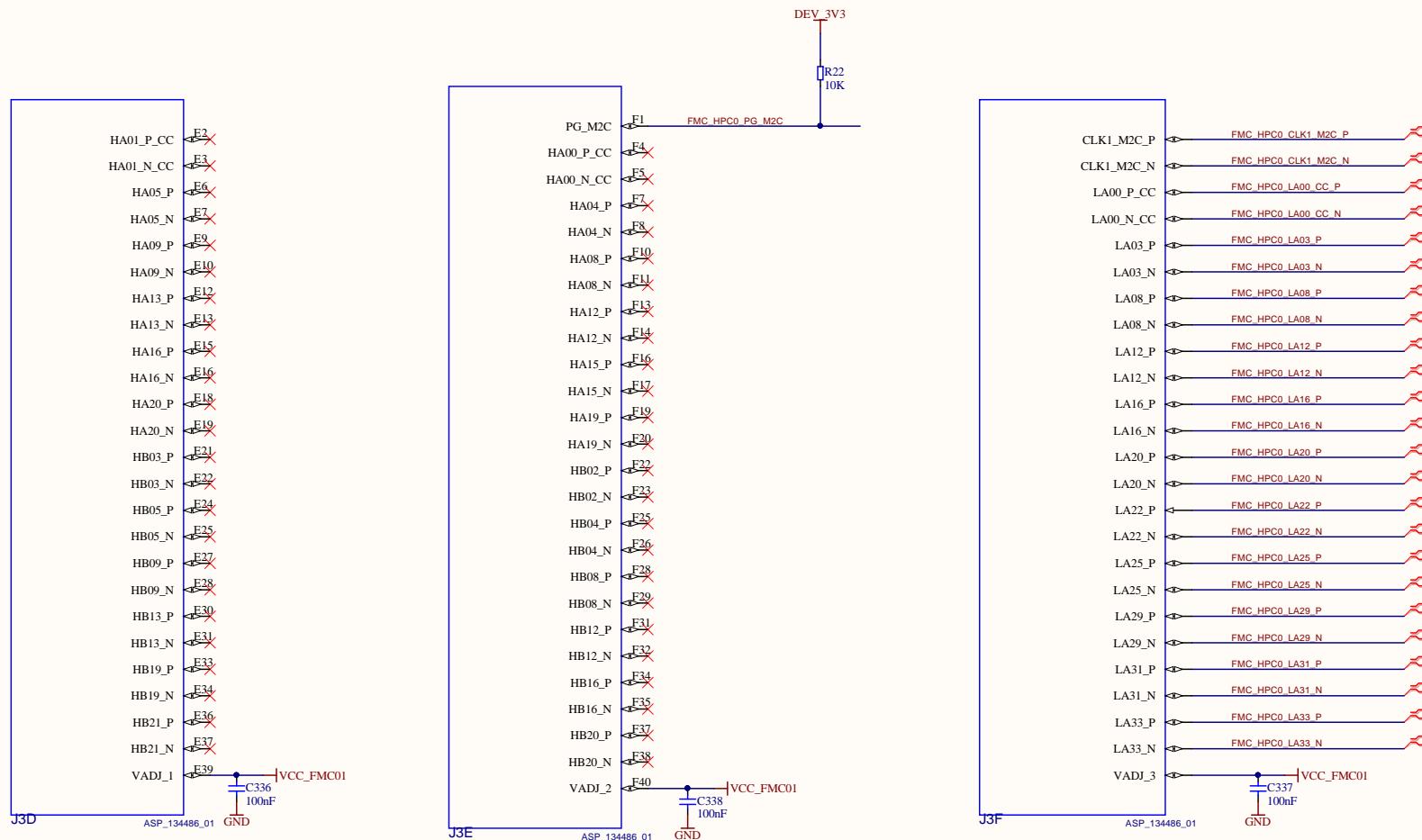
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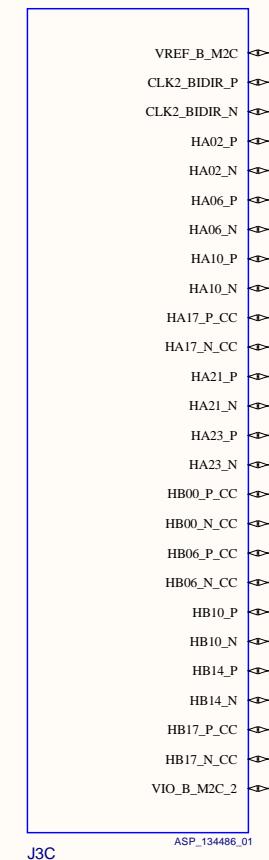
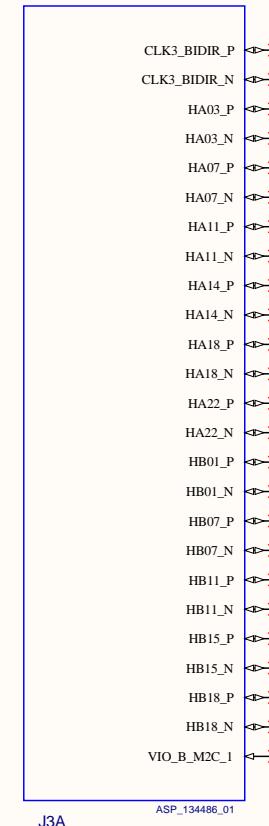
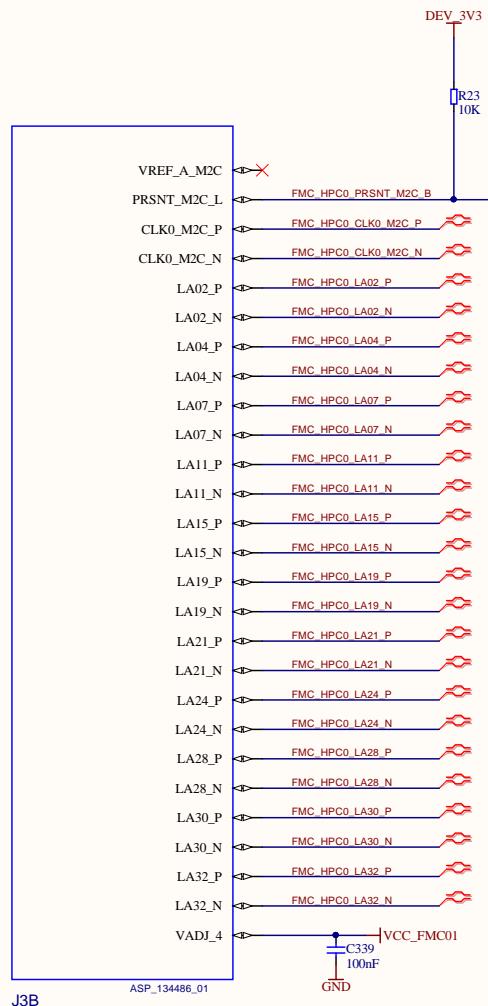
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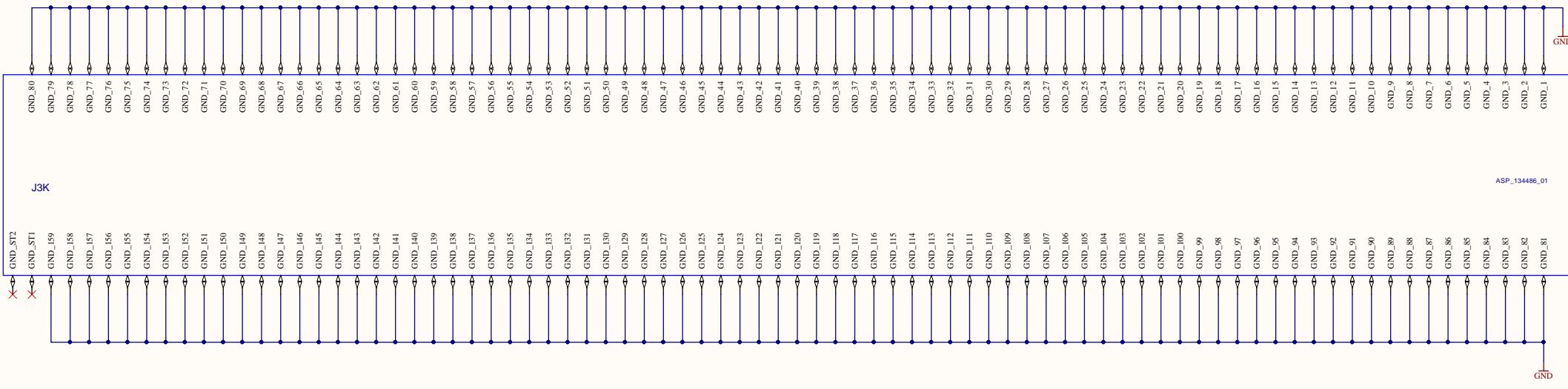


FMC_HPC0_IIC_SCL
FMC_HPC0_IIC_SDA



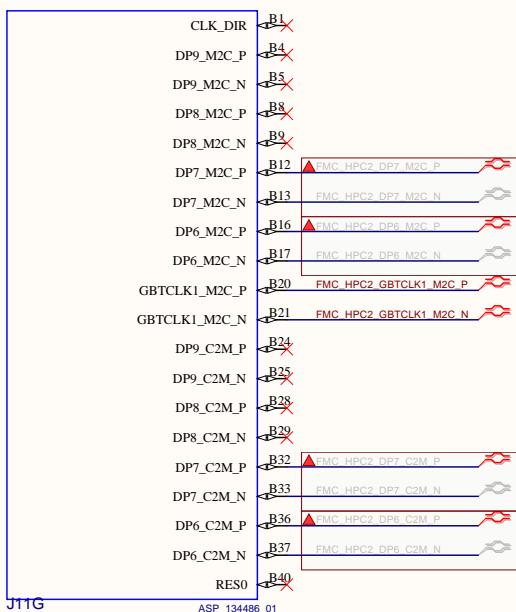
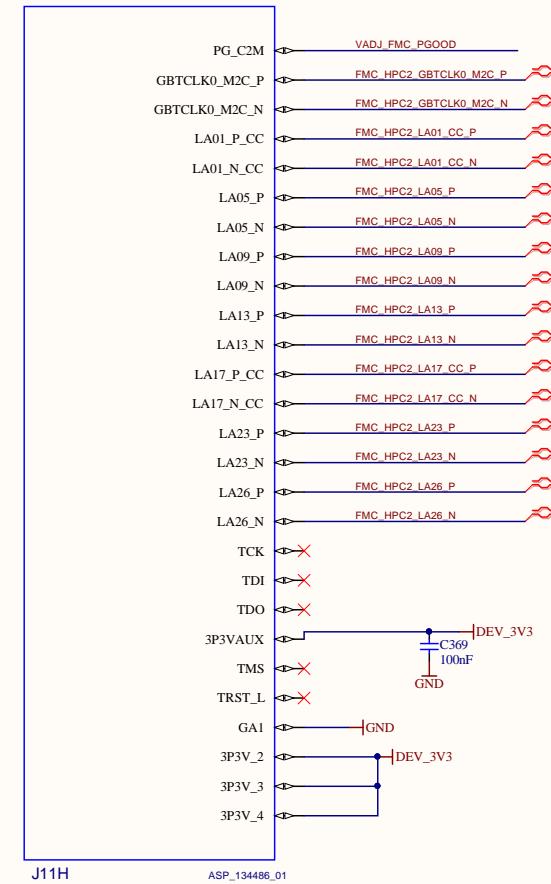
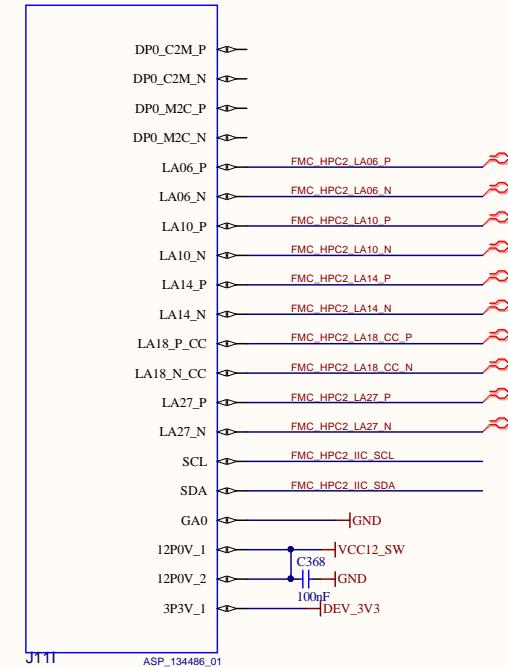
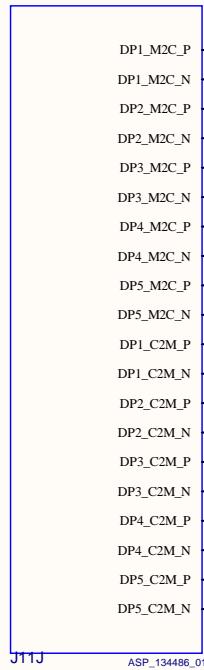
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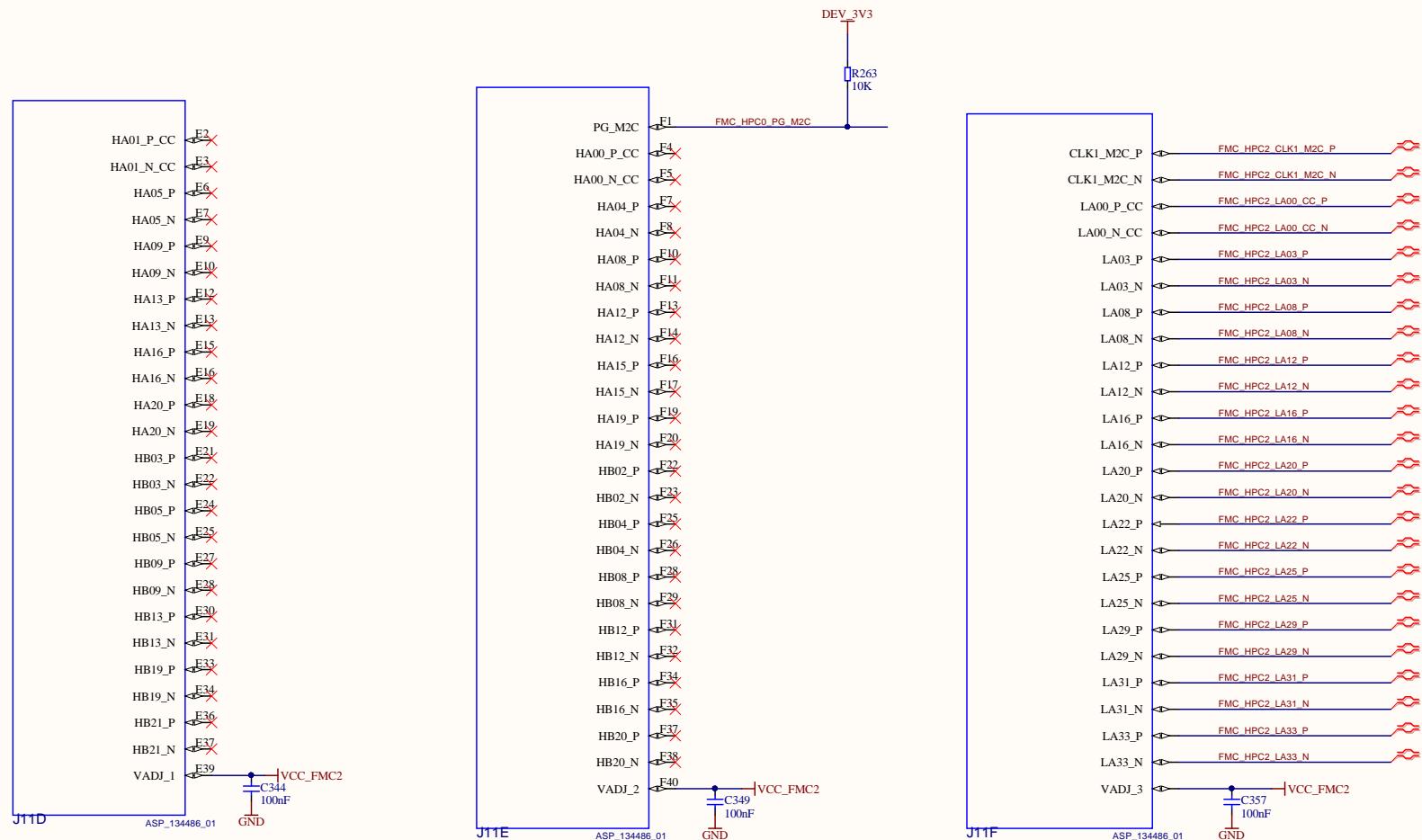




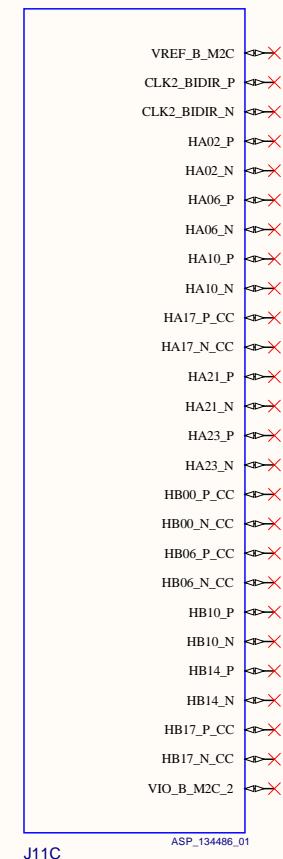
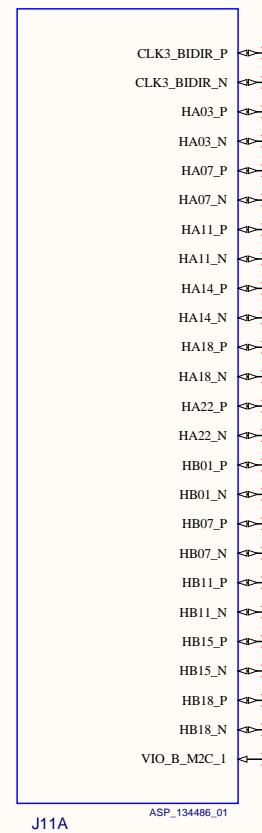
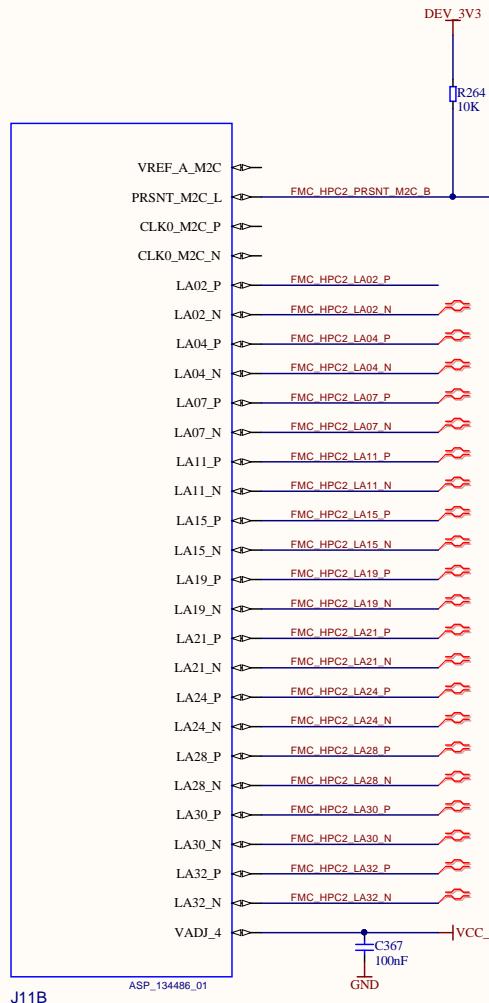
ANSI/VITA 57.1 - Revised 2010
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所有的HPC0都改成HPC2

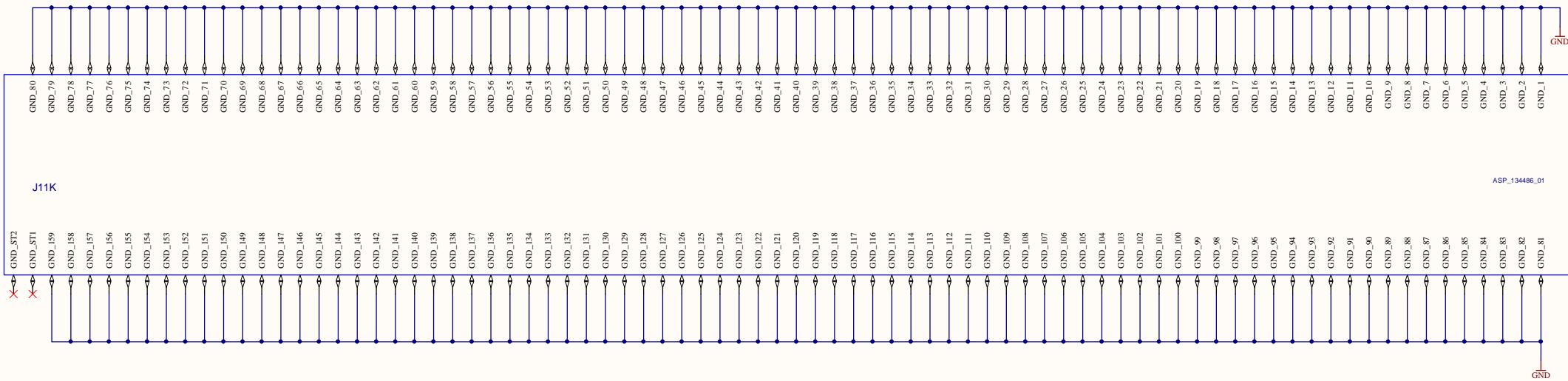




ANSI/VITA 57.1 - Revised 2010
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ANSI/VITA 57.1 - Revised 2010
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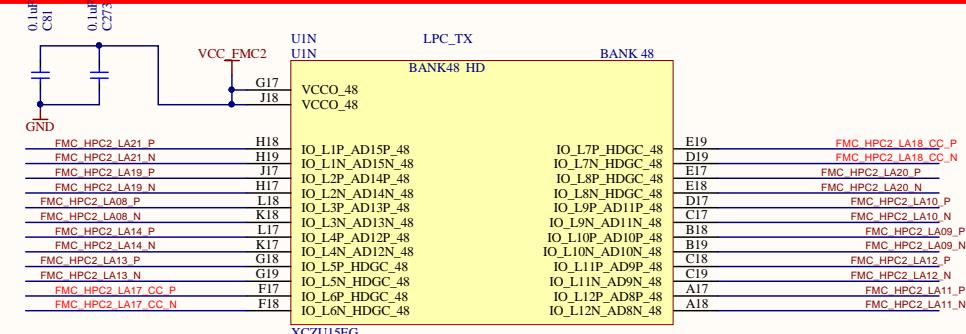


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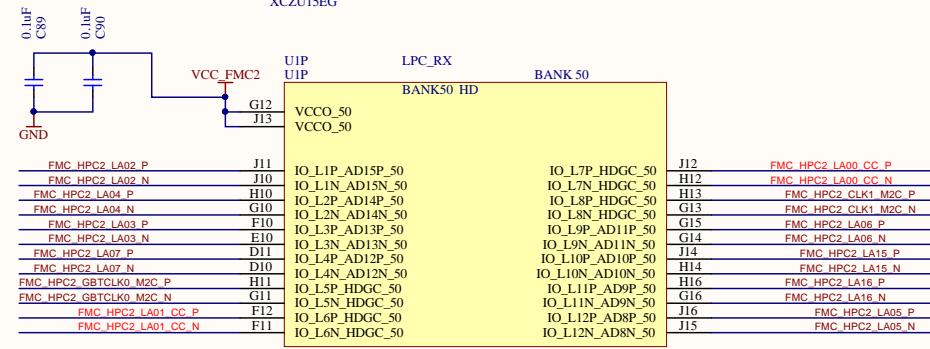
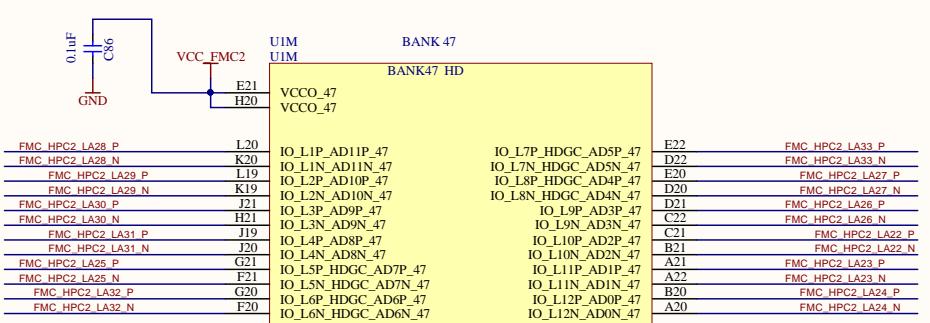


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GPIO_LED1	API4	IO_L1N_AD11N_44		AH13	USB_UART_PL2PC_1_LS
GPIO_LED2	AM14	IO_L2P_AD10P_44		AJ15	EMMC_D0
GPIO_LED3	AN13	IO_L2N_AD10N_44		AJ14	EMMC_D1
GPIO_DIP_SW3	AN12	IO_L3P_AD9P_44		AE13	EMMC_D2
GPIO_DIP_SW2	API2	IO_L3N_AD9N_44		AF13	EMMC_D3
GPIO_DIP_SW1	AL13	IO_L4P_AD8P_44		AG14	EMMC_D4
GPIO_DIP_SW0	AM13	IO_L4N_AD8N_44		AG13	EMMC_D5
GPIO_SW0	AK15	IO_L5P_HDGC_AD7P_44		AF15	EMMC_D6
GPIO_SW1	AK14	IO_L5N_HDGC_AD7N_44		AG15	EMMC_D7
Y7_74.25M_p	AK13	IO_L6P_HDGC_AD6P_44		AE15	EMMC_CMD
EMMC_RST_B	AL12	IO_L6N_HDGC_AD6N_44		AE14	EMMC_CLK



		U10	U10	BANK 49	
		VCCO_49	VCCO_49	C14	ETHER_RESET_B_AND
				B14	USB_RESET_B_AND
PMOD1_0_LS	F16	IO_L1P_HDGC_49		C13	QSFP_RSTN
PMOD1_1_LS	F15	IO_L1N_HDGC_49		B13	QSFP_INTn
PMOD1_2_LS	D16	IO_L2P_HDGC_49		A13	QSFP_MODE_PRSn
PMOD1_3_LS	C16	IO_L2N_HDGC_49		A12	USB_UART_PC2PL_0_LS
PMOD1_4_LS	B16	IO_L3P_HDGC_49		C12	USB_UART_PL2PC_0_LS
PMOD1_5_LS	A16	IO_L3N_HDGC_49		E12	PMOD2_4_LS
PMOD1_6_LS	B15	IO_L4P_HDGC_49		D12	PMOD2_5_LS
PMOD1_7_LS	A15	IO_L4N_HDGC_49		F13	PMOD2_6_LS
PMOD2_0_LS	E15	IO_L5P_HDGC_49		E13	PMOD2_7_LS
PMOD2_1_LS	D15	IO_L5N_HDGC_49			
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PMOD2_3_LS	D14	IO_L6N_HDGC_49			

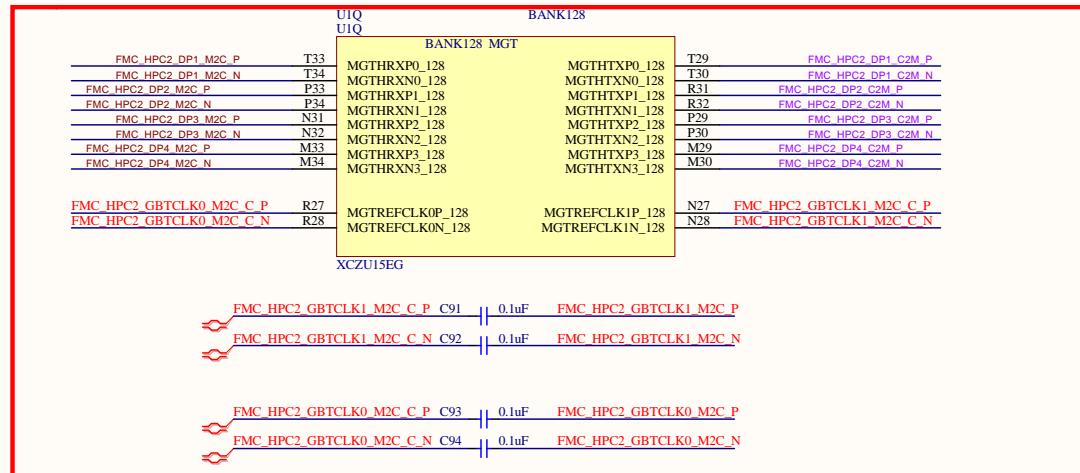
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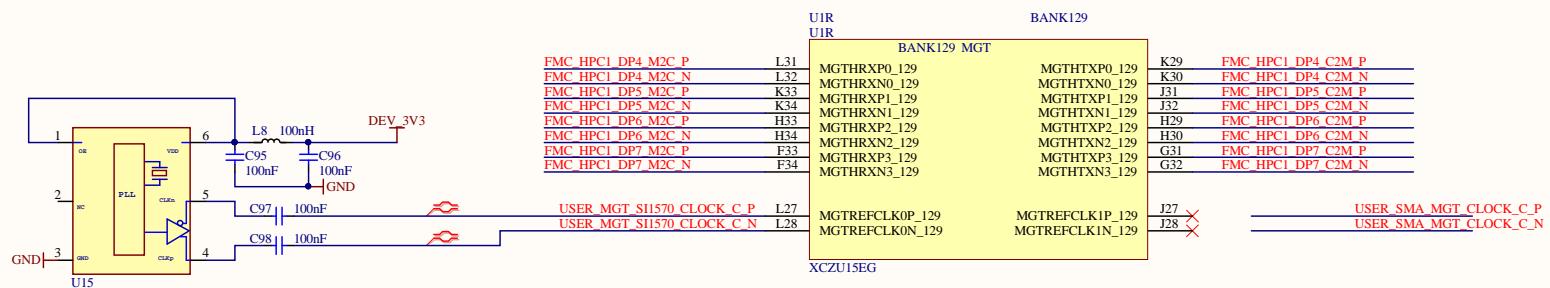
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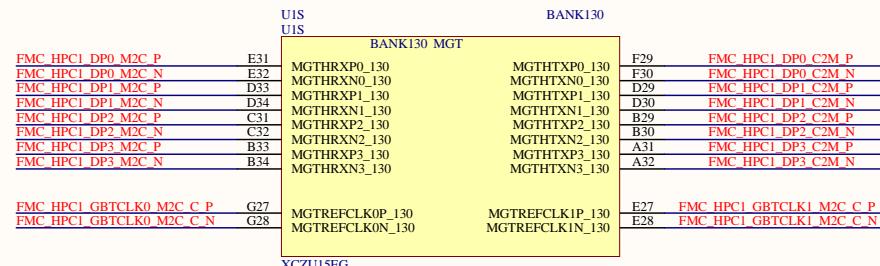
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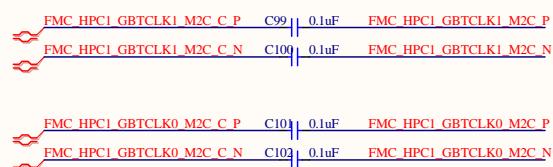
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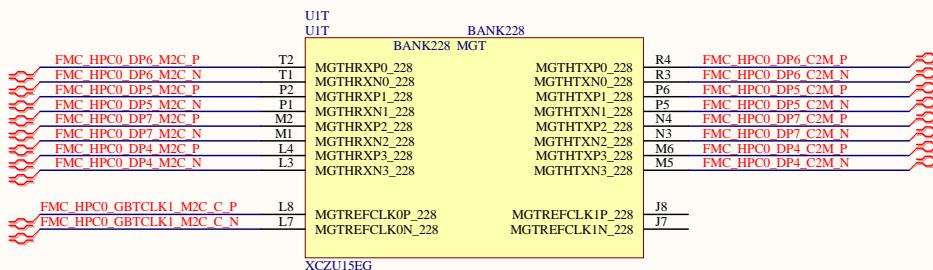
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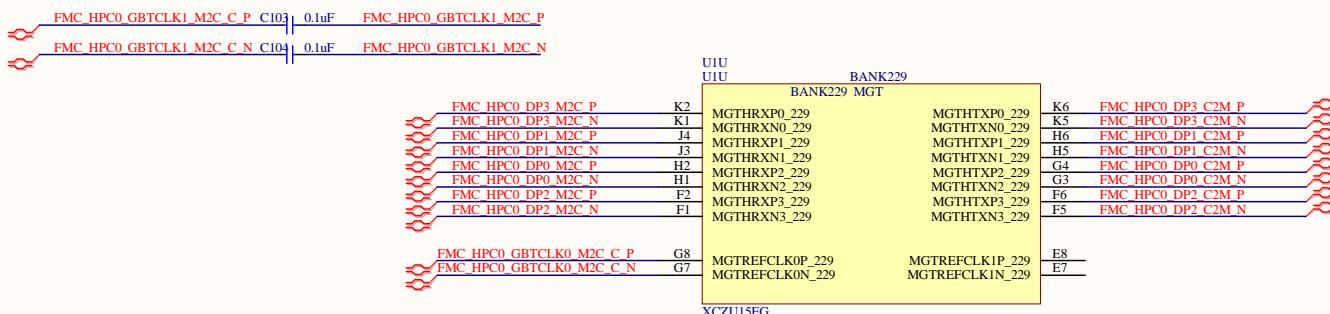
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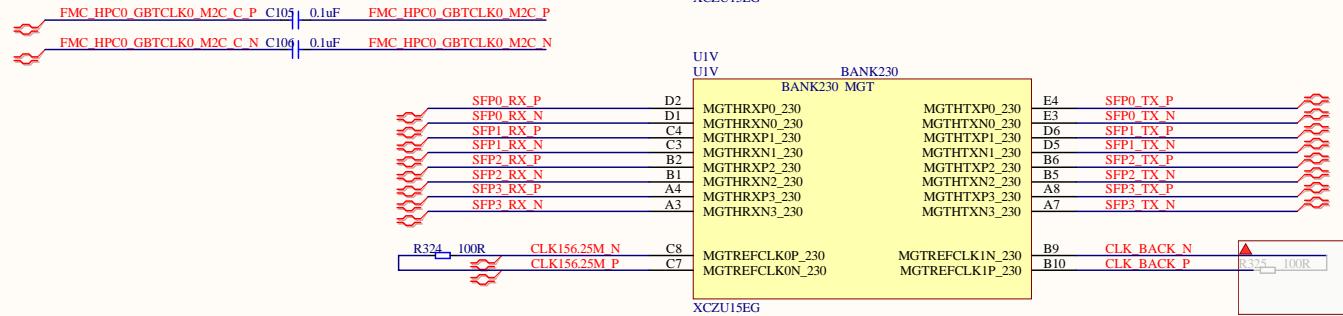
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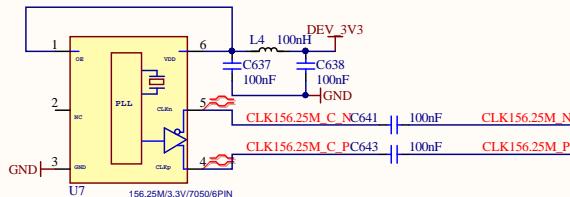
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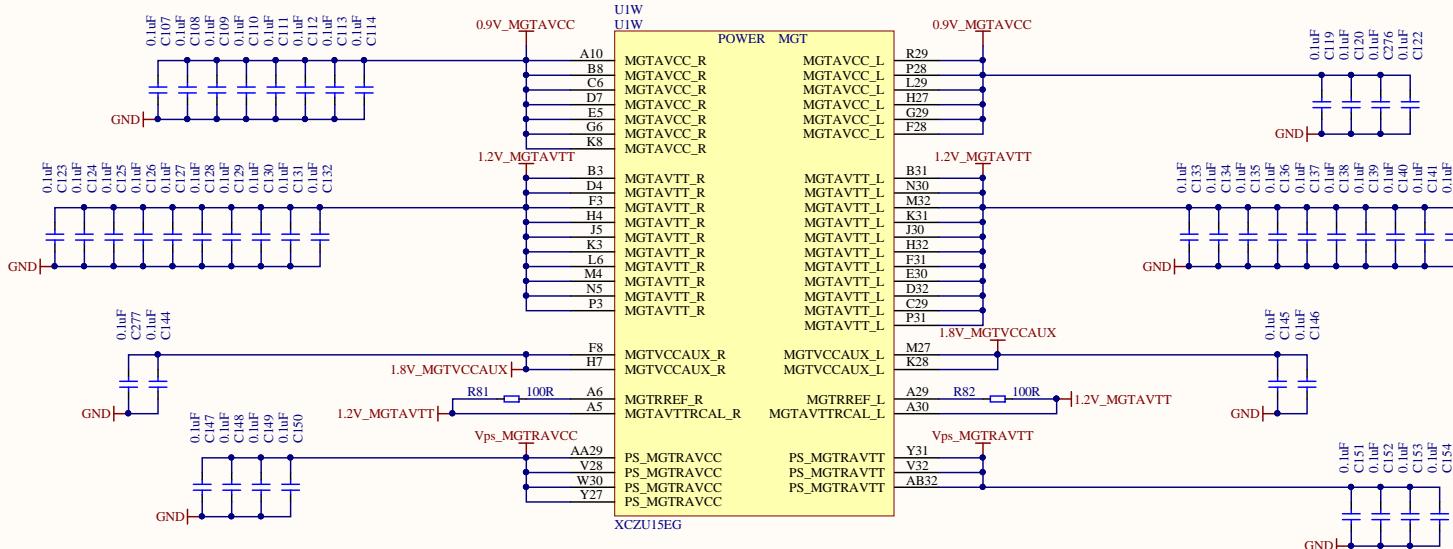
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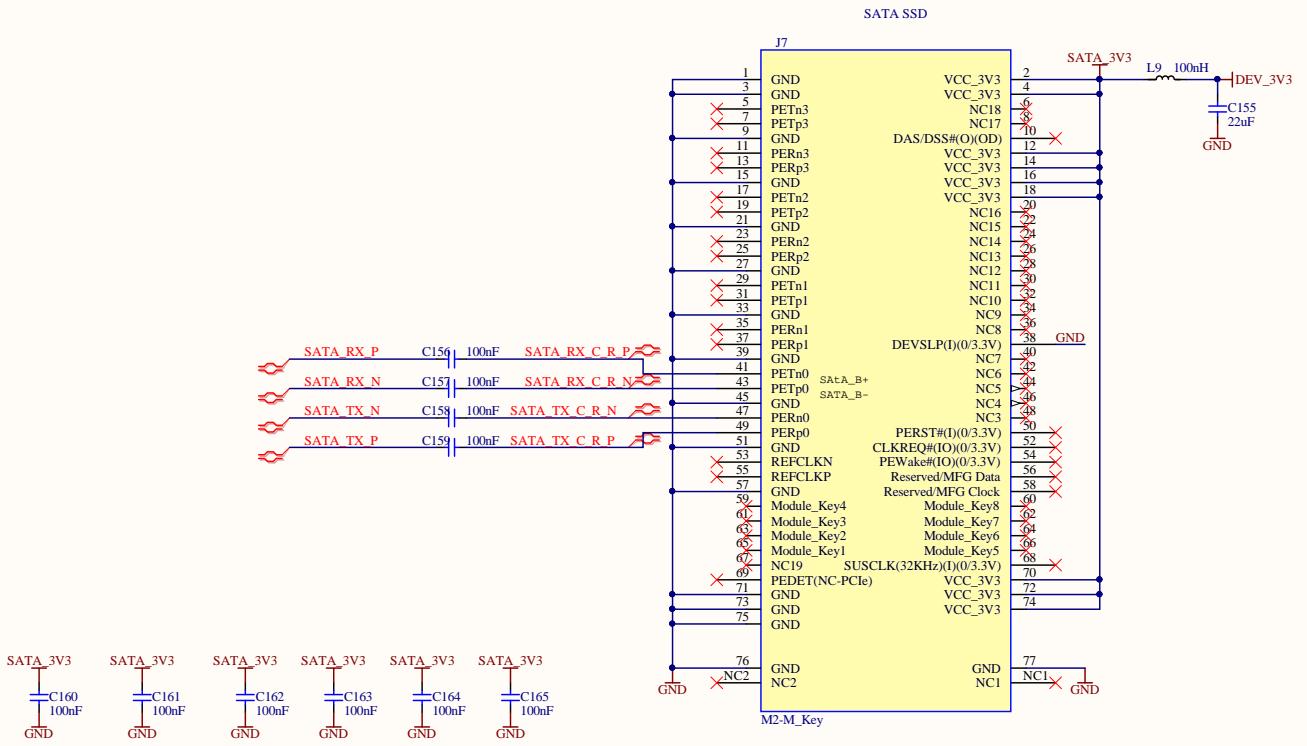
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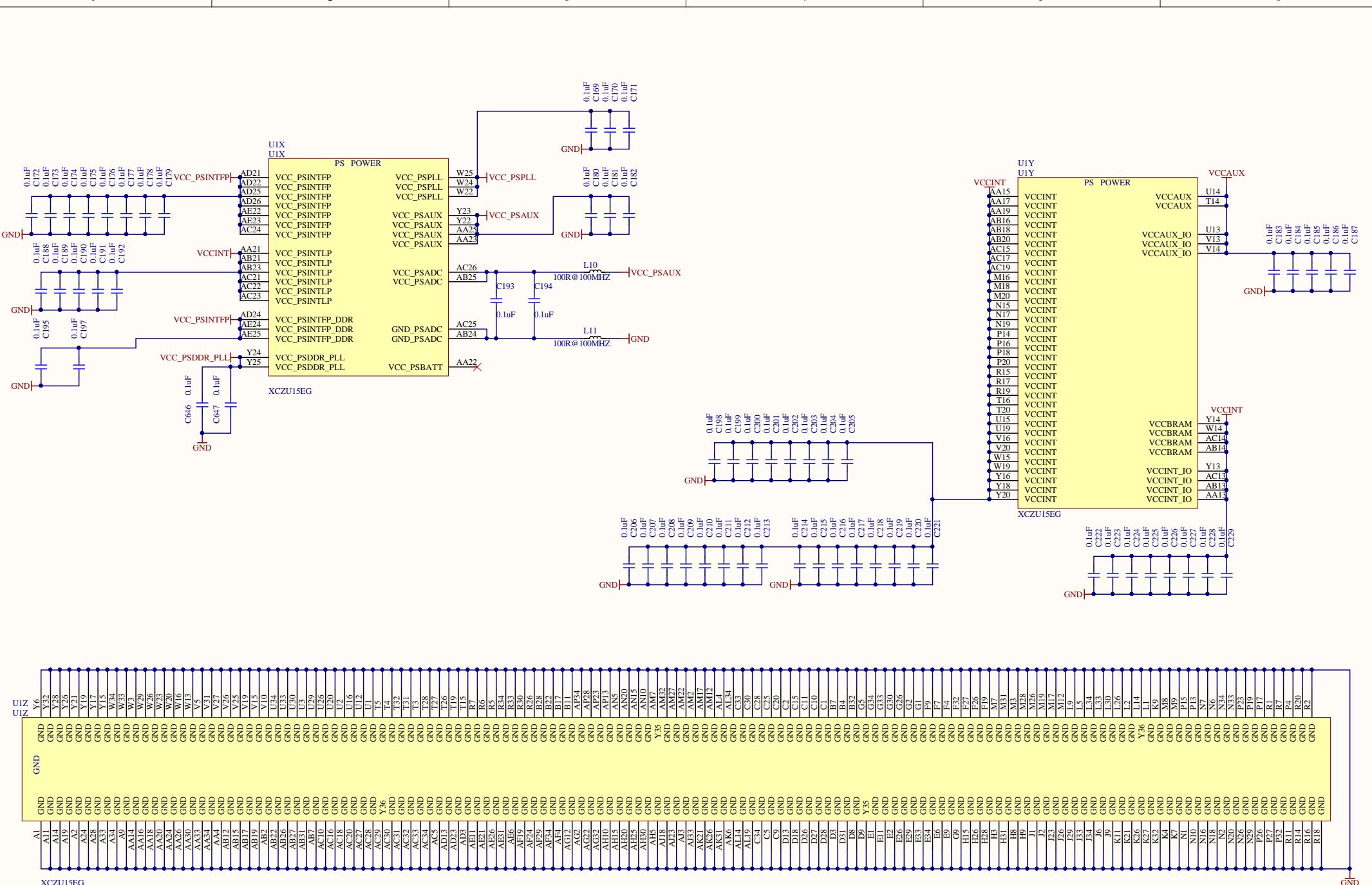


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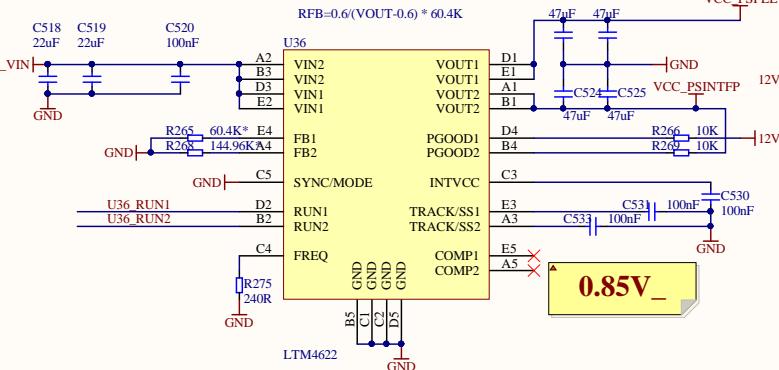
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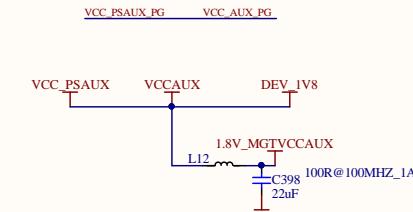
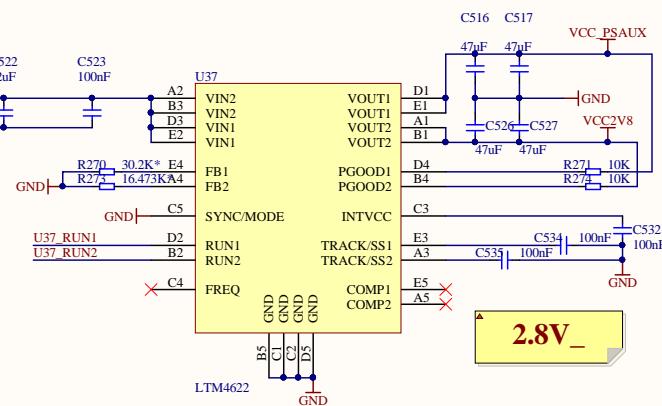


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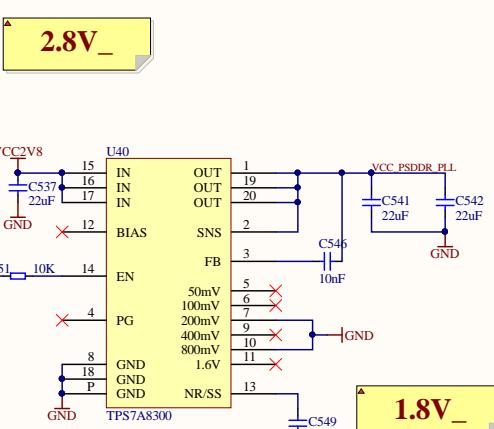
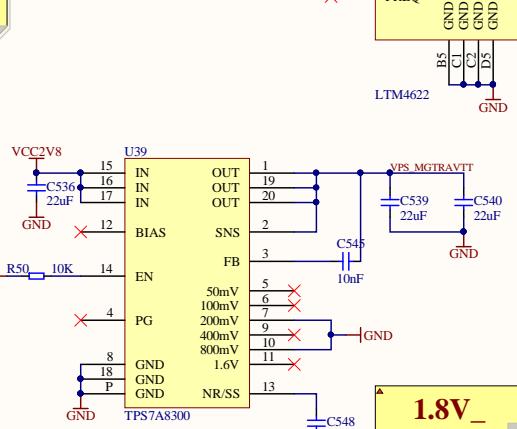
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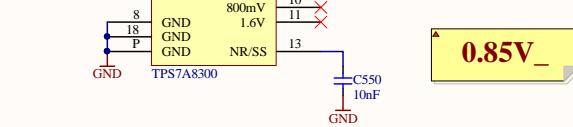
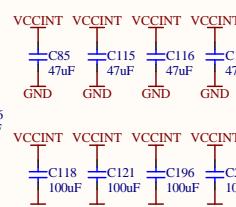
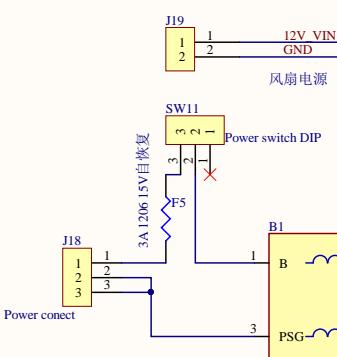
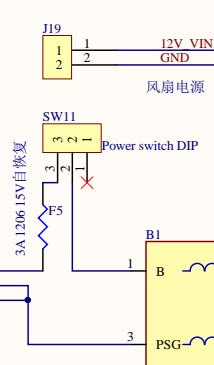
$$FBR = 36.24 / (V_{out} - 0.6) \text{ in K}$$

1.8V_

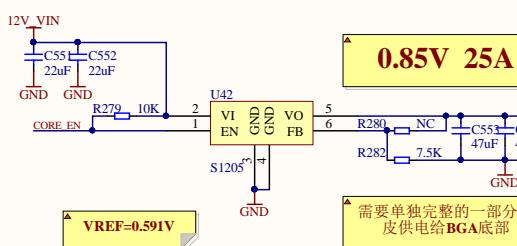
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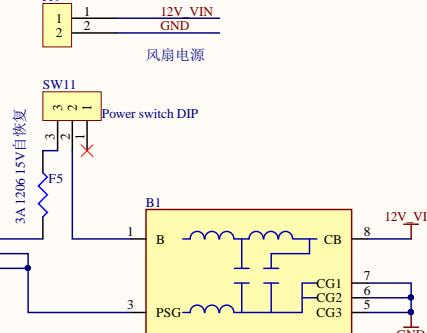
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**0.85V_****0.85V 25A****1.8V_**

D

**0.85V_**

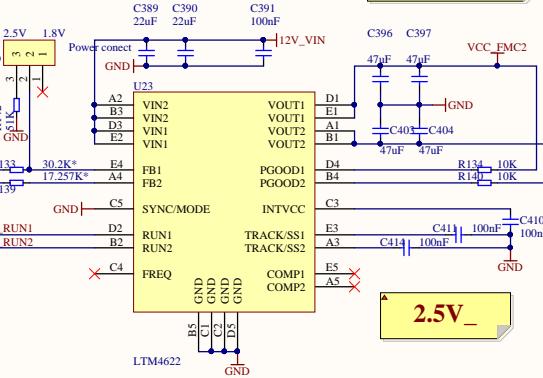
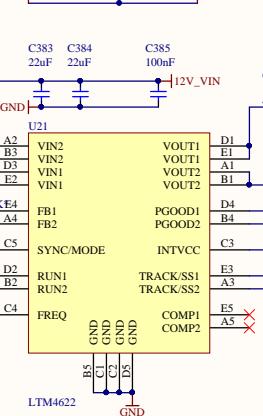
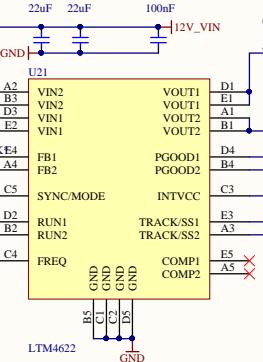
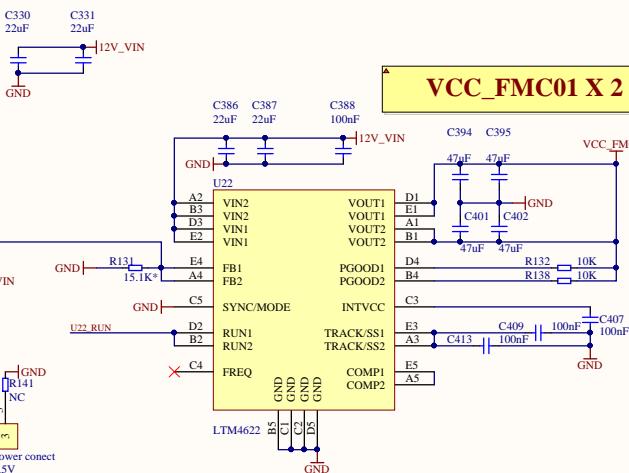
需要单独完整的一部分铜皮供电给BGA底部



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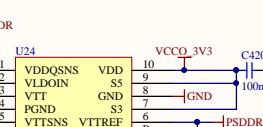
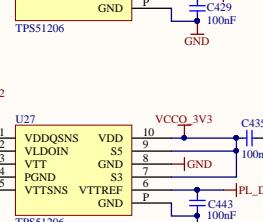
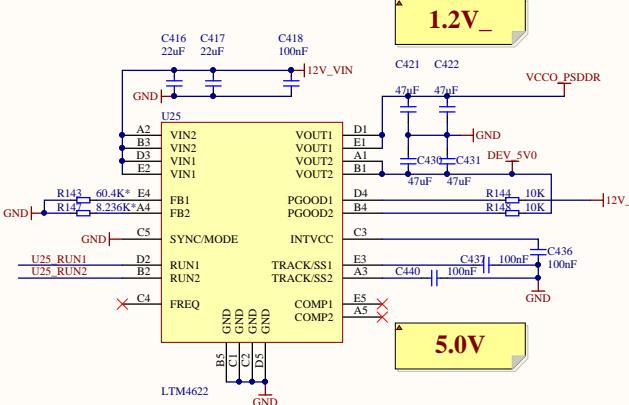
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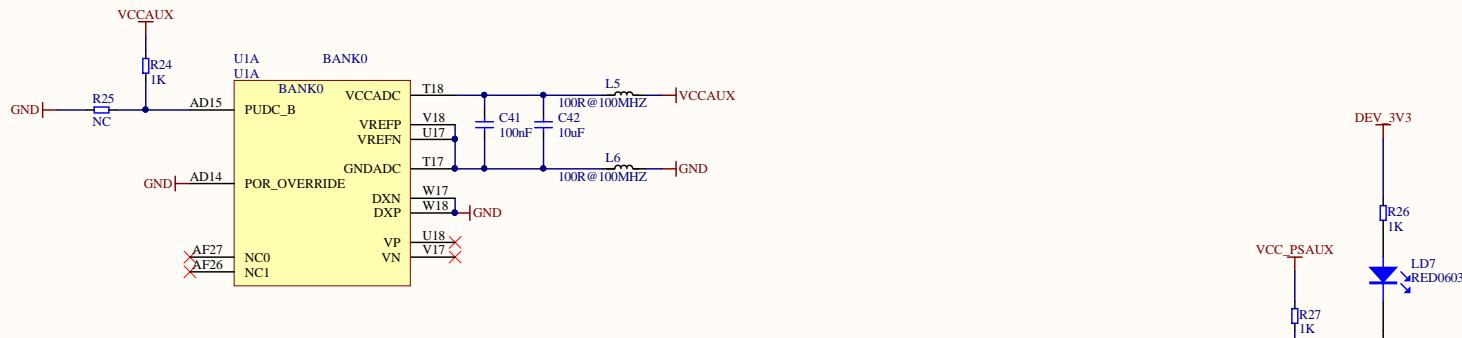
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B

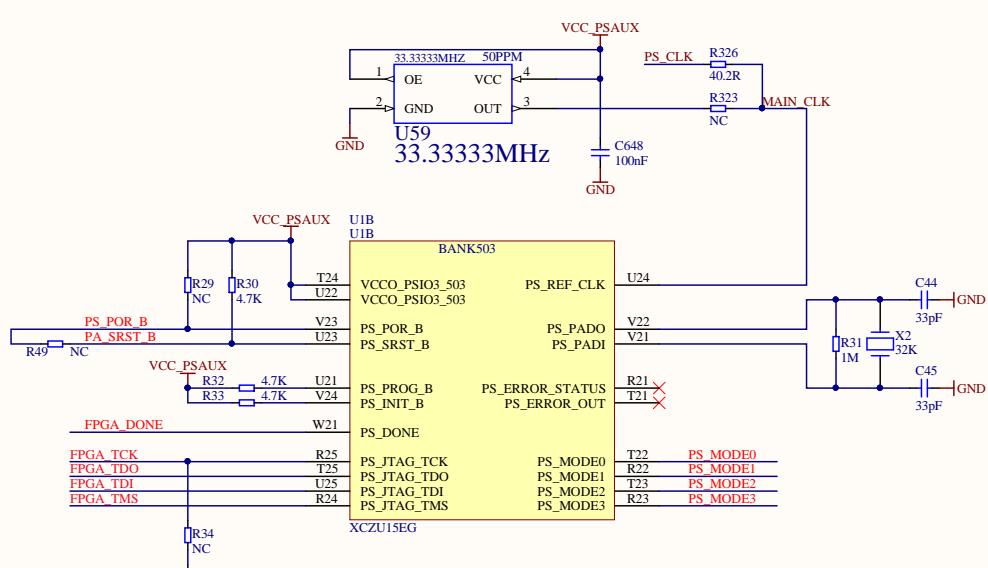
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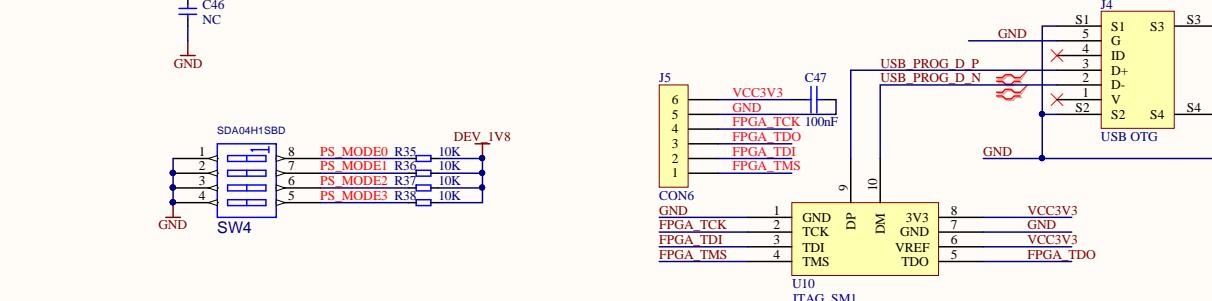
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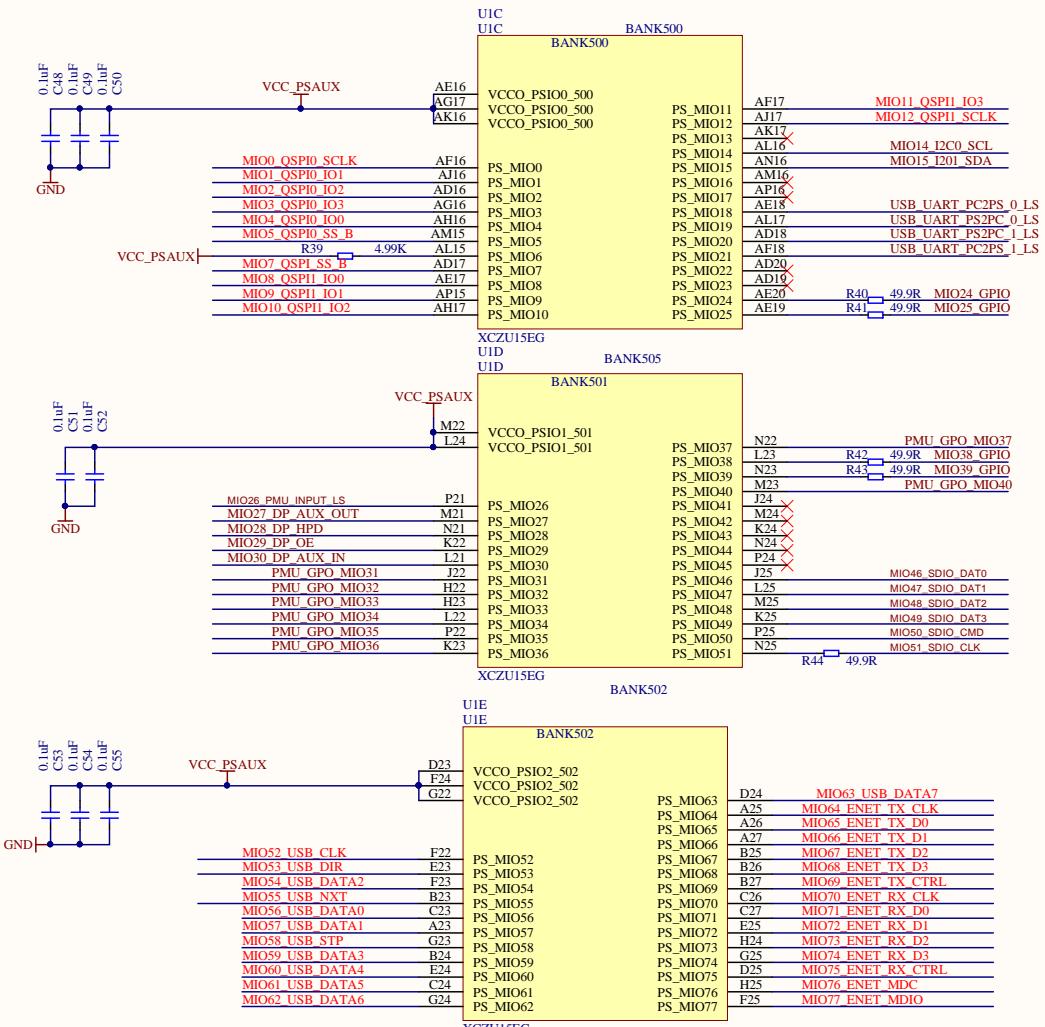
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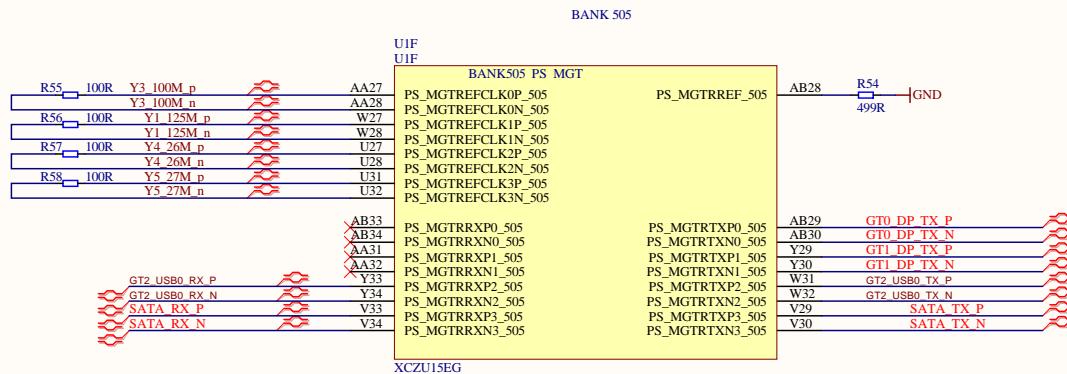
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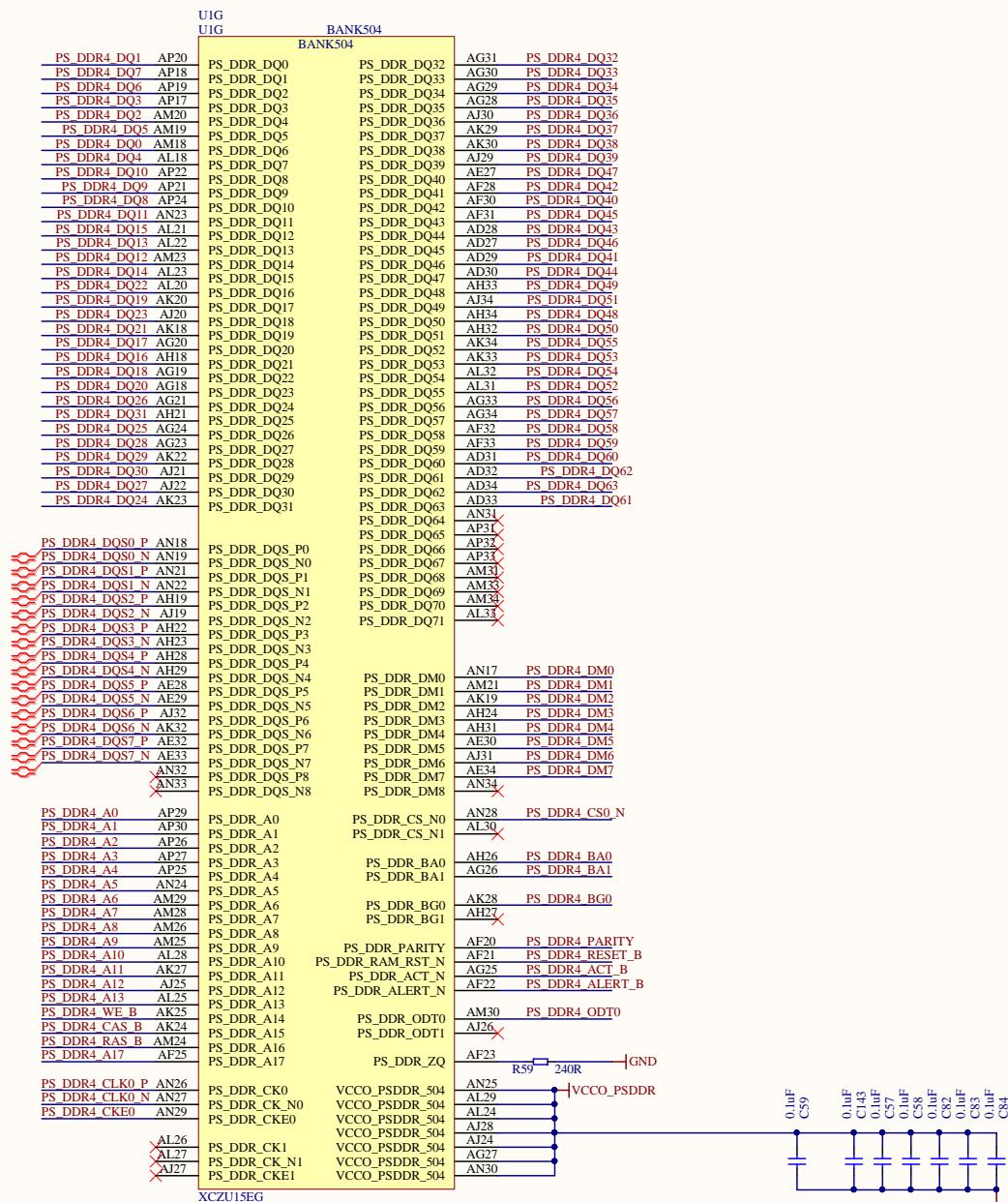
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Title

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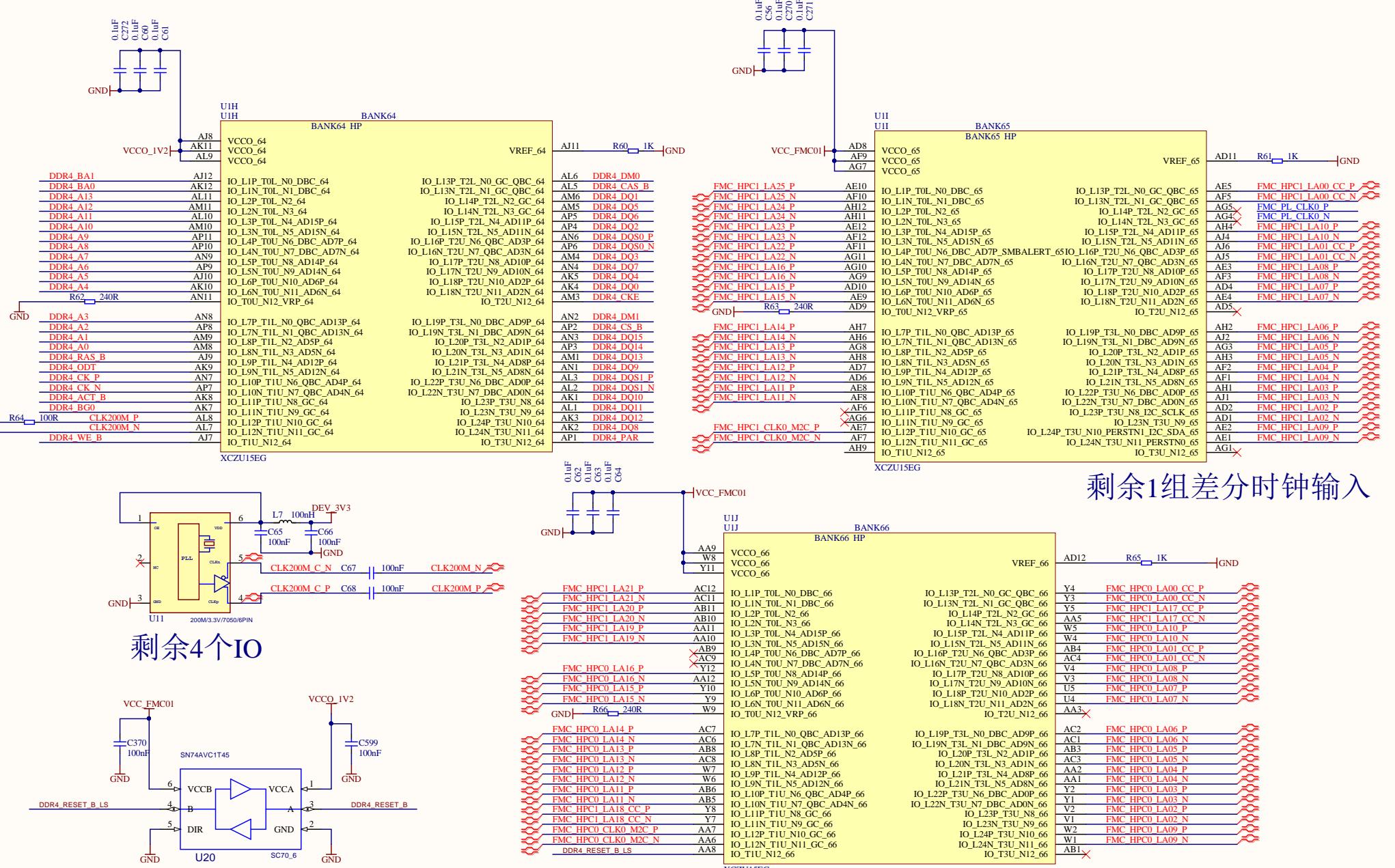
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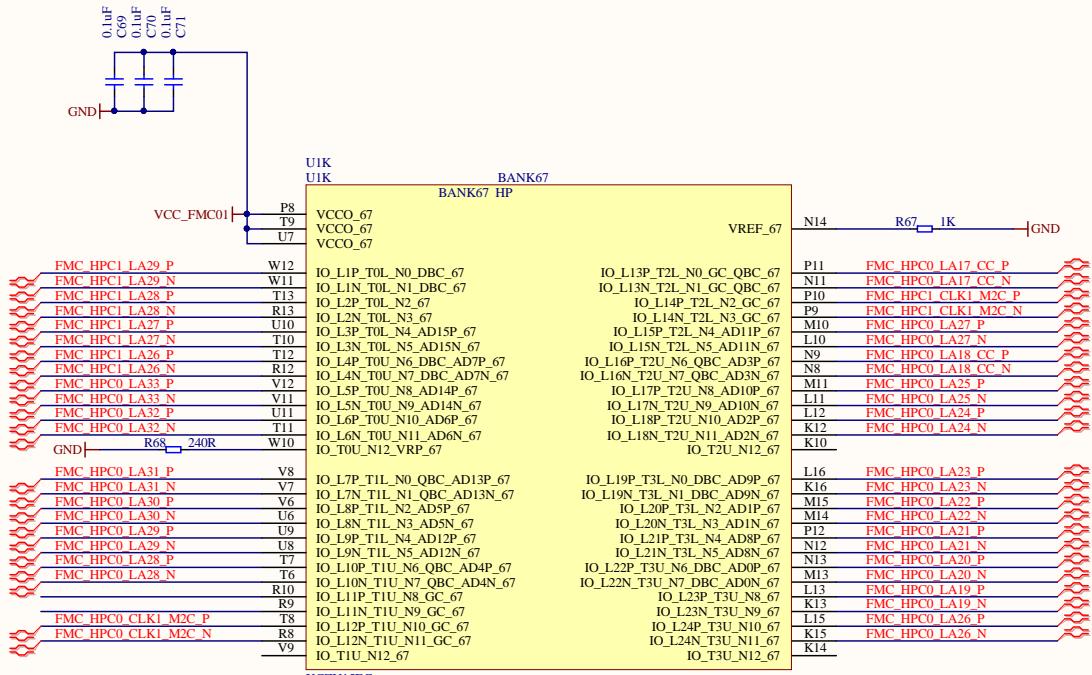
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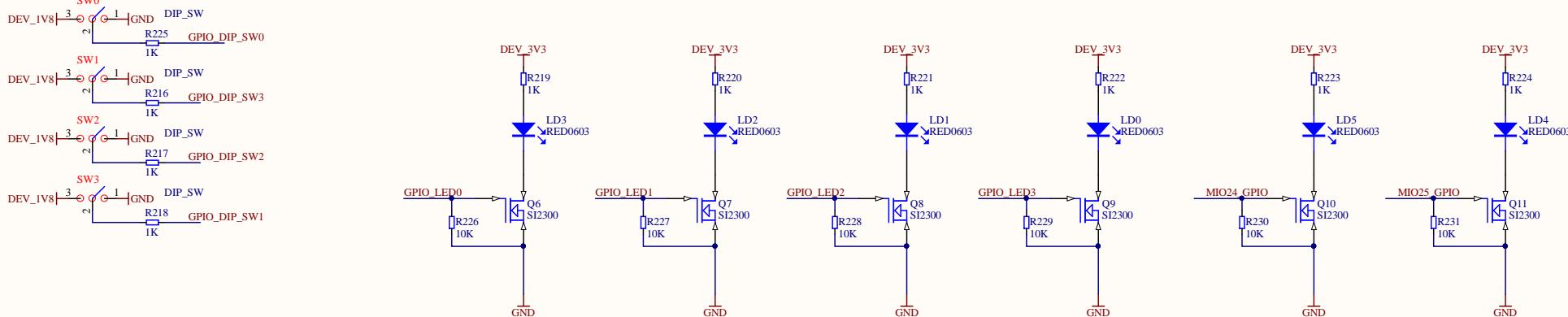
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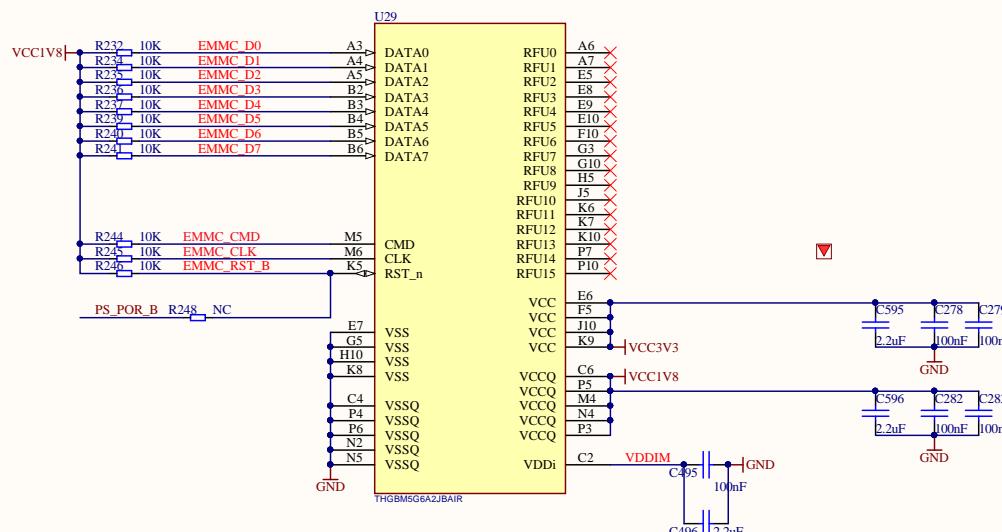


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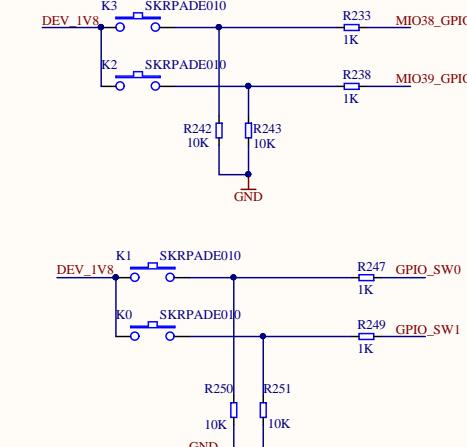
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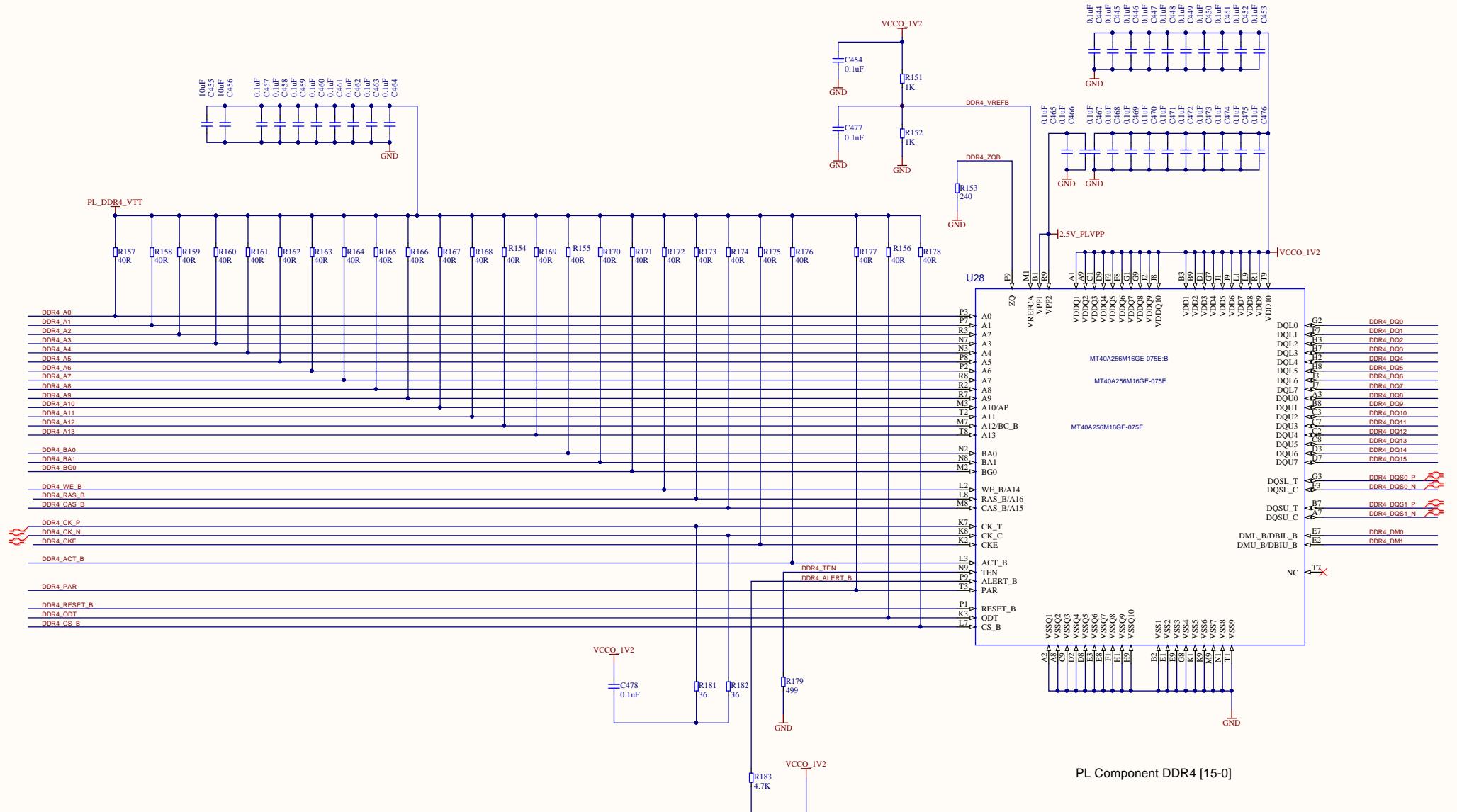


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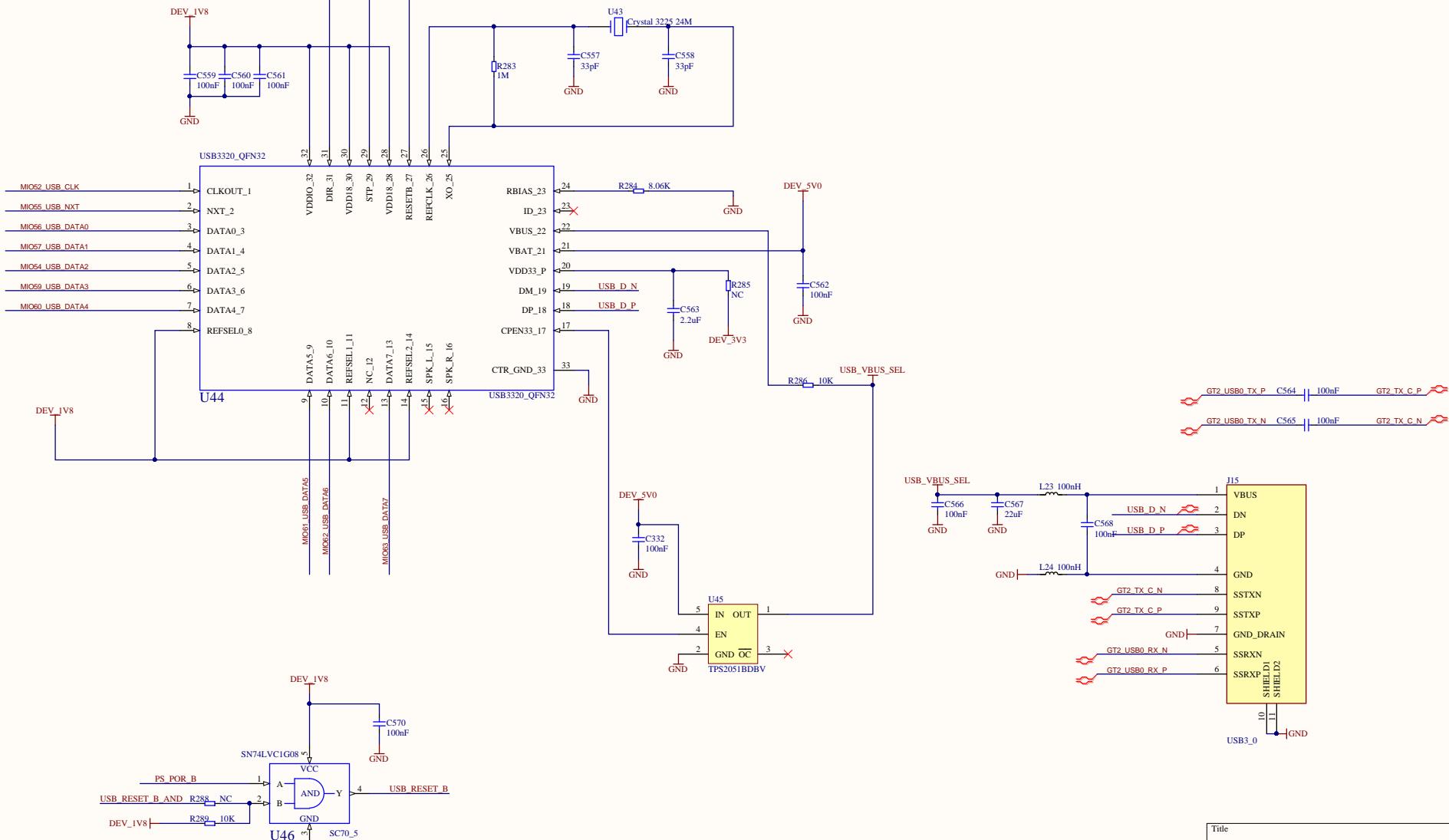


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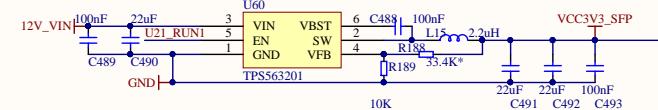
USB 2.0 ULPI TRANSCEIVER AND CONNECTOR



Title		
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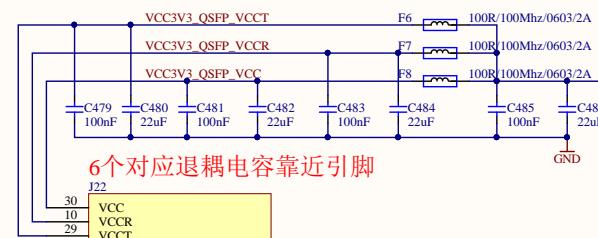
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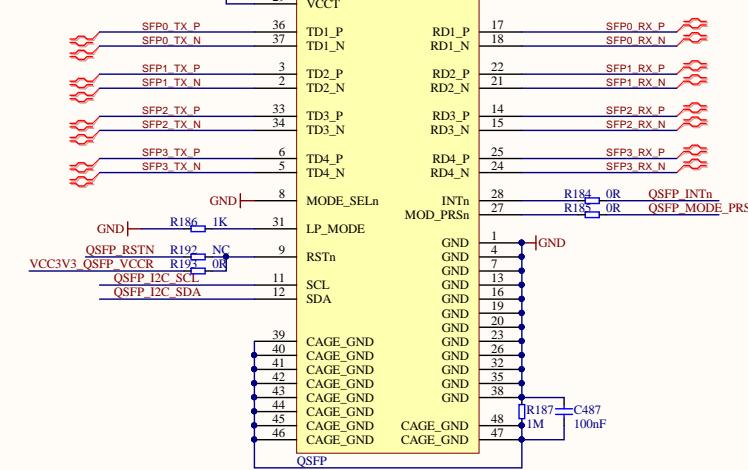
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6个对应退耦电容靠近引脚

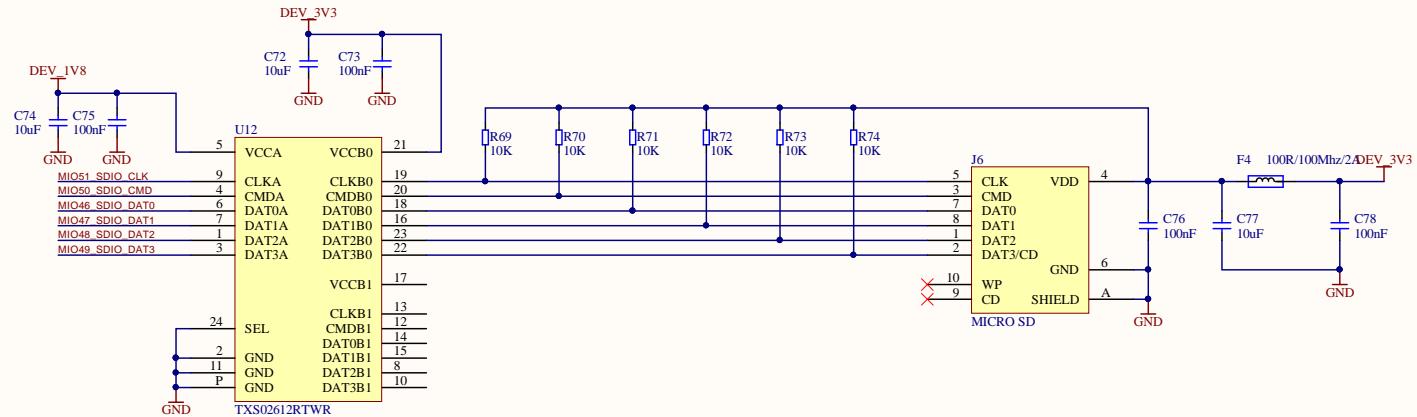
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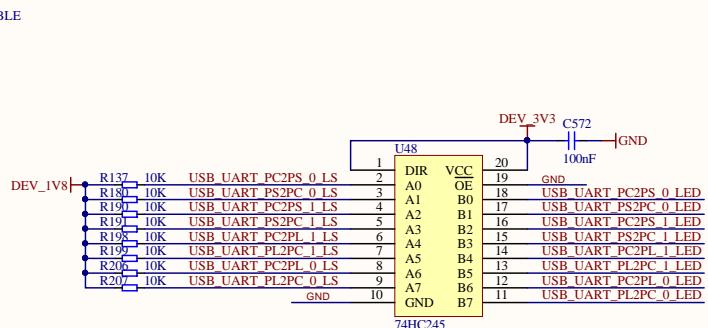
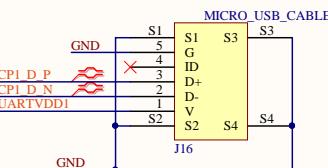
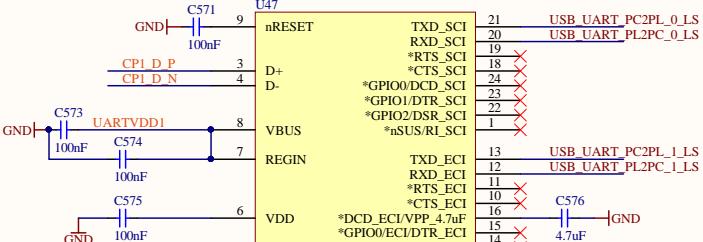
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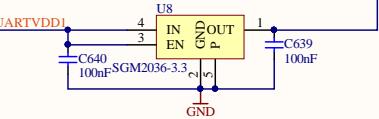
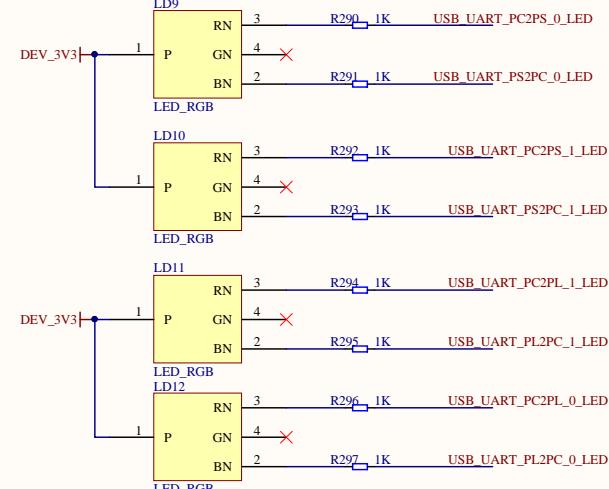
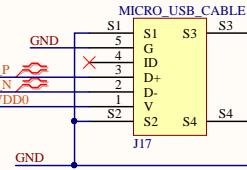
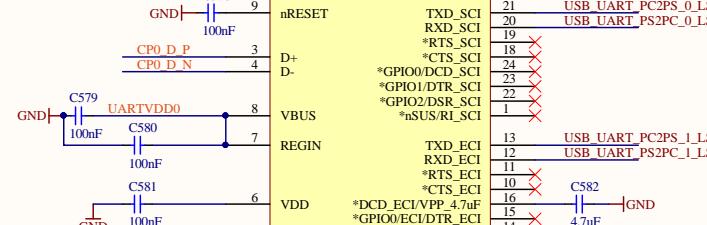


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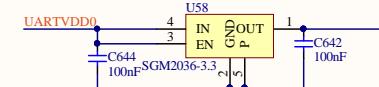
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PL UART

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**PS UART**

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Title		
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