

Project2: Optimizing the Performance of a Pipelined Processor

CS 359, COMPUTER ARCHITECTURE, Yanyan Shen, Spring 2017

1 Part C

1.1 Description

- The program **ncopy.js** copies a len-element integer array *src* to a non-overlapping *dst*, returning the count number of positive integers contained in *src*.
- In this part, we need to minimize the running time of **ncopy.js** as less as possible. As we have learned in the class, the *jump* instructions (including *jmp*, *jle*, *jl*, *je*, *jne*, *jge* and *jb* in Y86 instructions). Of course we can write one loop to solve this problem, but it costs too much time. So we use some other methods to solve this problem.

1.2 Solution

ncopy.js

```
1  /* $begin ncopy-ys */
2  #####
3  # ncopy.js - Copy a src block of len ints to dst.
4  # Return the number of positive ints (>0) contained in src.
5  #
6  # Include your name and ID here.
7  #
8  # Describe how and why you modified the baseline code.
9  #
10 #####
11 # Do not modify this portion
12 # Function prologue.
13 ncopy:  pushl %ebp                # Save old frame pointer
14         rrmovl %esp,%ebp        # Set up new frame pointer
15         pushl %esi              # Save callee-save regs
16         pushl %ebx
17         pushl %edi
18         mrmovl 8(%ebp),%ebx     # src
19         mrmovl 16(%ebp),%edx    # len
20         mrmovl 12(%ebp),%ecx    # dst
21 #####
22         xorl  %eax, %eax        # initialize the count to 0
23
24 #####
25 Loop8:
26         iaddl $-8, %edx         # len = len - 8
27         andl  %edx, %edx        # to see if the len is less than 0
28         jle  Loop4
29
30         mrmovl (%ebx), %esi
31         mrmovl 4(%ebx), %edi
```

```

32         rmmovl %esi , (%ecx)
33         andl %esi , %esi
34         jle StageOneOf8
35         iaddl $1 , %eax
36
37 StageOneOf8:
38         rmmovl %edi , 4(%ecx)
39         andl %edi , %edi
40         jle StageTwoOf8
41         iaddl $1 , %eax
42
43 StageTwoOf8:
44         mrmovl 8(%ebx) , %esi
45         mrmovl 12(%ebx) , %edi
46         rmmovl %esi , 8(%ecx)
47         andl %esi , %esi
48         jle StageThreeOf8
49         iaddl $1 , %eax
50
51 StageThreeOf8:
52         rmmovl %edi , 12(%ecx)
53         andl %edi , %edi
54         jle StageFourOf8
55         iaddl $1 , %eax
56
57 StageFourOf8:
58         mrmovl 16(%ebx) , %esi
59         mrmovl 20(%ebx) , %edi
60         rmmovl %esi , 16(%ecx)
61         andl %esi , %esi
62         jle StageFiveOf8
63         iaddl $1 , %eax
64
65 StageFiveOf8:
66         rmmovl %edi , 20(%ecx)
67         andl %edi , %edi
68         jle StageSixOf8
69         iaddl $1 , %eax
70
71 StageSixOf8:
72         mrmovl 24(%ebx) , %esi
73         mrmovl 28(%ebx) , %edi
74         rmmovl %esi , 24(%ecx)
75         andl %esi , %esi
76         jle StageSevenOf8
77         iaddl $1 , %eax
78
79 StageSevenOf8:
80         rmmovl %edi , 28(%ecx)
81         andl %edi , %edi

```

```

82         jle Forward8
83         iaddl $1, %eax
84
85 Forward8:
86         iaddl $32, %ebx
87         iaddl $32, %ecx
88         jmp Loop8
89
90 #####
91 Loop4:
92         iaddl $8, %edx
93         iaddl $-4, %edx
94         andl %edx, %edx
95         jl Loop2
96
97         mrmovl (%ebx), %esi
98         mrmovl 4(%ebx), %edi
99         rmmovl %esi, (%ecx)
100        andl %esi, %esi
101        jle StageOneOf4
102        iaddl $1, %eax
103
104 StageOneOf4:
105        rmmovl %edi, 4(%ecx)
106        andl %edi, %edi
107        jle StageTwoOf4
108        iaddl $1, %eax
109
110 StageTwoOf4:
111        mrmovl 8(%ebx), %esi
112        mrmovl 12(%ebx), %edi
113        rmmovl %esi, 8(%ecx)
114        andl %esi, %esi
115        jle StageThreeOf4
116        iaddl $1, %eax
117
118 StageThreeOf4:
119        rmmovl %edi, 12(%ecx)
120        andl %edi, %edi
121        jle Forward4
122        iaddl $1, %eax
123
124 Forward4:
125        iaddl $16, %ebx
126        iaddl $16, %ecx
127        iaddl $-4, %edx
128        jmp Loop2
129
130 #####
131 Loop2:

```

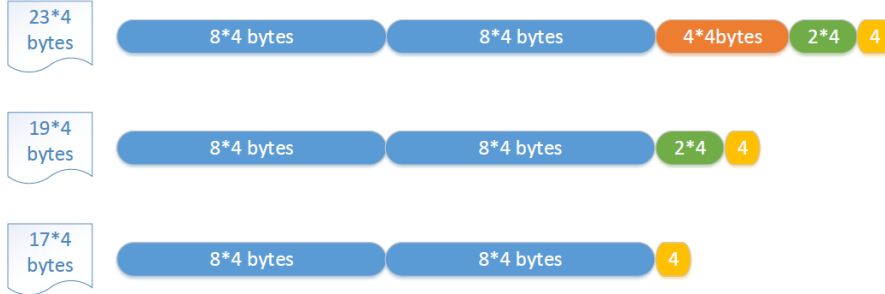
```

132         iaddl $4, %edx
133         iaddl $-2, %edx
134         andl %edx, %edx
135         jl Loop1
136
137         mrmovl (%ebx), %esi
138         mrmovl 4(%ebx), %edi
139         rmmovl %esi, (%ecx)
140         andl %esi, %esi
141         jle StageOneOf2
142         iaddl $1, %eax
143
144 StageOneOf2:
145         rmmovl %edi, 4(%ecx)
146         andl %edi, %edi
147         jle Forward2
148         iaddl $1, %eax
149
150 Forward2:
151         iaddl $8, %ebx
152         iaddl $8, %ecx
153         iaddl $-2, %edx
154         jmp Loop1
155
156 #####
157 Loop1:
158         iaddl $2, %edx
159         iaddl $-1, %edx
160         andl %edx, %edx
161         jl Done
162
163         mrmovl (%ebx), %esi
164         rmmovl %esi, (%ecx)
165         andl %esi, %esi
166         jle Done
167         iaddl $1, %eax
168         jmp Done
169 #####
170 # Do not modify the following section of code
171 # Function epilogue.
172 Done:
173         popl %edi                # Restore callee-save registers
174         popl %ebx
175         popl %esi
176         rmmovl %ebp, %esp
177         popl %ebp
178         ret
179 #####
180 # Keep the following label at the end of your function
181 End:

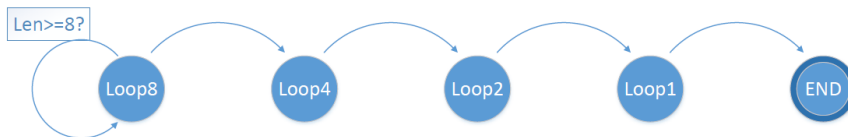
```

1.3 Analysis

1.3.1 sum.ys



- To explain our solution clearly, we provide the above picture to express the overview of our solution.
- Our main idea is: In each copy instruction, we copy 4 *bytes*. Firstly, we copy each 8 * 4 *bytes* in a loop until the left data needed to copy is less than 8 * 4 *bytes*. Secondly, we check if the left data is more or equal to 4 * 4 *bytes*. If so, we copy 4 * 4 *bytes* in this step. Thirdly, we check if the left data is more or equal to 2 * 4 *bytes*. If so, we copy 2 * 4 *bytes* in this step. Finally, we check if there are still 1 * 4 *bytes* left. If so, we copy these 4 *bytes* and end the problem. Since we need to return the count number of positive integers contained in *src*, we will do a judge after each copy instruction to decided if we need to increase the count by 1.
- For the registers, we define *esi* and *edi* to store the value of contiguous 2 words(4 bytes each word). We define *ebx* to store the address of current *src* and *dst* to store the address of current *dst*. We define *edx* to store the *len* of left words needed to copy. We define *eax* to store the count of positive integers in our copied data.
- For the Loops, we define 4 main blocks: *Loop8*, *Loop4*, *Loop2* and *Loop1*. However, only the *Loop8* is the "real" Loop which means only *Loop8* can be excuted more than 1 time. In the above picture, blue means *Loop8*, oringe means *Loop4*, green means *Loop2* and yellow means *Loop1*. We provide following picture to represent the relations among these Loop.



- We take *Loop4* for example. First we plus 4 to *edx* and check if it is less than 0. If it is, the program will jump to *Loop2*. Else, we do 4 copy instructions in *Loop4*. The register *esi* and *edi* stores the current data we need to copy. After each copy instruction, we will check if the data is a positive integer. If it is, the register *eax* will be added by 1. Else, the problem will ignore the add instruction and jump to next copy instruction.
- Assume we need to copy *n* words and *n* is a very large number. Assume the count number of positive integers contained in *src* is *m*. In our progress, we need $O(\log_2^n + m)$ jumps.

1.4 Outcome

1.5 Review