assert
~~property~~

property y-0 ;(@ posedge (clk) a |-> ##2 b)';

property y-1 ;(@ posedge (clk) a && b |-> ##[1:3] c)

property y-2 ;(@ posedge (clk)

Sequence S;11b ;
    ## 2 ~ b ;
endSequence

property y-3 ; (@ posedge (clk) $one-hot (Y))

property y-4 ; (@ posedge (clk)(D=0) |-> ##[1 is invalid)