ECO-GREEN ATM

Our project is The standing ATM machine that when a person approaches the ATM to withdraw cash the ATM a "HELLO" message is displayed and LED is switched on , however if no one approaches the ATM ;no message will be displayed and LED is switched off.

Our code implements this functionality as if the IR sensor detects that person has approached the ATM and is about to withdraw cash and from it.It detects his presence and the ATM displays a message to welcome the person with "Hello" and when the person leaves the ATM machine the IR sensor detects the person has left and the message "Hello" disappear . Also , there is a light sensor that detects light if the light sensor detect light (DAY) the LED above the ATM is switched off and if the light sensor didn't detect light (NIGHT) and the IR sensor detects a person in fronts of the ATM the LED is switched on

The first vhdl file containing ENITY finalproject

Where the inputs of this entity is the output of the IR sensor and light sensor, clk and reset and output is LED and screen(7 Segments). So, if the reset button is not pushed we check if

the output of the IR sensor is '0' then a person is in front of the ATM so the signal 'inside' is assigned with '1',

then we check if the inside signal is assigned to '1' and the light sensor output is '0' (did detect light so, it is day time) so the light is assigned to '0' to switch off the LED and the screen is assigned to '1' to display the message "Hello"

if the inside signal assigned to '1' and the light sensor output is '1' (didn't detect light so , it is night time) so the light is assigned to '1' to switch on the LED and the screen is assigned to '1' to display the message "Hello"

However if the output of the IR sensor is '1' then a person isn't in front of the ATM has left so the signal 'inside' is assigned with '0' and the 'screen' is assigned to '0' and 'light' assigned to '0',

if the reset button is pushed then 'inside' is assigned to '0' and light is assigned to '0' and the screen is assigned to '0' (LED is switched off and there is no display of message

The second vhdl file containing ENTITY seven_seg_dec

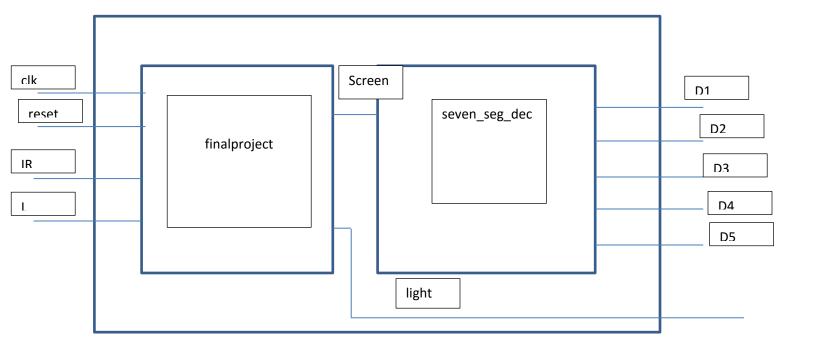
is responsible for showing the message "HELLO" on the 5 seven segments

Our entity takes as input the screen from the top level entity which states whether it is activated or not and the output is represented as 5 vectors where each bit in the vector represents

whether this part of the segment is on or not ,otherwise the output in all the 5 segments will be 1 as the seven segment is active-low.

THE TOP ENTITY

Is responsible for gathering both entities Elaborated more in the diagram



PIN Assignment

Named: * ~	«» Edit: ×	4										Filter: Pins: all
Node Name	Direction	Location	I/O Bank	/REF Group	tter Locatic	'O Standarı	Reserved	Current Strength	Slew Rate	fferential P.	ct Preserva	
reset	Input	PIN B8	7	B7 N0	PIN B8	2.5 V		12mA (default)				
🛎 light	Output	PIN AB2	3	B3 N0	PIN AB2	2.5 V		12mA (default)	2 (default)			
- L	Input	PIN V8	3	B3 N0	PIN V8	2.5 V		12mA (default)				
LIR	Input	PIN V10	3	B3 N0	PIN V10	2.5 V		12mA (default)				
4 d5[6]	Output	PIN B17	7	B7_N0	PIN B17	2.5 V		12mA (default)	2 (default)			
d 5[5]	Output		7	B7 N0		2.5 V		12mA (default)	2 (default)			
≝ d5[4]	Output		7	B7_N0		2.5 V		12mA (default)	2 (default)			
d 5[3]	Output	PIN B16	7	B7_N0	PIN B16	2.5 V		12mA (default)	2 (default)			
4 d5[2]	Output	PIN E18	6	B6_N0	PIN_E18	2.5 V		12mA (default)	2 (default)			
5 d5[1]	Output	PIN D18	6	B6 N0		2.5 V		12mA (default)	2 (default)			
4 d5[0]	Output	PIN_C18	7	B7_N0		2.5 V		12mA (default)	2 (default)			
4 d4[6]	Output	PIN_E17	6	B6_N0	PIN_E17	2.5 V		12mA (default)	2 (default)			
4 d4[5]	Output	PIN_D19	6	B6_N0	PIN_D19	2.5 V		12mA (default)	2 (default)			
d 4[4]	Output	PIN_C20	6	B6_N0		2.5 V		12mA (default)	2 (default)			
4 d4[3]	Output		7	B7_N0		2.5 V		12mA (default)	2 (default)			
4 d4[2]	Output	PIN E21	6	B6 N0	PIN E21	2.5 V		12mA (default)	2 (default)			
4 d4[1]	Output	PIN E22	6	B6 N0	PIN E22	2.5 V		12mA (default)	2 (default)			
d 4[0]	Output	PIN F21	6	B6 N0	PIN F21	2.5 V		12mA (default)	2 (default)			
d 3[6]	Output	PIN B22	6	B6 N0	PIN B22	2.5 V		12mA (default)	2 (default)			
d 3[5]	Output	PIN C22	6	B6 N0	PIN C22	2.5 V		12mA (default)	2 (default)			
4 d3[4]	Output	PIN B21	6	B6 N0	PIN B21	2.5 V		12mA (default)	2 (default)			
4 d3[3]	Output	PIN A21	6	B6 N0	PIN A21	2.5 V		12mA (default)	2 (default)			
43 [2]	Output	PIN B19	7	B7 N0	PIN B19	2.5 V		12mA (default)	2 (default)			
4 d3[1]	Output	PIN A20	7	B7_N0	PIN A20	2.5 V		12mA (default)	2 (default)			
d 3[0]	Output	PIN B20	6	B6 N0	PIN B20	2.5 V		12mA (default)	2 (default)			
≝ d2[6]	Output	PIN F20	6	B6 N0	PIN F20	2.5 V		12mA (default)	2 (default)			
d 2[5]	Output	PIN F19	6	B6 N0	PIN F19	2.5 V		12mA (default)	2 (default)			
≝ d2[4]	Output	PIN H19	6	B6 N0	PIN H19	2.5 V		12mA (default)	2 (default)			
d 2[3]	Output	PIN J18	6	B6 N0	PIN J18	2.5 V		12mA (default)	2 (default)			
4 d2[2]	Output	PIN E19	6	B6 N0	PIN E19	2.5 V		12mA (default)	2 (default)			
d 2[1]	Output	PIN E20	6	B6 N0	PIN E20	2.5 V		12mA (default)	2 (default)			
d 2[0]	Output	PIN F18	6	B6 N0	PIN F18	2.5 V		12mA (default)	2 (default)			
d 1[6]	Output	PIN N20	6	B6 N0	PIN N20	2.5 V		12mA (default)	2 (default)			
4 d1[5]	Output	PIN N19	6	B6 N0	PIN N19	2.5 V		12mA (default)	2 (default)			
d 1[4]	Output	PIN M20	6	B6 N0	PIN M20	2.5 V		12mA (default)	2 (default)			
d 1[3]	Output	PIN N18	6	B6 N0		2.5 V		12mA (default)	2 (default)			
d 1[2]	Output	PIN L18	6	B6 N0	-	2.5 V		12mA (default)	2 (default)			
d 1[1]	Output	PIN K20	6	B6 N0		2.5 V		12mA (default)	2 (default)			
d 1[0]	Output	PIN J20	6	B6 N0	PIN J20	2.5 V		12mA (default)	2 (default)			
- clk	Input	PIN P11	3	B3 N0		2.5 V		12mA (default)				
< <new node="">></new>		/////		-	-							



