

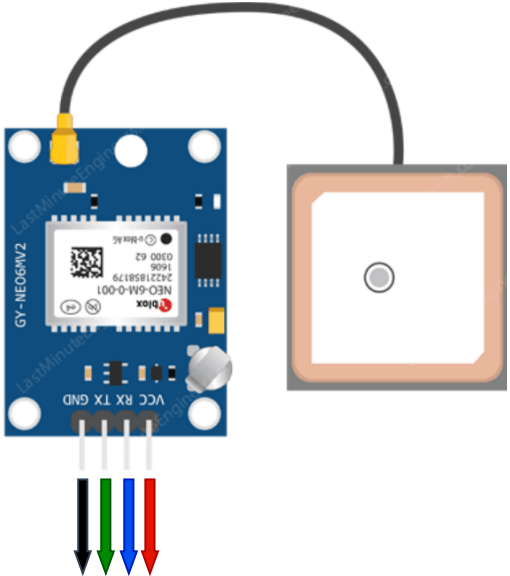
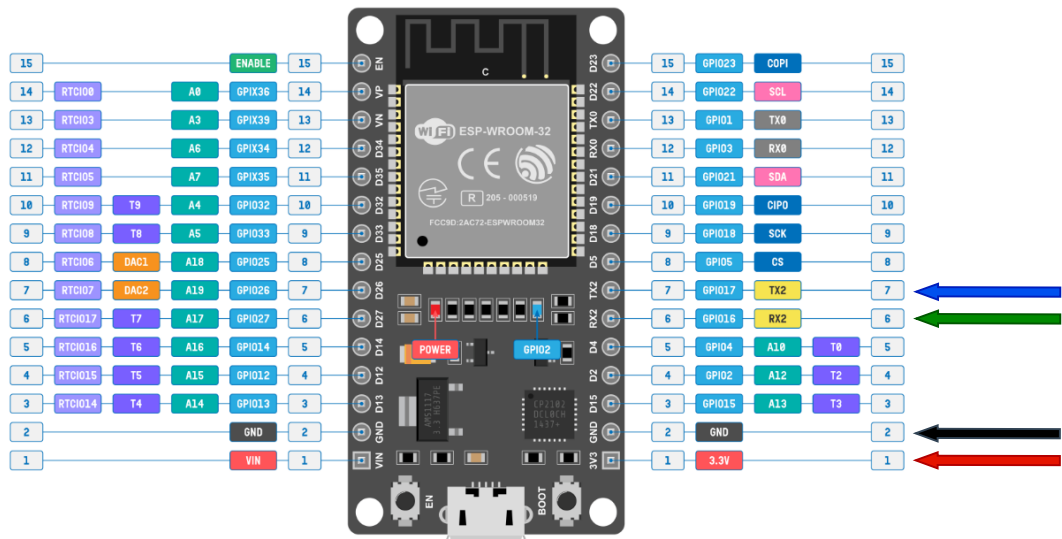
ESP32 Pin Connection Diagram

Note: Not all ESP32 board have the same pinout. The GPIO pins selected for UART have been carefully selected by referring to the pinout of my specific board mode and seeing if it maps out with the following ESP-IDF mapping for GPIO pins:
`{ESP-IDF Version}/esp-idf/components/soc/{compile target device}/include/soc/gpio_sig_map.h`

Refer to the information in `main/gps_library/gps_library.h` for more information.

DOIT ESP32 DEVKIT V1 PINOUT

CIRCUITSTATE

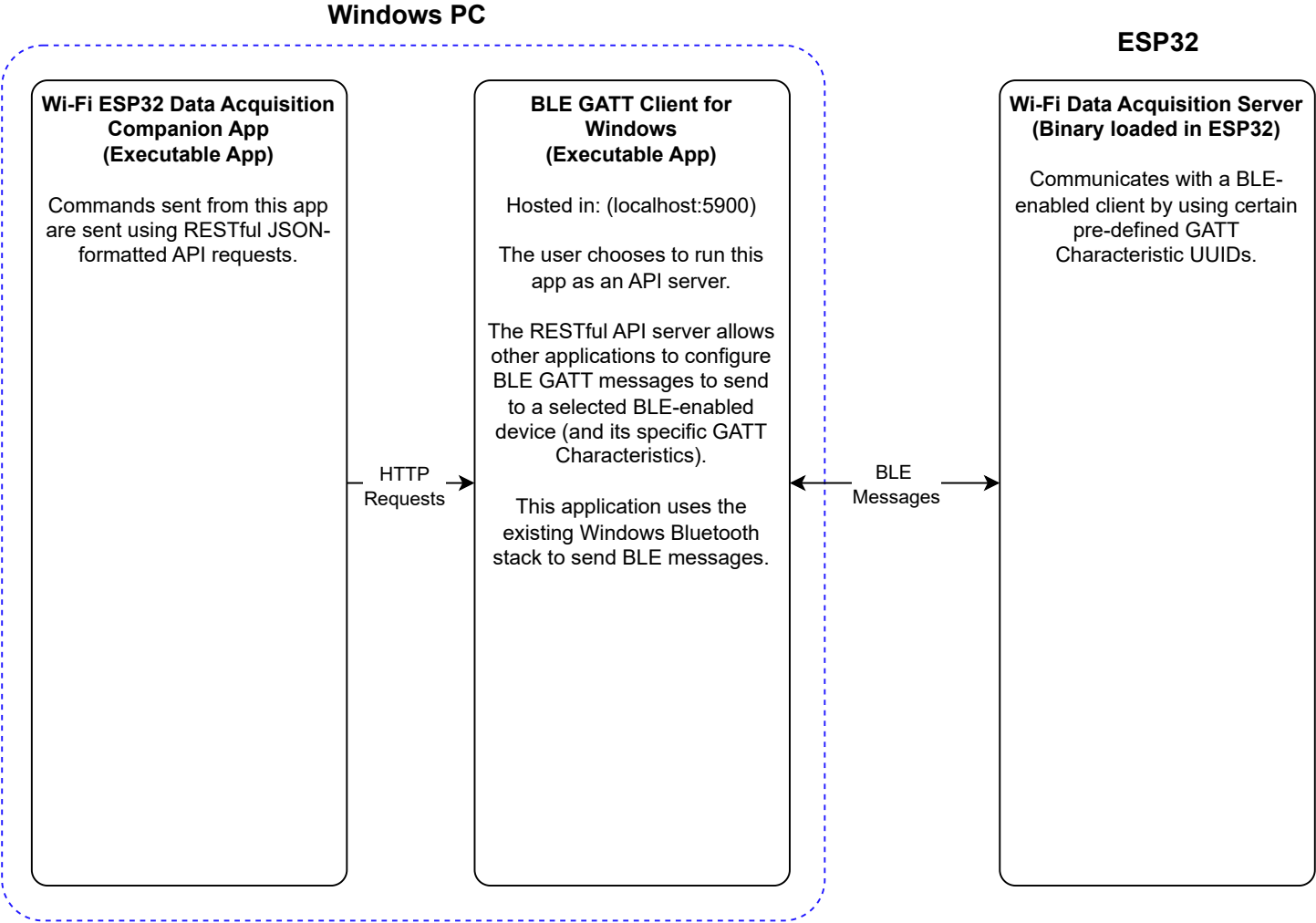


PHYSICAL PIN	POSITIVE SUPPLY	DAC OUTPUTS	SPI PINS
CONTROL PINS	GROUND SUPPLY	TOUCH INPUTS	UART PINS
GPIO PINS	ADC INPUTS	I2C PINS	EXCLUDED PINS

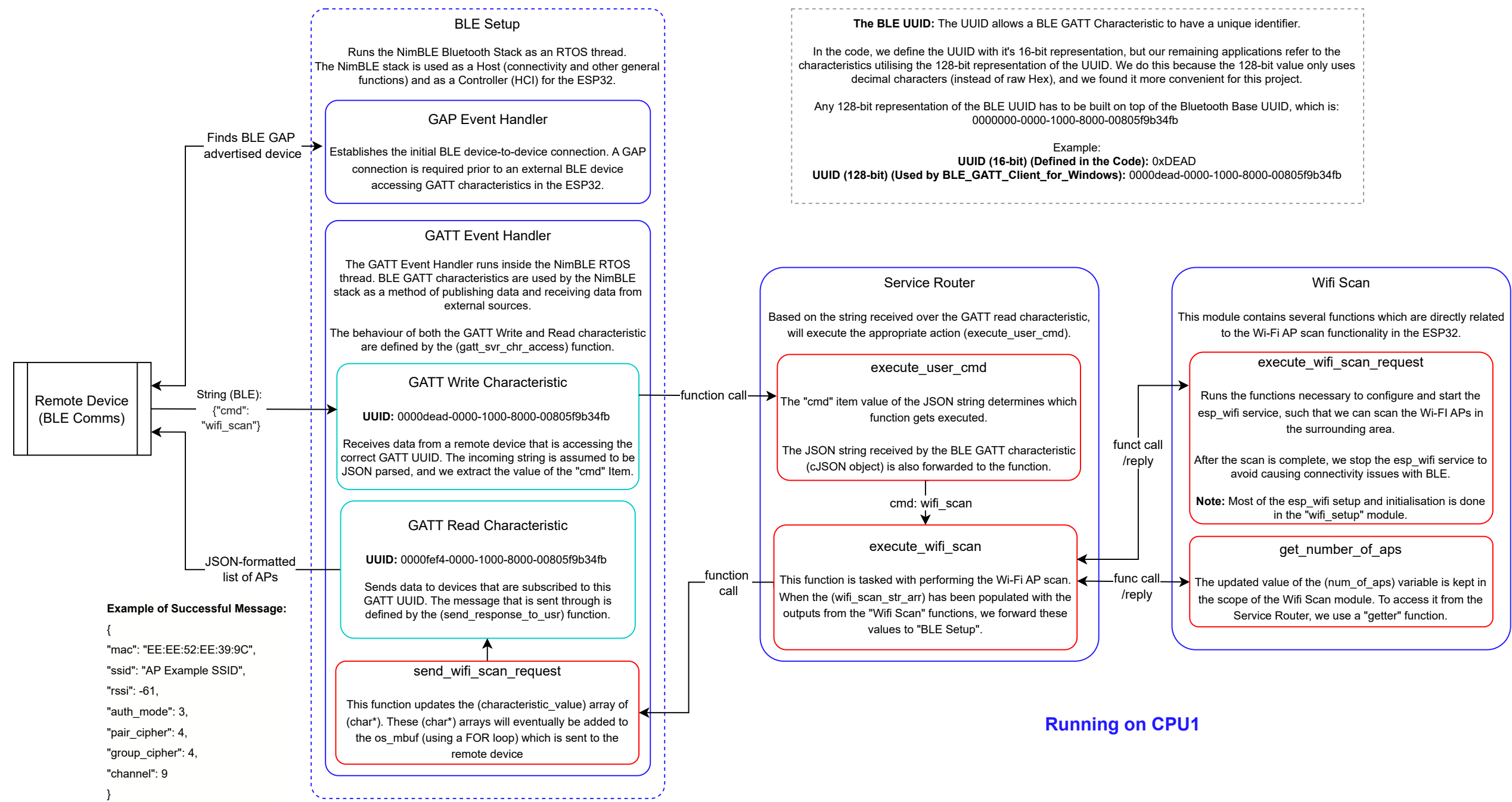
- GPIO pins 34, 35, 36 and 38 are input only.
- TX0 and RX0 (Serial0) are used for serial programming.
- TX2 and RX2 can be accessed as Serial2.
- Default SPI is VSPI. Both VSPI and HSPI pins can be set to any GPIO pins.
- All GPIO pins support PWM and interrupts.
- Built-in LED is connected to GPIO2.
- Some GPIO pins are used for interfacing flash memory and thus are not shown.



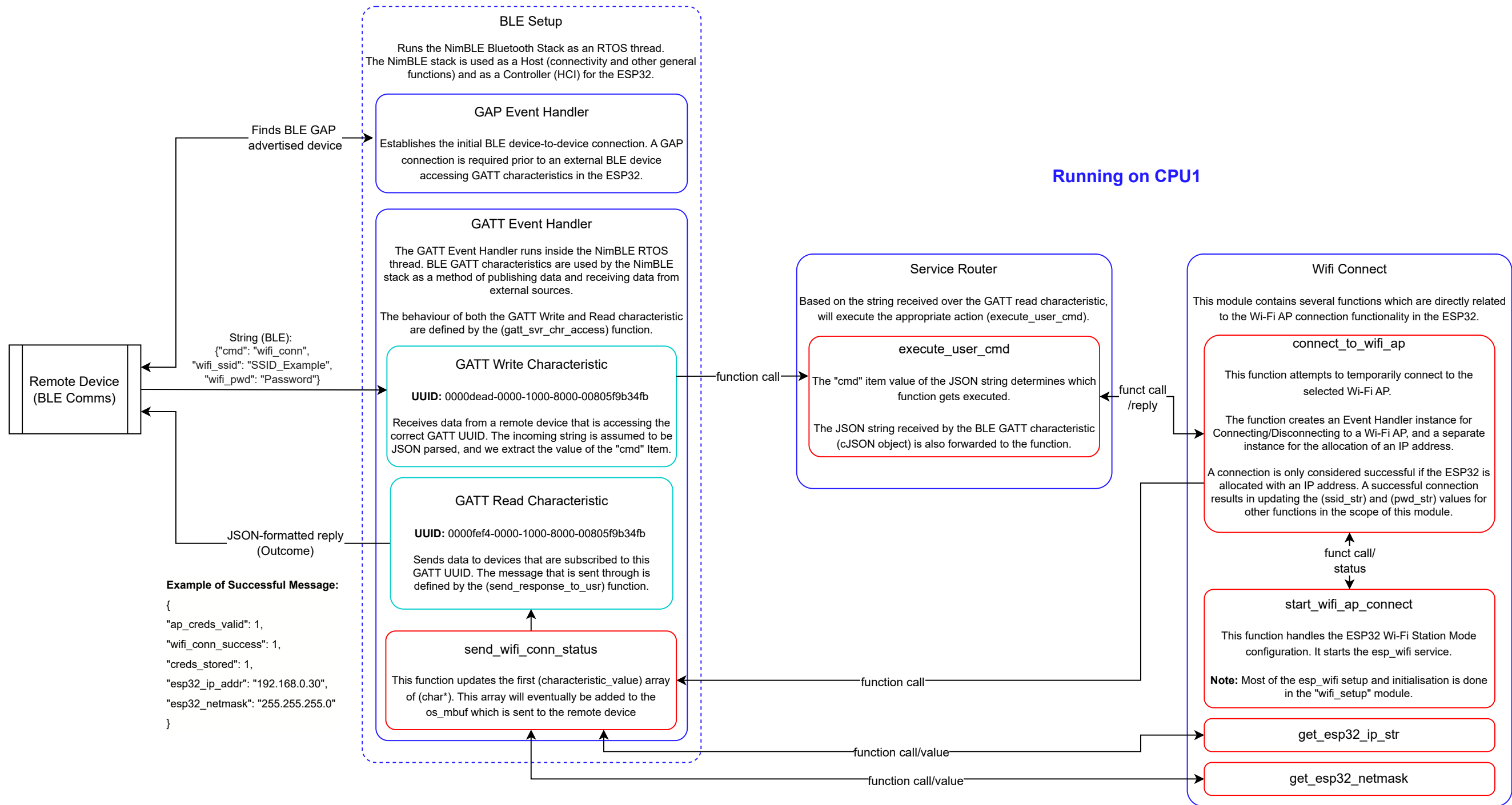
Project Applications Coordination



ESP32 (wifi_scan) Function

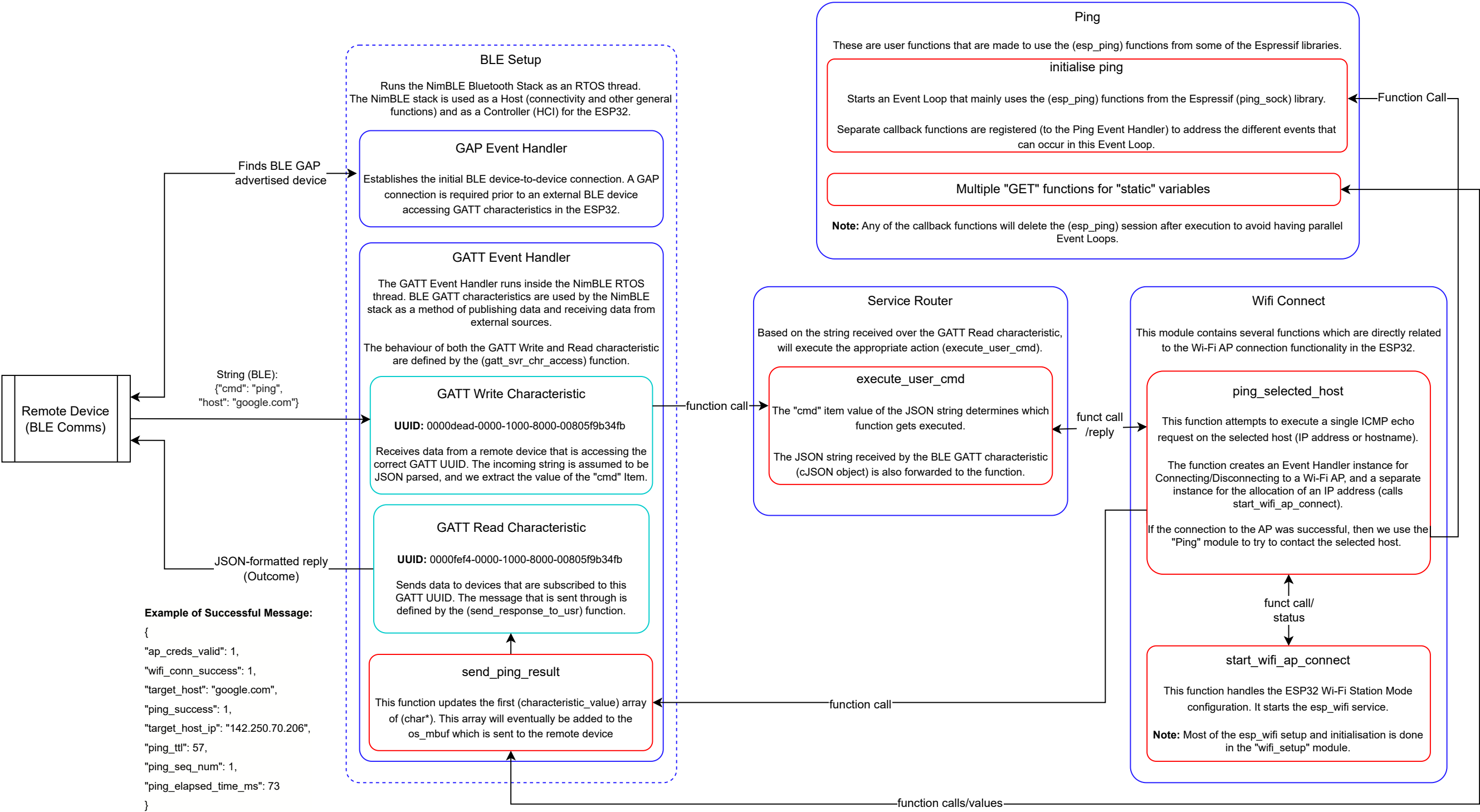


ESP32 (wifi_conn) Function



ESP32 (ping) Function

Running on CPU1



ESP32 (gps_location) Function

