

Towards DBMS-aware Memory Management in Unikernels

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18.11.2024 – 18.03.2025

The provider hosts databases for customers (DBaaS)



Feasible to choose custom operating system (OS)



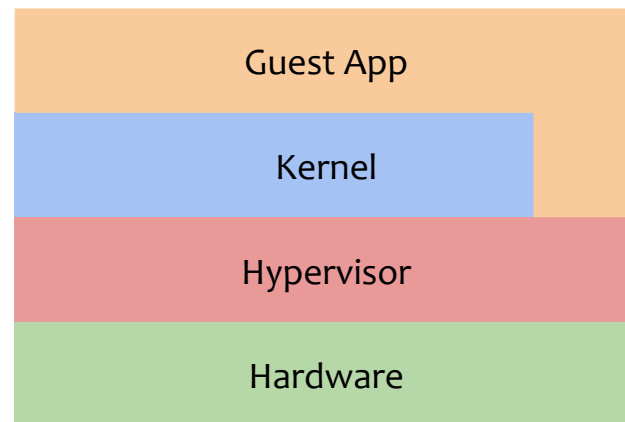
Move towards optimized OSs for database systems

OS Abstractions

- OS hides kernel level functionality from application
 - Scheduling
 - Memory Management

→ OS generality conflicts with DBMS requirements

	Efficient	Flexible	Complex
User-space systems	no	yes	no
Kernel modules ¹	yes	no	yes
Dune-based systems ²	very	yes	very

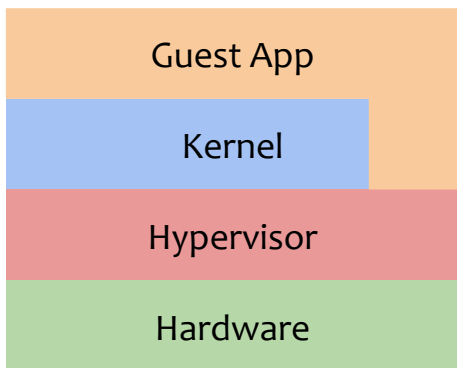


Traditional VM

¹ [ExMap\[SIGMOD'23\]](#), [Kreon\[TOS'21\]](#)

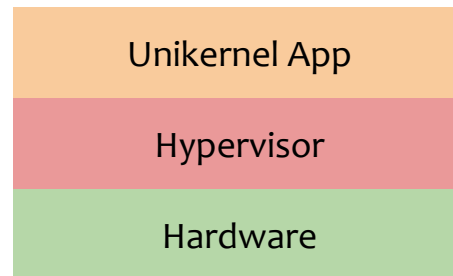
² [Aquila\[EuroSys'21\]](#), [Libdbos\[SIGMOD'25\]](#)

Figure inspired by [unikraft](#)



Traditional VM

- Heavyweight
- Address space / privilege isolation
- Large codebase

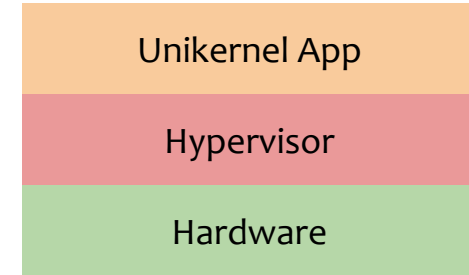


Unikernel

- Lightweight
- No address space / privilege isolation
- Small codebase

Unikernel-based Memory Management

	Efficient	Flexible	Complex
User-space systems	no	yes	no
Kernel modules	yes	no	yes
Dune-based systems	very	yes	very
Kernel API	yes	very	no



Unikernel

Outline



- ~~Motivation & Background~~
- Overview
- Design & Implementation
- Evaluation
- Conclusion

How can we design a **unikernel's** virtual memory (VM) subsystem to allow **efficient** and **general** DBMS/OS co-design?

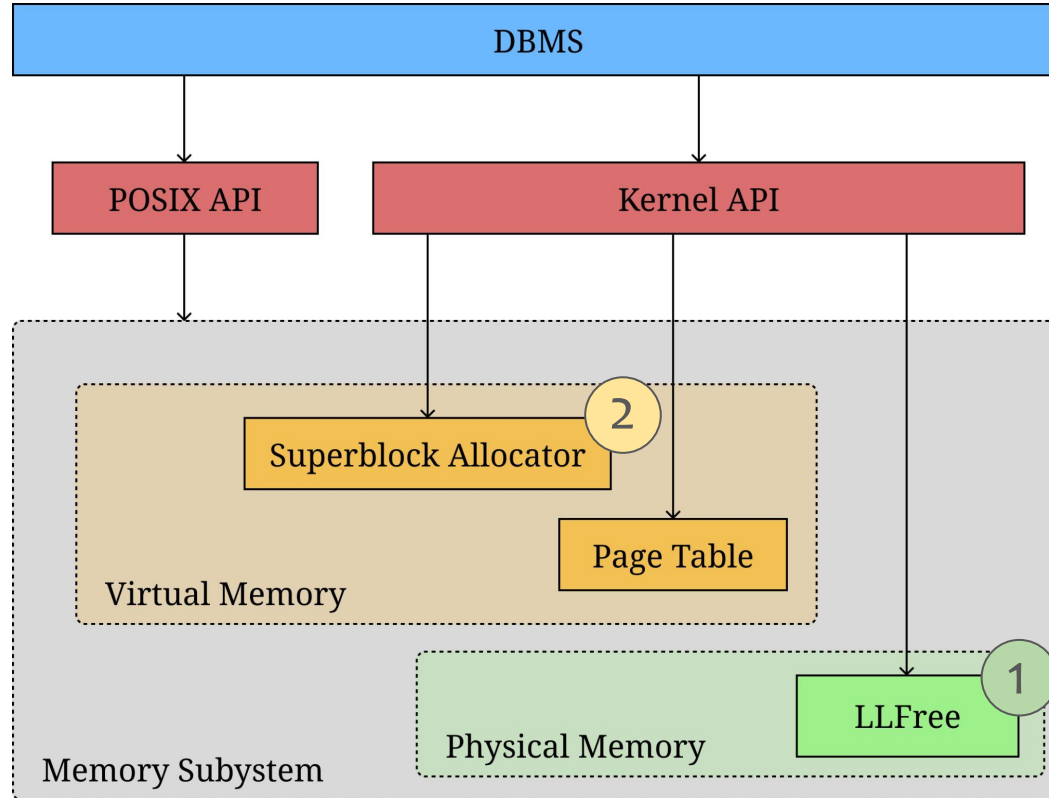
A new **VM subsystem** design exposing a **kernel API** tailored to DBMS requirements using a **unikernel architecture**

System design goals:

- Flexibility
- Scalability
- Compatibility
- Memory efficiency

- ~~Motivation & Background~~
- ~~Overview~~
- Design & Implementation
 - Overview
 - System design
 - Algorithms
- Evaluation
- Conclusion

Overview: Unikernel/DBMS Co-design



1

LLFree¹

Properties

- Scalable
- Lockfree
- Anti-fragmenting

Challenges

- Early allocation
- Initialization
- Memory contiguity

Kernel API: Physical memory allocation, Memory metrics

¹ [LLFree\[ATC'23\]](#)

2

Superblock Allocator

Properties

- Scalable
- Core local

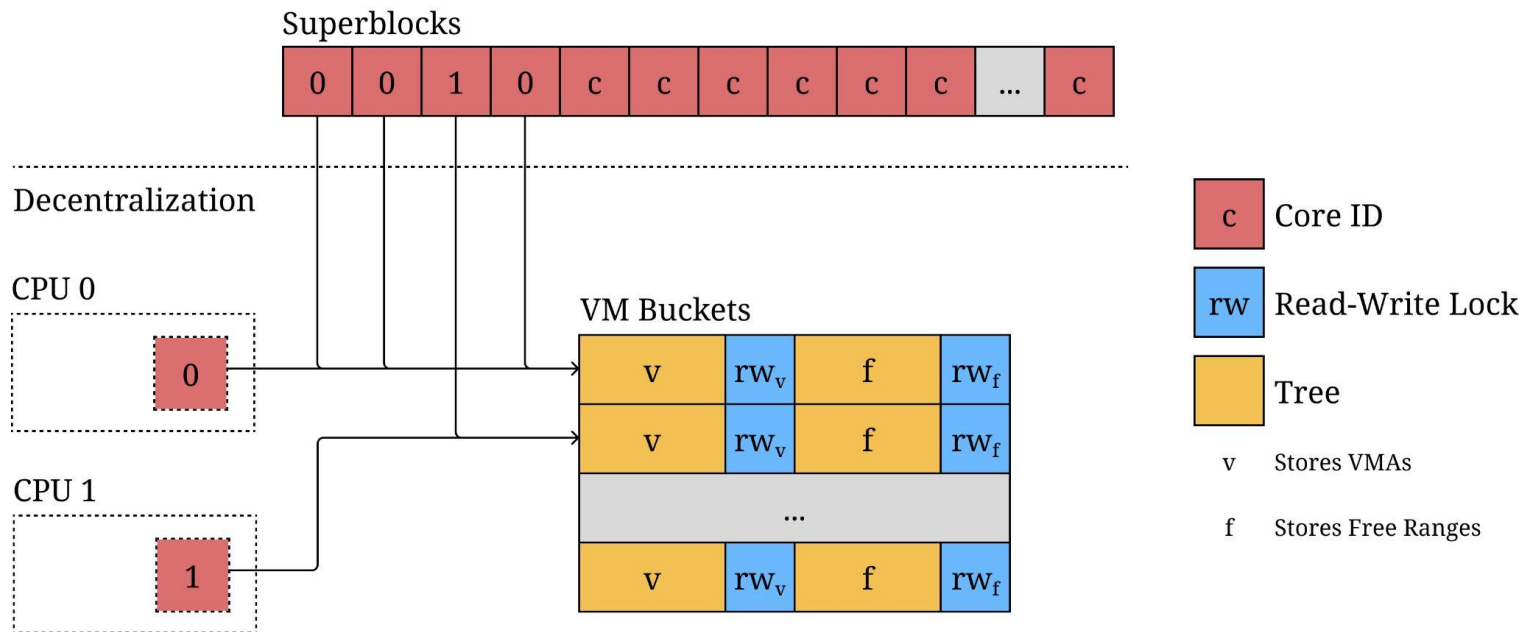
Challenges

- Optimistic superblock allocation
- Core distribution implications

Kernel API: VM allocation, VM area manipulation,
page table synchronization

Superblock Allocator

Lock-free global allocation



- ~~Motivation & Background~~
- ~~Overview~~
- ~~Design & Implementation~~
- Evaluation
 - Microbenchmarks
 - Macrobenchmarks
- Conclusion

Evaluation: Microbenchmarks

- Boot Time
- Memory Footprint
- VM Scalability
- Bulk (de-)allocation (Best case)
- Pipeline deallocation
- Randomized deallocation (Worst case)

Competitors

- Linux virtual machine +jemalloc
- Original OSv version
- Optimized OSv version +jemalloc

Bulk (Best case)

- Simulate allocation pattern of arena allocators
- Measure throughput of overall memory subsystem

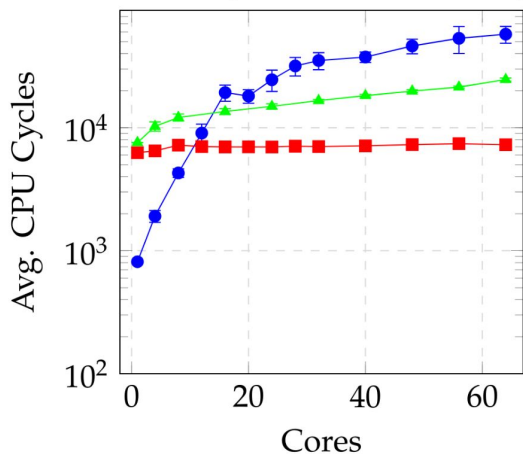
Random (Worst case)

- Approximate highly shared workloads
- Constructed to highlight worst case performance

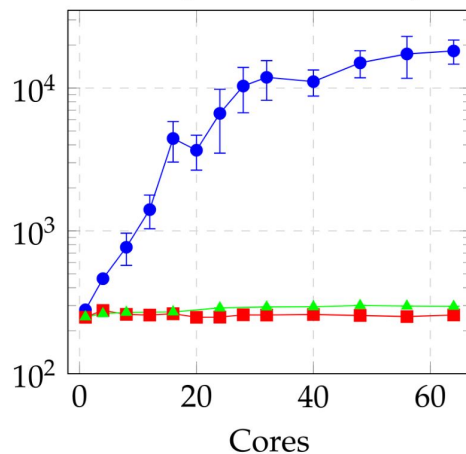
Microbenchmarks: Bulk

Bulk allocation | 200,000 pages | Core local allocation + deallocation

Linux VM + jemalloc Original OSv Optimized OSv + jemalloc



(a) Allocation



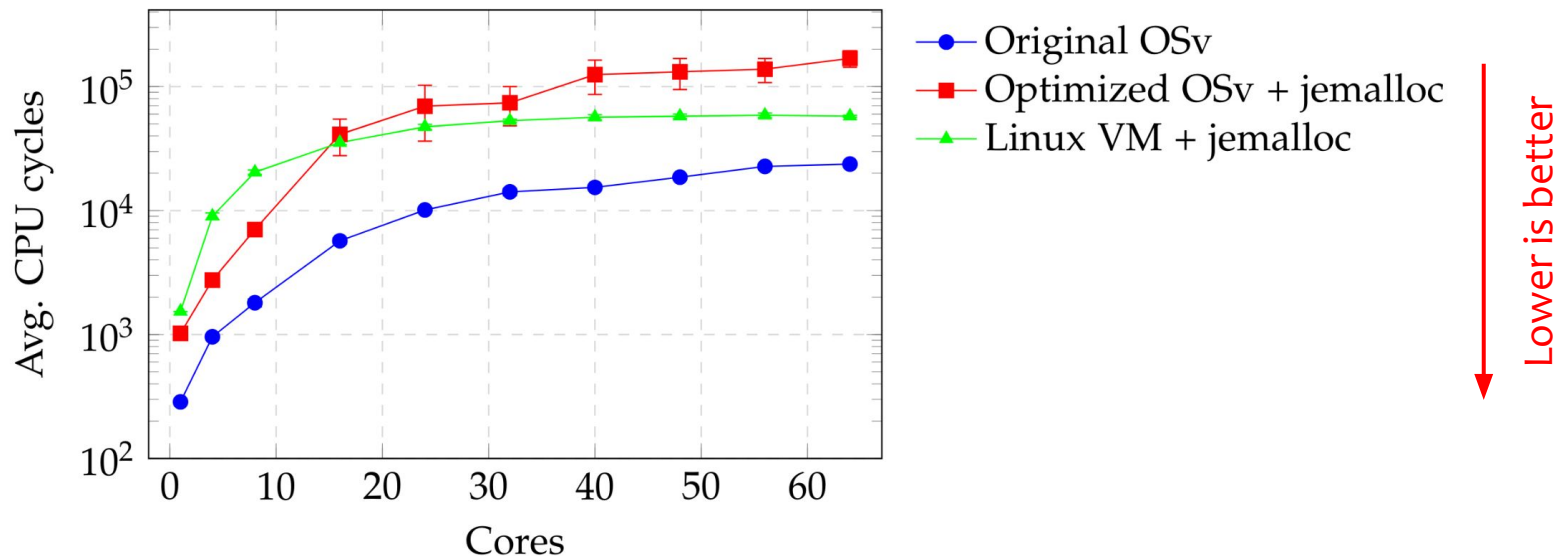
(b) Deallocation

Lower is better

Superior allocation scalability

Microbenchmarks: Random

Random deallocation | 20,000 pages | Barely core-local deallocations



Limited scalability for highly shared workloads

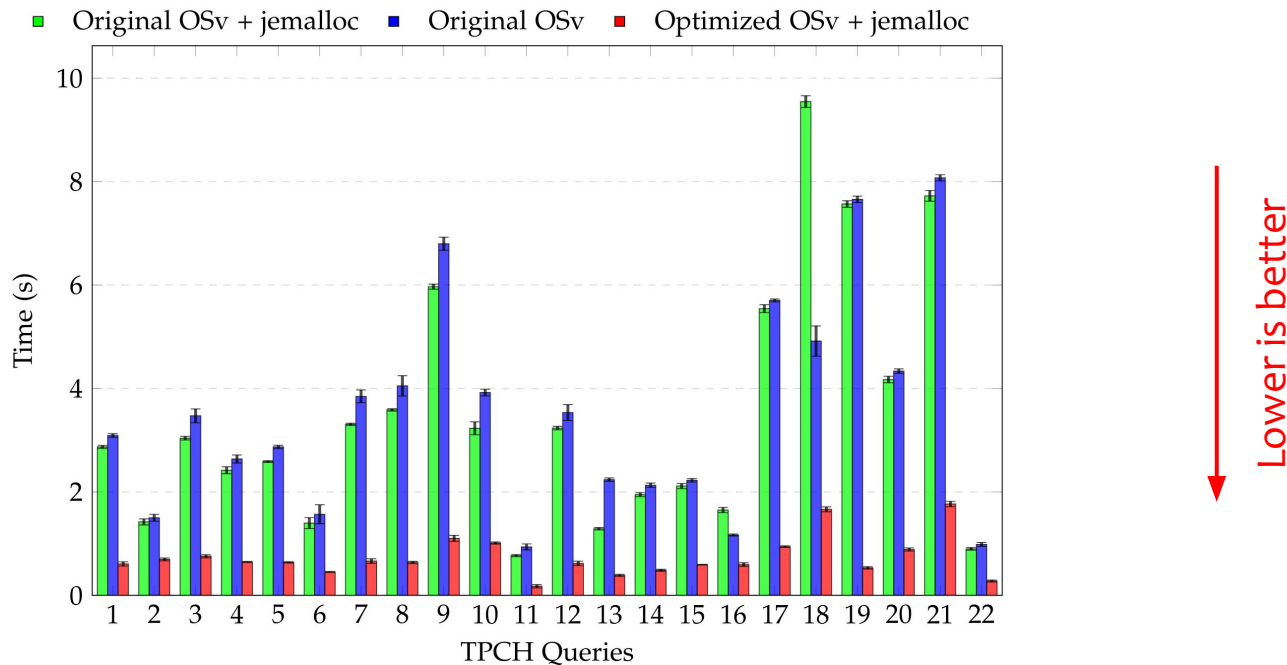
Competitors

- Linux virtual machine +jemalloc
- Original OSv version
- Original OSv version +jemalloc
- Optimized OSv version +jemalloc

TPC-H benchmark on DuckDB

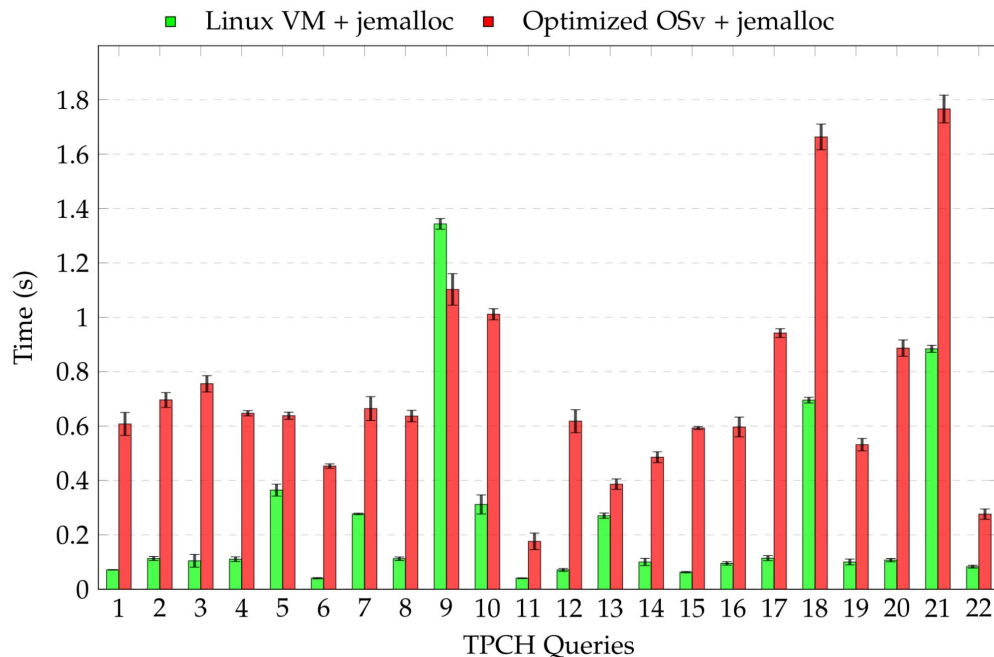
- How does our implementation perform for database workloads?

Database workloads: OSv comparison



Superior OLAP performance for all TPC-H queries

Database workloads: Linux comparison



Lower is better

Narrowing OLAP performance gap to Linux

Proposition: Design a new VM subsystem for unikernels to efficiently expose kernel functionality to DBMS

Result: We created a platform for general DBMS/OS co-design that offers

- **Flexible** use of kernel functionality due to Kernel API
- Linux **compatibility** due to maintained POSIX interfaces
- A **scalable** memory subsystem due to unikernel optimizations
 - Narrowing performance gap to linux without Kernel API utilization

Implementation and Benchmarks:

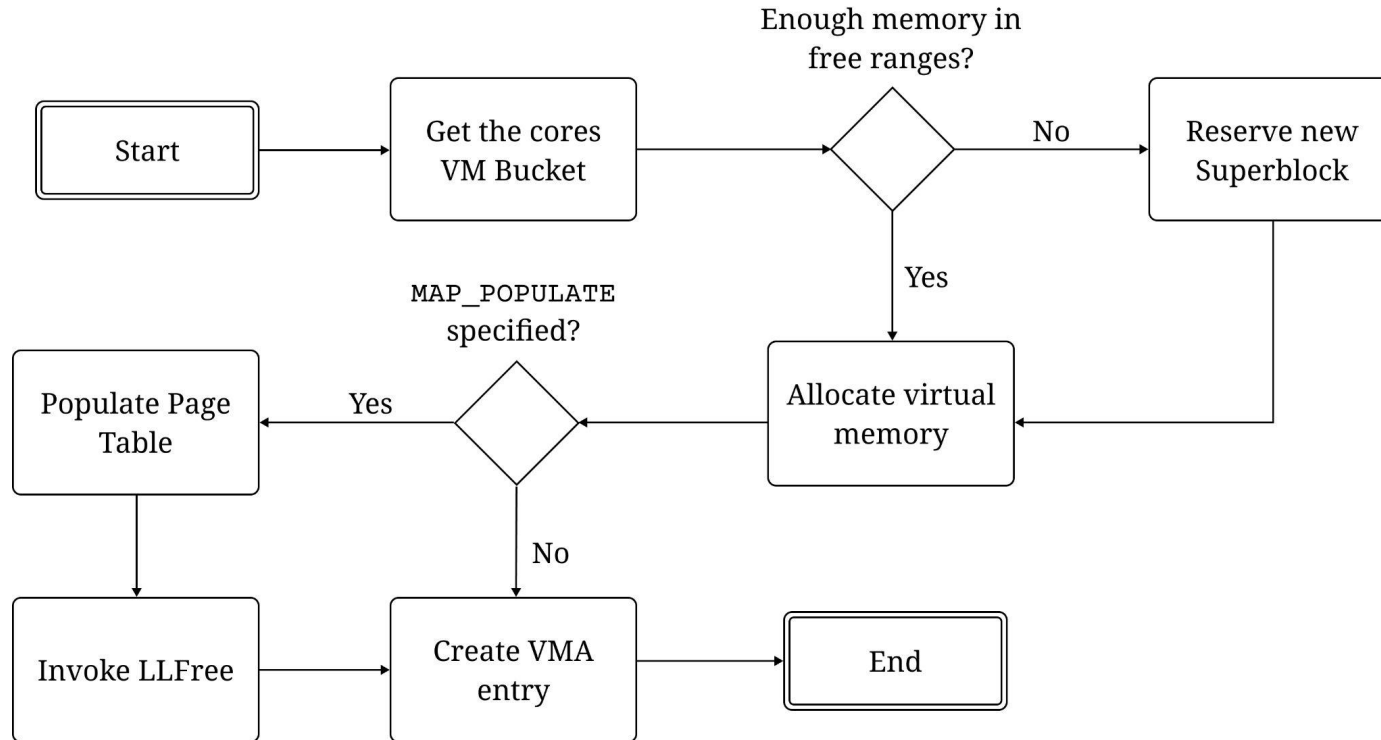
https://github.com/Martin-Lndbl/osv_controller

Questions?

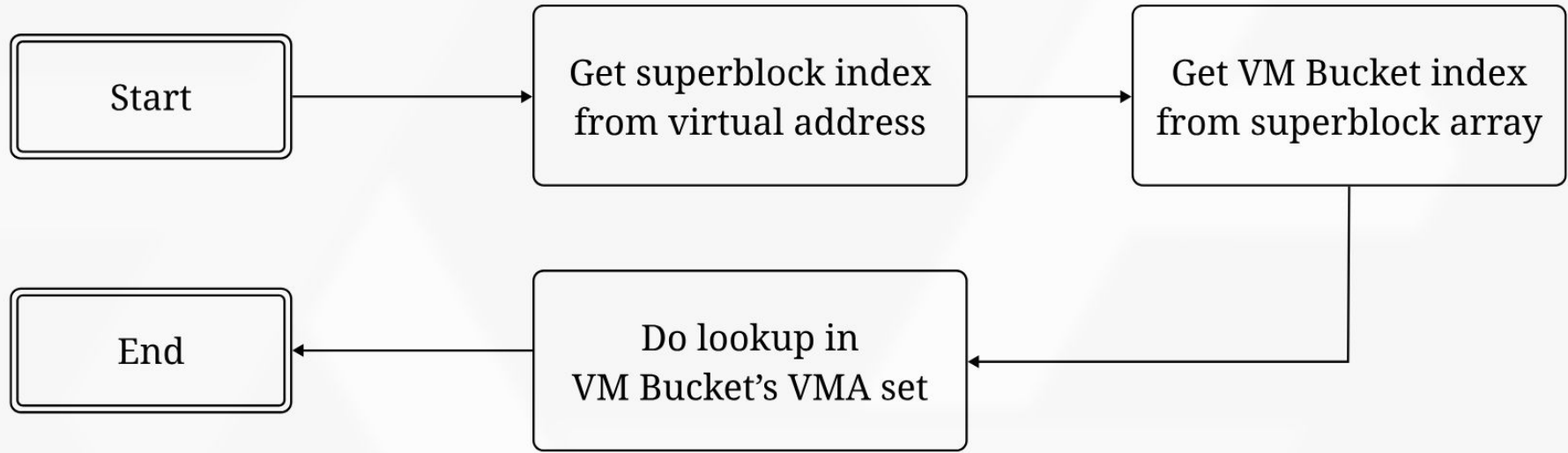
Backup

Kernel API table

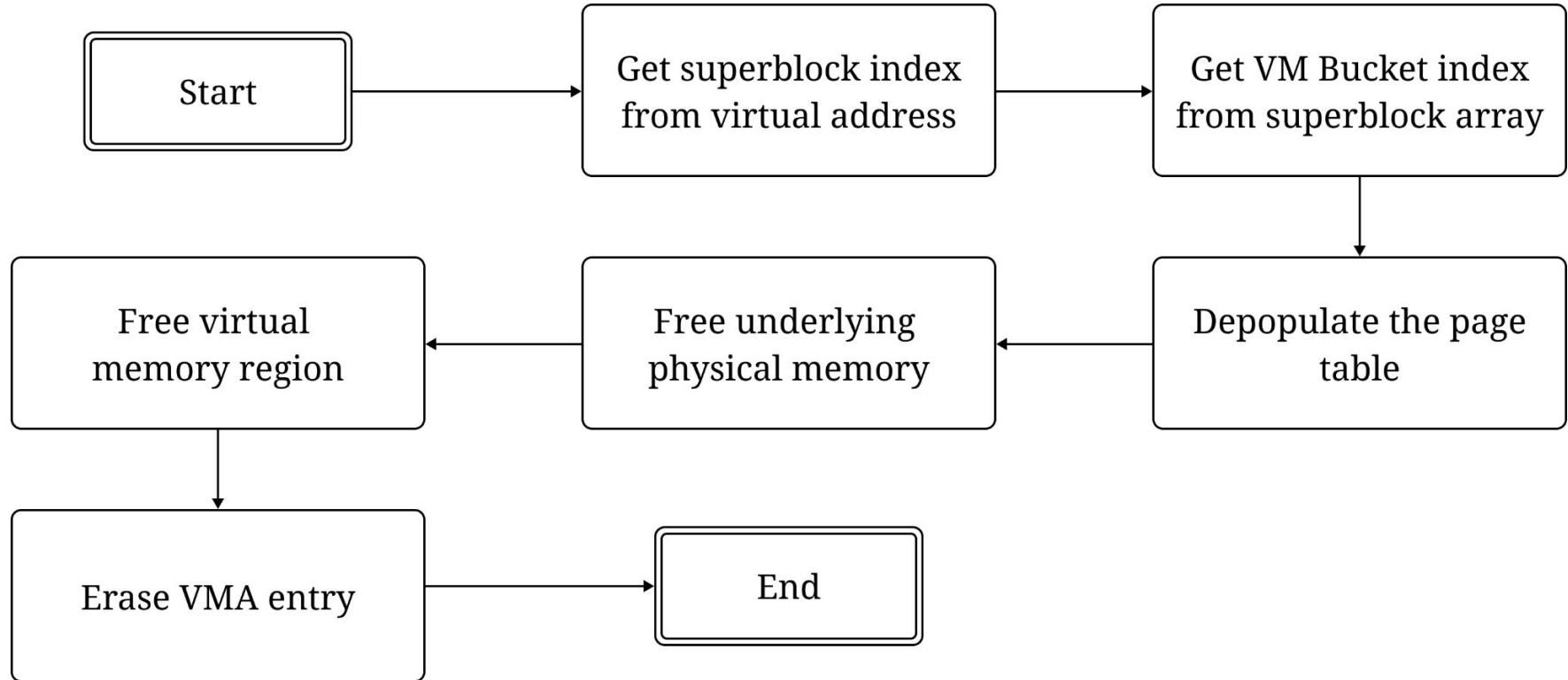
Interface	Description
<code>void* frames_alloc(order)</code> <code>void frames_free(addr, order)</code>	Allocate physically contiguous memory of the specified order and return the linearly mapped address. Free the physically contiguous memory represented by the linearly mapped address.
<code>u64 stat_free_phys_mem()</code> <code>u64 stat_total_phys_mem()</code>	Get the amount of free physical memory. Get the amount of total physical memory.
<code>void flush_tlb_all()</code> <code>mutex& pt_high_mutex()</code> <code>rwlock_t& vma_lock(addr)</code> <code>rwlock_t& free_ranges_lock(addr)</code>	Flushes all TLBs in the system. Returns the mutex for higher page table levels for modifications of linear mappings. Returns the read-write lock for an address range containing the VMA intersecting with the specified address. Returns the read-write lock for the memory range set containing the address.
<code>void insert(vma)</code> <code>void erase(vma)</code> <code>optional<vma*></code> <code>find_intersecting_vma(addr)</code> <code>vector<vma*></code> <code>find_intersecting_vmas(addr, size)</code>	Insert the VMA into the kernel's internal state. Erase the VMA from the kernel's internal state. Find the VMA intersecting with addr, if one exists. Find all VMAs intersecting with the address range specified.
<code>bool validate(addr, size)</code> <code>void allocate_range(addr, size)</code> <code>uintptr_t reserve_range(size)</code> <code>void free_range(addr, size)</code>	Test if the given region can be allocated. Allocate a given virtual memory region. Allocate any virtual memory range of the given size. Free the given virtual memory range by returning it back to OSv's free lists.



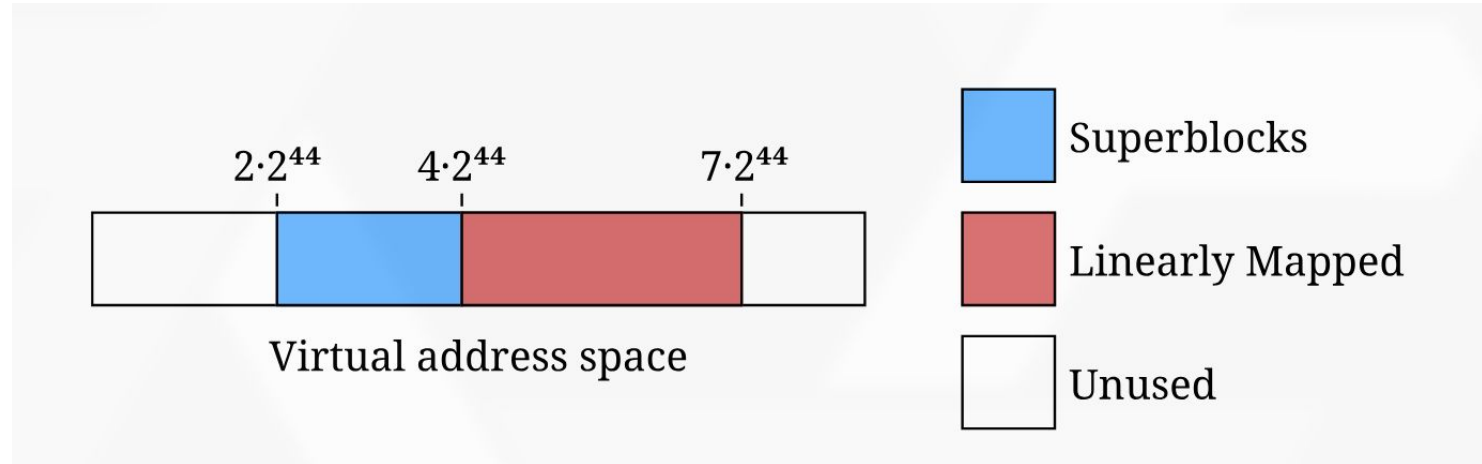
VM Area Lookup



VM Deallocation



VM Layout in OSv



Host

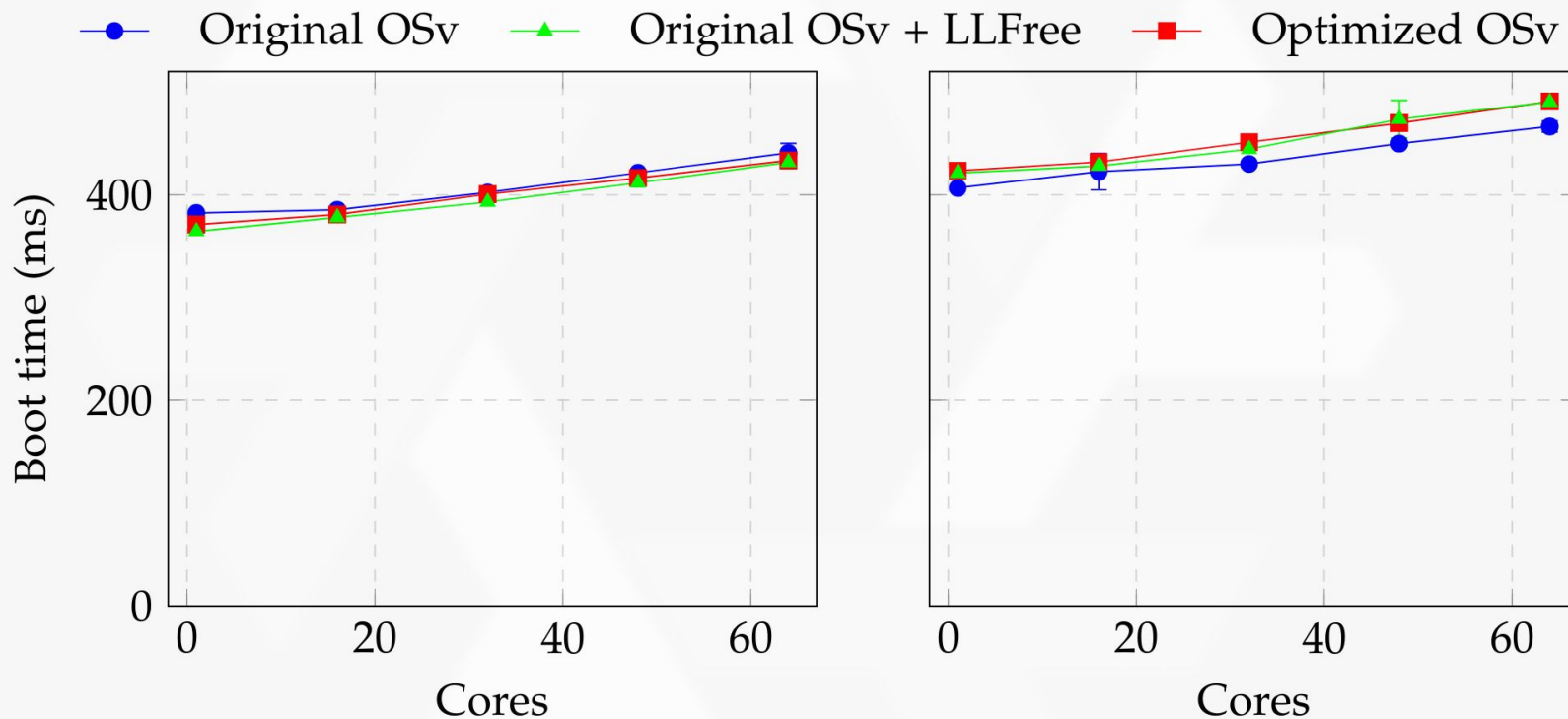
- AMD EPYC 9654P processor (96 cores @ 2.4 GHz, 192 hardware threads)
- 768 GiB DRAM
- Linux 6.12.12 / Qemu 9.2.0

Guest OS

- 60 GB DRAM
- OSv or Linux 6.1.96

Baseline: Original OSv version with native allocation mechanism

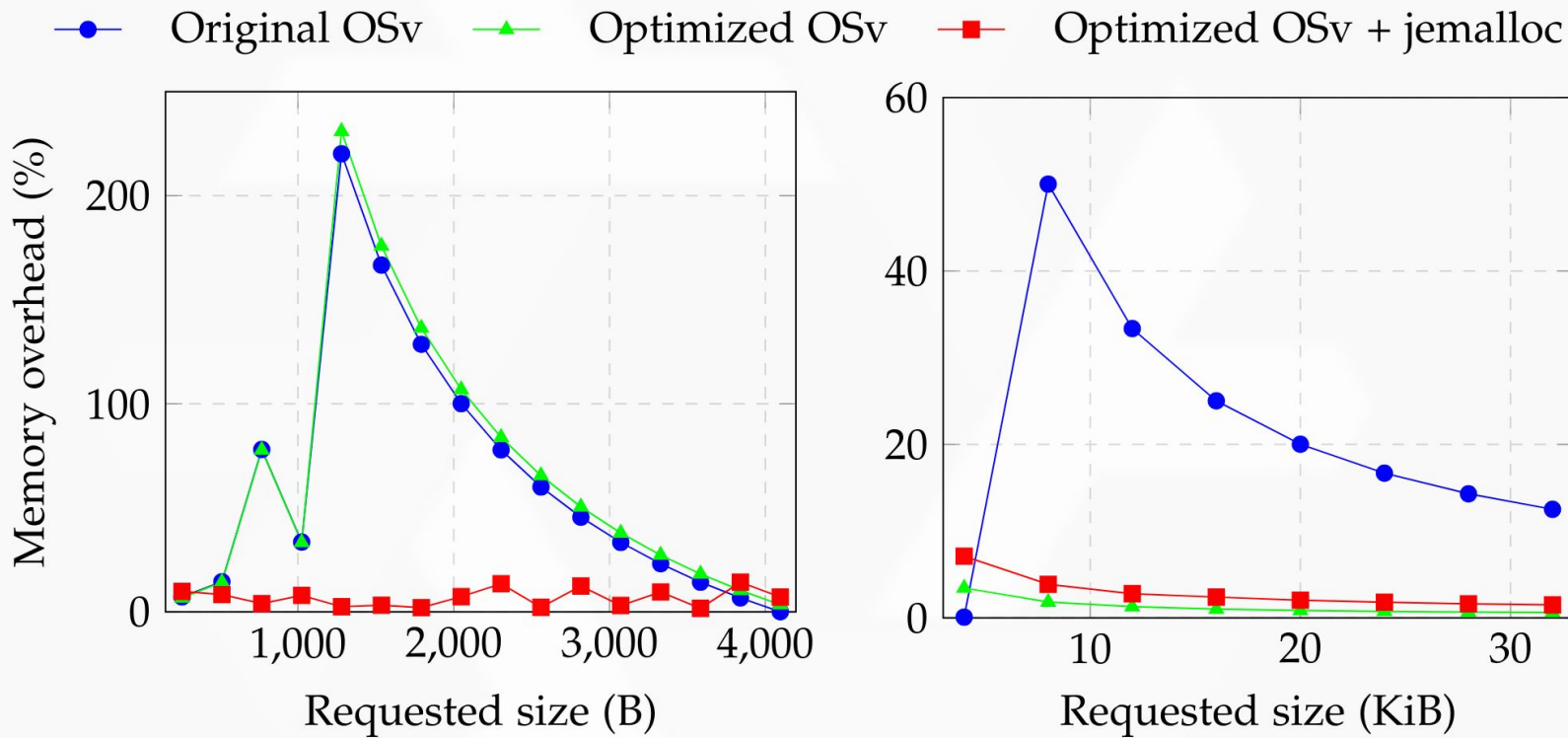
Boot Time



(a) 1GB RAM

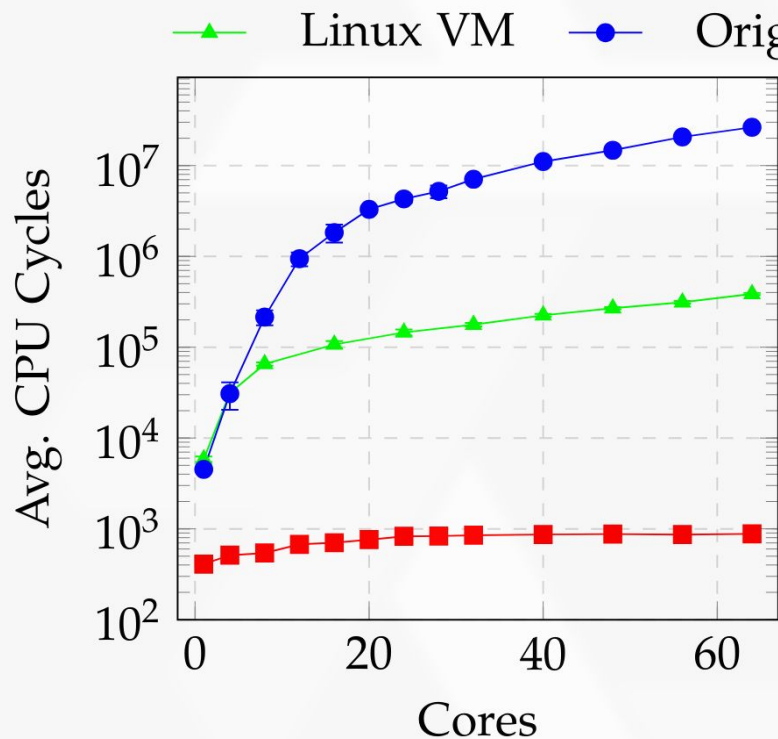
(b) 500GB RAM

Memory Footprint

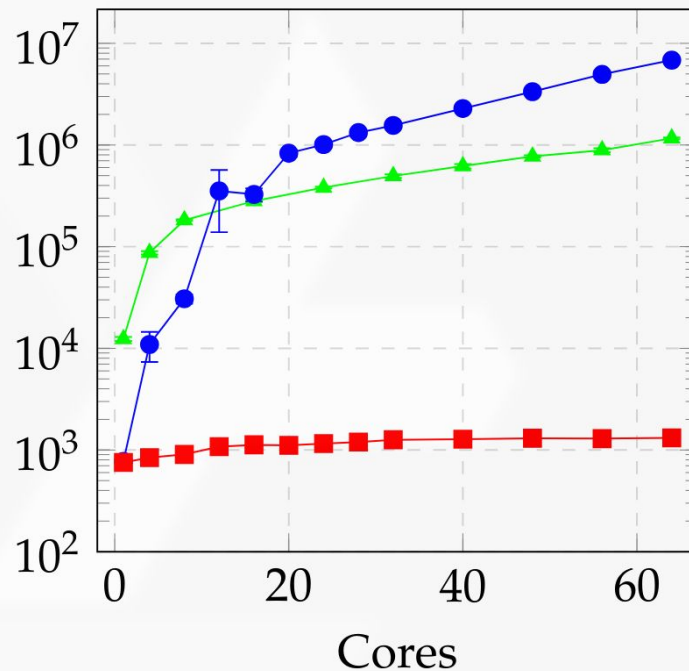


(a) Small allocations

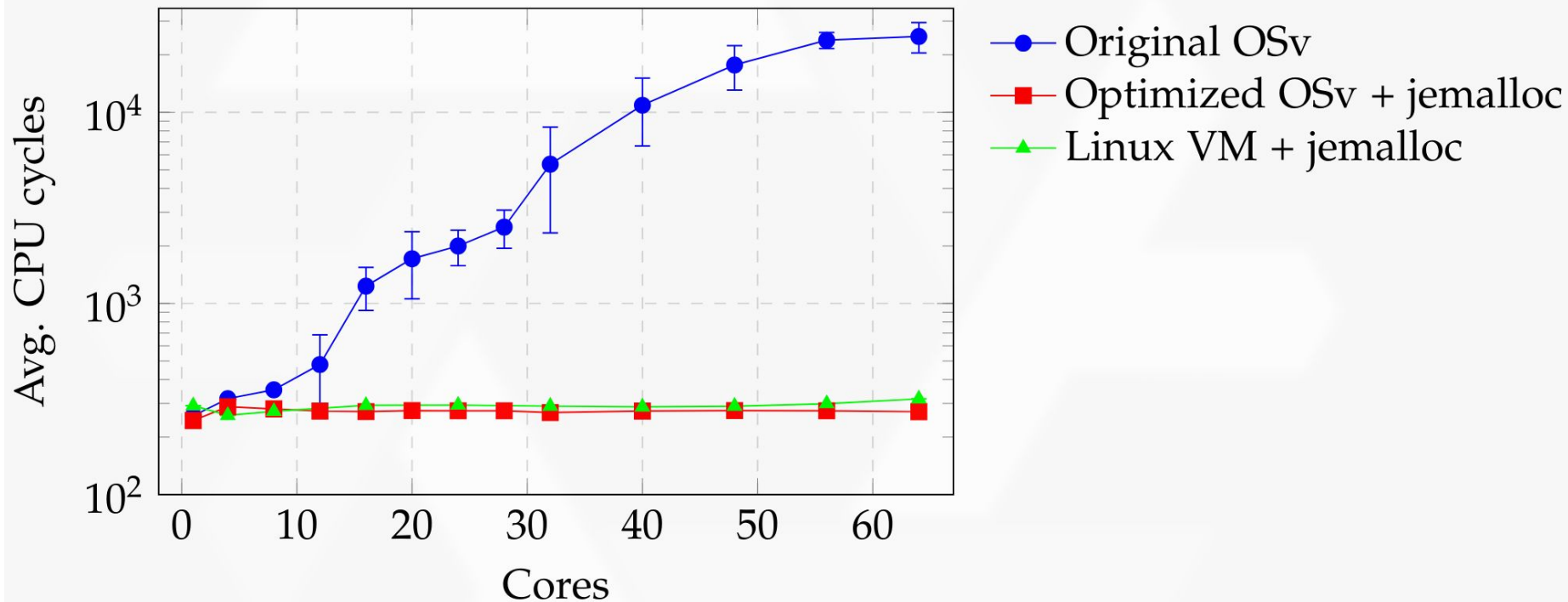
(b) Page size aligned allocations

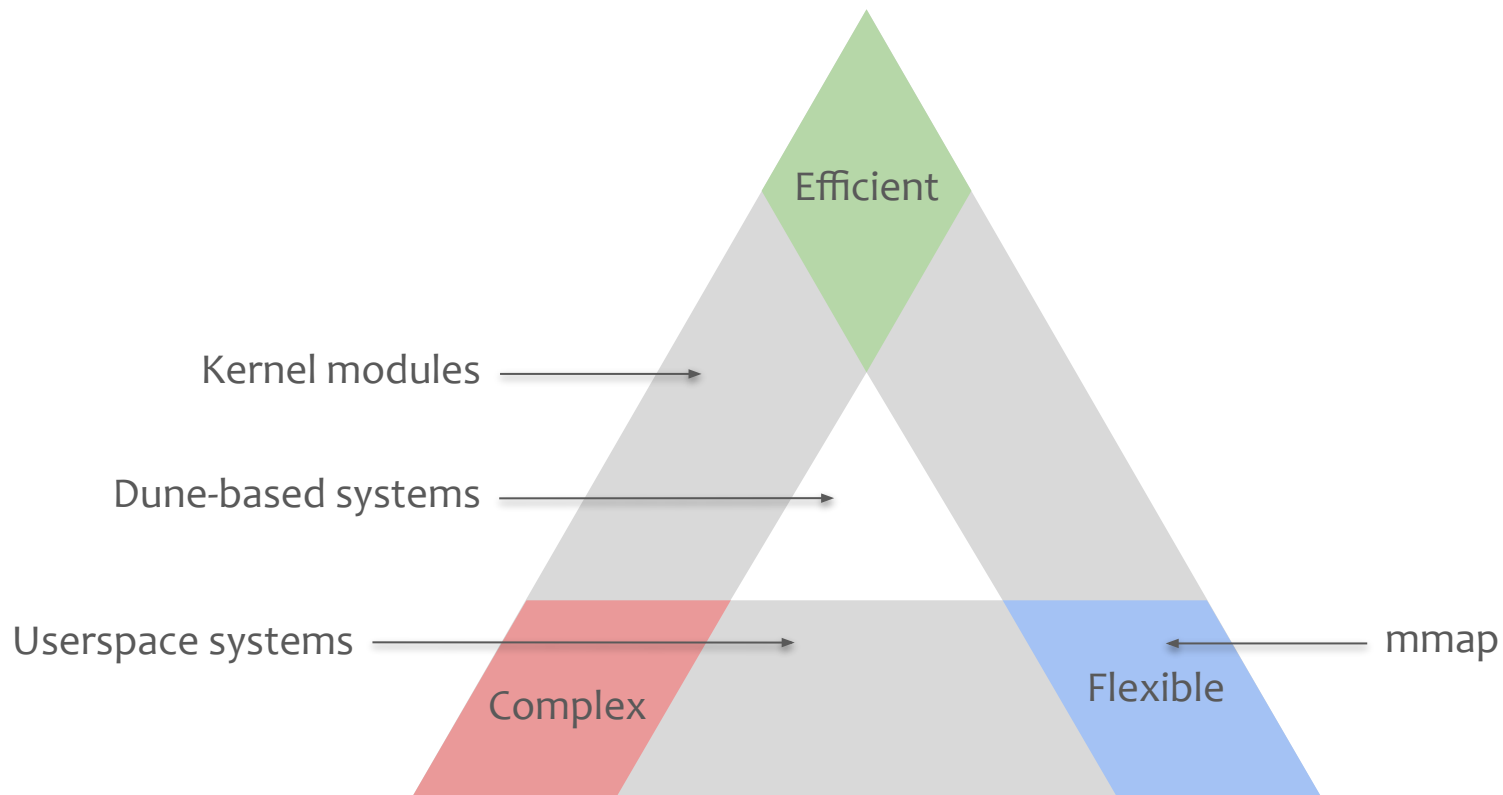


(a) Create virtual mapping

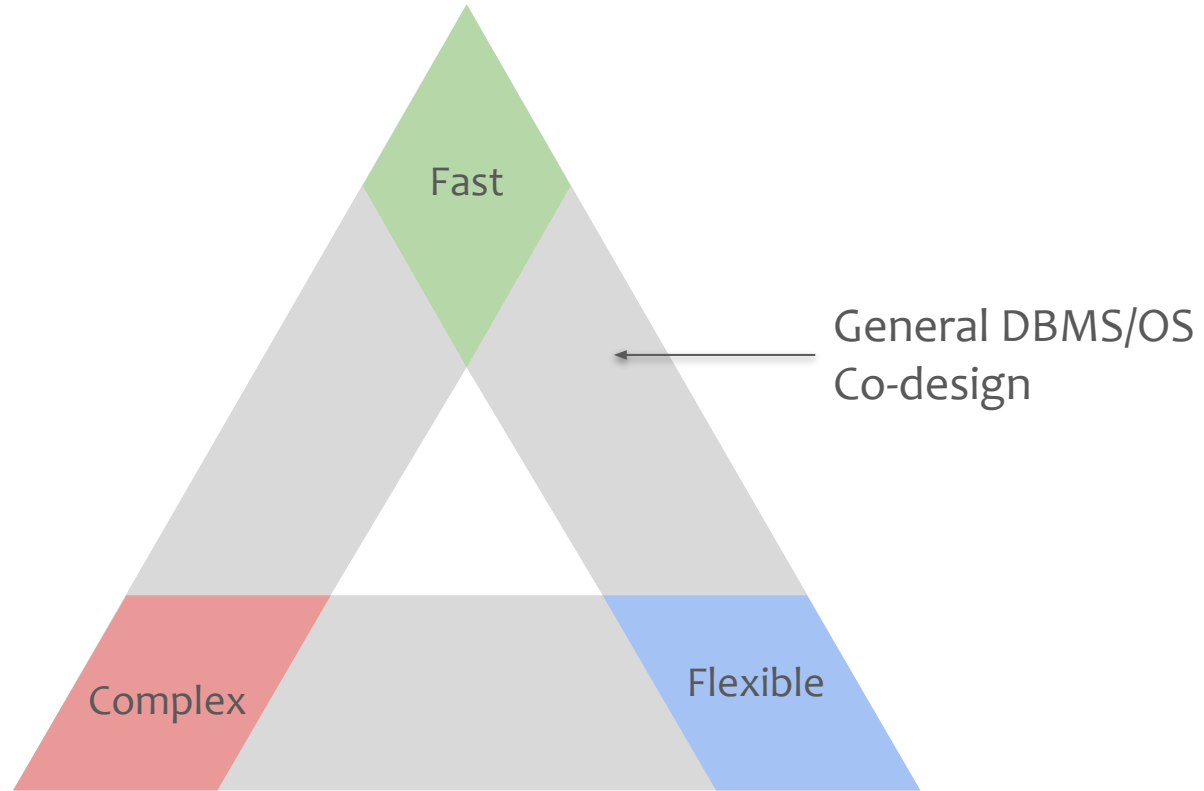


(b) Destroy virtual mapping





Research Gap



Overview: Unikernel/DBMS Co-design

