Chapter 6: Process Synchronization



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Chapter 6: Process Scheduling

- BackgroundThe Critical-Section Problem

- The Critical-Section Problem
 Peterson's Solution
 Synchronization Hardware
 Mutex Locks
 Semaphores
 Classic Problems of Synchronization

- Monitors Synchronization Examples Alternative Approaches





Objectives

- To introduce the critical-section problem, whose solutions can be used to ensure the consistency of shared data
- To present both software and hardware solutions of the critical-section problem
- To examine several classical process-synchronization problems
- To explore several tools that are used to solve process synchronization problems







- Processes can execute concurrently
 - May be interrupted at any time, partially completing execution
- Concurrent access to shared data may result in data inconsistency
- Maintaining data consistency requires mechanisms to ensure the orderly execution of cooperating processes
- Illustration of the problem:
 Suppose that we wanted to provide a solution to the consumer-producer problem that fills all the buffers.
 We can do so by having an integer counter that keeps track of the number of full buffers. Initially, counter is set to 0. It is incremented by the producer after it produces a new buffer and is decremented by the consumer after it consumes a buffer.





Producer

```
while (true) {  /* \  \, \text{produce an item in next produced */} 
     while (counter == BUFFER SIZE) ;
         /* do nothing */
     buffer[in] = next produced;
     in = (in + 1) % BUFFER SIZE;
     counter++;
```





Consumer

```
while (true) {
    while (counter == 0)
      ; /* do nothing */
   next consumed = buffer[out];
    out = (out + 1) % BUFFER SIZE;
    /* consume the item in next consumed */
```





Race Condition

counter++ could be implemented as

```
register1 = counter
register1 = register1 + 1
counter = register1
```

■ counter-- could be implemented as

```
register2 = counter
register2 = register2 - 1
counter = register2
```

■ Consider this execution interleaving with "count = 5" initially:

```
Solution in Schedule registers and solution in the control of the
```

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Critical Section Problem

- Consider system of n processes {p₀, p₁, ... p_{n-1}}
- Each process has critical section segment of code
 - Process may be changing common variables, updating table, writing file, etc
 - When one process in critical section, no other may be in its critical section
- Critical section problem is to design protocol to solve this
- Each process must ask permission to enter critical section in entry section, may follow critical section with exit





Critical Section

General structure of process p_i is

do { entry section critical section remainder section } while (true);







Solution to Critical-Section Problem

- 2. Progress If no process is executing in its critical section and there exist some processes that wish to enter their critical section, then the selection of the processes that will enter the critical section next cannot be postsponed indefinitely
- Bounded Waiting A bound must exist on the number of times that other processes are allowed to enter their critical sections after a process has made a request to enter its critical section and before that request is granted
 - Assume that each process executes at a nonzero speed
 - No assumption concerning relative speed of the *n* processes.
- Two approaches depending on if kernel is preemptive or non-preemptive
 - Preemptive allows preemption of process when running in kernel mode
 Non-preemptive runs until exits kernel mode, blocks, or voluntarily yields CPU
 - ▶ Essentially free of race conditions in kernel mode





Peterson's Solution

- Good algorithmic description of solving the problem
- Assume that the load and store instructions are atomic; that is, cannot be interrupted
- The two processes share two variables:

 - Boolean flag[2]
- The flag array is used to indicate if a process is ready to enter the critical section. flag[i] = true implies that process P_i is ready!





Algorithm for Process Pi

```
flag[i] = true;
  turn = j;
  while (flag[j] && turn == j);
        critical section
  flag[i] = false;
        remainder section
} while (true);
```

- Provable that
- 1. Mutual exclusion is preserved
- 2. Progress requirement is satisfied
- 3. Bounded-waiting requirement is met





Synchronization Hardware

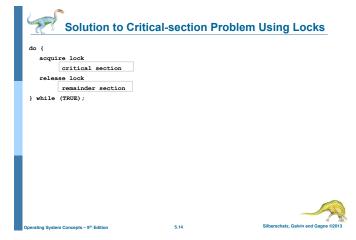
- Many systems provide hardware support for critical section code
- All solutions below based on idea of locking Protecting critical regions via locks
- Uniprocessors could disable interrupts
 - Currently running code would execute manager,
 Generally too inefficient on multiprocessor systems
 The systems using this not broadly scalated. Currently running code would execute without preemption
 - > Operating systems using this not broadly scalable
- Modern machines provide special atomic hardware instructions

 - Atomic = non-interruptible
 Either test memory word and set value
 - Or swap contents of two memory words



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test_and_set Instruction

```
boolean test_and_set (boolean *target)
     boolean rv = *target;
      *target = TRUE;
      return rv:
```







Solution using test_and_set()

- Shared boolean variable lock, initialized to FALSE
- Solution:

```
while (test_and_set(&lock))
     ; /* do nothing */
  /* critical section */
  lock = false;
  /* remainder section */
} while (true);
```





Bounded-waiting Mutual Exclusion with test_and_set

```
waiting[i] = true;
key = true;
while (waiting[i] && key)
      key = test_and_set(&lock);
   waiting[i] = false;
   /* critical section */
   j = (i + 1) % n;
   while ((j != i) && !waiting[j])
     j = (j + 1) % n;
      lock = false;
      waiting[j] = false;
   /* remainder section */
} while (true);
```





Mutex Locks

- Previous solutions are complicated and generally inaccessible to application programmers
- OS designers build software tools to solve critical section problem
- Simplest is mutex lock
- Product critical regions with it by first acquire() a lock then release() it
 - Boolean variable indicating if lock is available or not
- Calls to acquire() and release() must be atomic
 - Usually implemented via hardware atomic instructions
- But this solution requires busy waiting
 - This lock therefore called a spinlock





acquire() and release()

```
; /* busy wait */
   available = false:;
release() (
  available = true;
 acquire lock
      critical section
  release lock
     remainder section
} while (true);
```

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Semaphore

- Synchronization tool that does not require busy waiting
- Semaphore *S* integer variable
- Two standard operations modify S: wait() and signal()
 - Originally called P() and V()
- Less complicated
- Can only be accessed via two indivisible (atomic) operations

```
wait (S) {
   while (S <= 0)
     ; // busy wait
   S--:
signal (S) {
```





Semaphore Usage

- Counting semaphore integer value can range over an unrestricted domain
- Binary semaphore integer value can range only between 0 and 1
 - Then a mutex lock
- Can implement a counting semaphore S as a binary semaphore
- Can solve various synchronization problems
- \blacksquare Consider $\textbf{\textit{P}}_{1}$ and $\textbf{\textit{P}}_{2}$ that require $\textbf{\textit{S}}_{1}$ to happen before $\textbf{\textit{S}}_{2}$

S,; signal(synch); wait(synch);





Semaphore Implementation

- Must guarantee that no two processes can execute wait () and signal () on the same semaphore at the same time
- Thus, implementation becomes the critical section problem where the wait and signal code are placed in the critical section
 - Could now have busy waiting in critical section implementation
 - But implementation code is short
 - Little busy waiting if critical section rarely occupied
- Note that applications may spend lots of time in critical sections and therefore this is not a good solution







Semaphore Implementation with no Busy waiting

- With each semaphore there is an associated waiting queue
- Each entry in a waiting queue has two data items:value (of type integer)
 - pointer to next record in the list
- - block place the process invoking the operation on the appropriate waiting queue
 - wakeup remove one of processes in the waiting queue and place it in the ready queue





Semaphore Implementation with no Busy waiting (Cont.)

```
int value;
   struct process *list;
} semaphore;
wait(semaphore *S) {
   S->value--;
   if (S->value < 0) {
   add this process to S->list;
       block();
signal(semaphore *S) {
   S->value++;
   if (S->value <= 0) {
   remove a process P from S->list;
       wakeup(P);
```





Deadlock and Starvation

- Deadlock two or more processes are waiting indefinitely for an event that can be caused by only one of the waiting processes
- Let S and Q be two semaphores initialized to 1

```
wait(S);
                           wait(0);
wait(Q);
                           wait(S);
signal(S);
                          signal(Q);
signal(Q);
                          signal(S);
```

- A process may never be removed from the semaphore queue in which it is suspended

 Priority Inversion Scheduling problem when lower-priority process holds a lock needed by higher-priority process
 - Solved via priority-inheritance protocol





- - Bounded-Buffer Problem
 - Readers and Writers Problem
 - Dining-Philosophers Problem





Bounded-Buffer Problem

- n buffers, each can hold one item
- Semaphore mutex initialized to the value 1
- Semaphore **full** initialized to the value 0
- Semaphore empty initialized to the value n







■ The structure of the producer process

```
/* produce an item in next produced */
  wait(empty);
  wait(mutex);
      ^{\prime\prime} add next produced to the buffer */
   signal(mutex);
   signal(full);
} while (true);
```





Bounded Buffer Problem (Cont.)

■ The structure of the consumer process

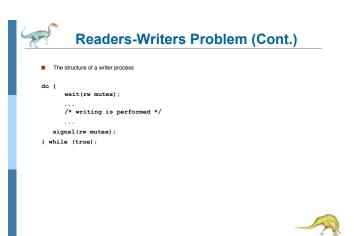
```
wait(full);
   /* remove an item from buffer to next_consumed */
signal(mutex);
signal(empty);
   /* consume the item in next consumed */
} while (true);
```

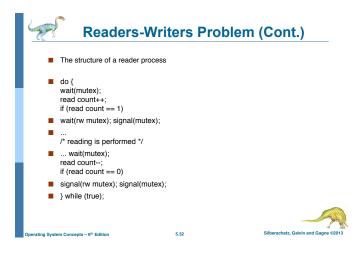


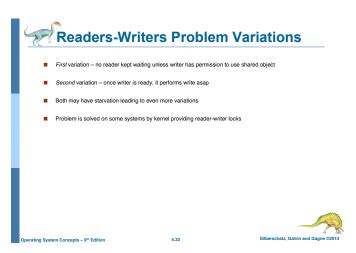
Readers-Writers Problem

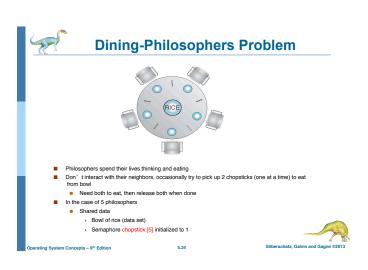
- A data set is shared among a number of concurrent processes
 - Readers only read the data set; they do not perform any updates
 - Writers can both read and write
- Problem allow multiple readers to read at the same time
 - Only one single writer can access the shared data at the same time
- Several variations of how readers and writers are treated all involve priorities
- Shared Data
 - Data set
 - Semaphore rw_mutex initialized to 1
 - Semaphore mutex initialized to 1
 - Integer read_count initialized to 0

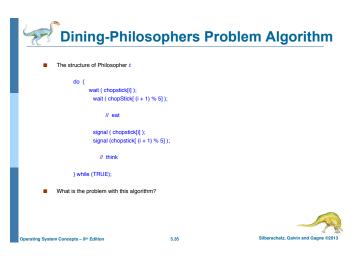


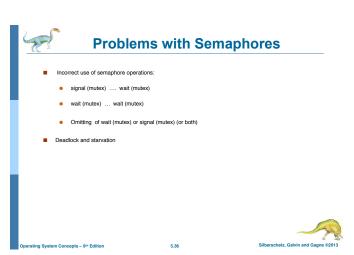














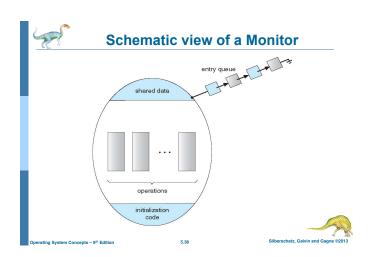
Monitors

- A high-level abstraction that provides a convenient and effective mechanism for process synchronization
- Abstract data type, internal variables only accessible by code within the procedure
- Only one process may be active within the monitor at a time
- But not powerful enough to model some synchronization schemes

```
monitor monitor-name
  // shared variable declarations
  procedure P1 (...) { .... }
  procedure Pn (...) {......}
  Initialization code (...) { ... }
```

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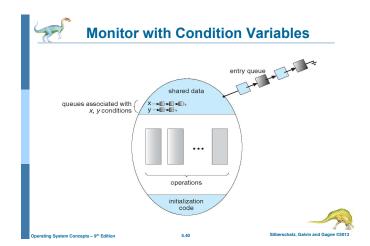


Condition Variables

- Two operations on a condition variable:
 - x.wait () a process that invokes the operation is suspended until x.signal ()
 - x.signal () resumes one of processes (if any) that invoked x.wait ()
 - If no x.wait () on the variable, then it has no effect on the variable







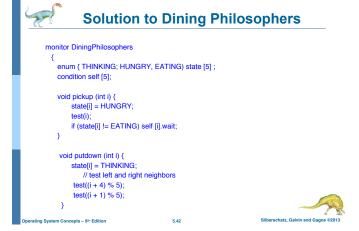


Condition Variables Choices

- If process P invokes x.signal (), with Q in x.wait () state, what should happen next?
 - If Q is resumed, then P must wait
- Options include
 - Signal and wait P waits until Q leaves monitor or waits for another condition
 - Signal and continue Q waits until P leaves the monitor or waits for another condition
 - Both have pros and cons language implementer can decide
 - Monitors implemented in Concurrent Pascal compromise
 - › P executing signal immediately leaves the monitor, Q is resumed
 - Implemented in other languages including Mesa, C#, Java





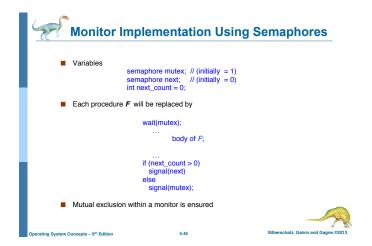


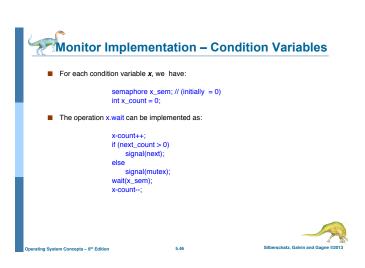
```
void test (int i) {
    if ( (state[(i + 4) % 5] != EATING) &&
        (state[i] == HUNGRY) &&
        (state[i] == EATING) } {
        state[i] == EATING;
        self[i].signal ();
        }
    }

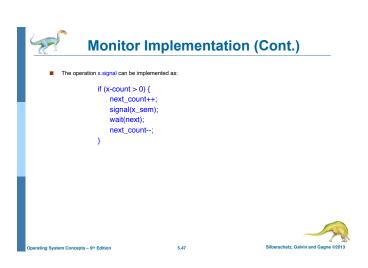
initialization_code() {
    for (int i = 0; i < 5; i++)
        state[i] = THINKING;
    }
}

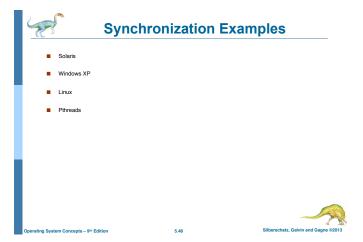
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Solaris Synchronization

- Implements a variety of locks to support multitasking, multithreading (including real-time threads), and multiprocessing
- Uses adaptive mutexes for efficiency when protecting data from short code segments
 Starts as a standard semaphore spin-lock

 - If lock held, and by a thread running on another CPU, spins
 - If lock held by non-run-state thread, block and sleep waiting for signal of lock being released
- Uses condition variables
- Uses readers-writers locks when longer sections of code need access to data
- Uses turnstiles to order the list of threads waiting to acquire either an adaptive mutex or reader-writer lock
 - Turnstiles are per-lock-holding-thread, not per-object
- Priority-inheritance per-turnstile gives the running thread the highest of the priorities of the threads in its turnstile

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Windows XP Synchronization

- Uses spinlocks on multiprocessor systems
 - Spinlocking-thread will never be preempted
- Also provides dispatcher objects user-land which may act mutexes, semaphores, events, and timers

 - An event acts much like a condition variable
 - Timers notify one or more thread when time expired
 - Dispatcher objects either signaled-state (object available) or non-signaled state (thread will block)





Linux Synchronization

- - Prior to kernel Version 2.6, disables interrupts to implement short critical sections
 - Version 2.6 and later, fully preemptive
- Linux provides:

 - spinlocks
- On single-cpu system, spinlocks replaced by enabling and disabling kernel preemption







Pthreads Synchronization

- Pthreads API is OS-independent
- - mutex locks
 - condition variables
- Non-portable extensions include:
 - read-write locks
 - spinlocks

