Spring 2015 COMP 3511 Homework Assignment #2 Handout Date: Mar 10, 2015 Due Date: Mar 24, 2015

	N	lame:	ID:	E-Mail:
Ple	ease :	read the followin	g instructions care	fully before answering the questions:
•	The Who	ere are a total of 4 en you write your in your name, stu	answers, please try dent ID, email at the	to be precise and concise.
•	coll		he collection boxes	s required and the homework is collected in locate outside Room 4210, near lift 21 (there
1.	(20	points) Multiple	choices	
	1)	a multithreaded	process?	of program state are shared across threads in ③Global variables ④Stack memory
		A) 12 B) 23 C) 14 D) 34		
	2)		X, increments it, and	ele X to zero, then spawns three threads, each d stores the result back. What is the value of
		A) X = 1 B) X = 3 C) X = 1 or X = D) X = 1 or X =		
	3)	Which of the fol	lowing is the drawb	ack of the One to One Model?
		B) Decreased co C) Creating so n		

4)	Cancelling a thread asynchronously
	 A) spoils the process execution B) may not free each resource C) frees all the resources properly D) allows the target thread to periodically check if it should be cancelled
5)	According to Amdahl's Law, what is the speedup gain for an application that is 40% parallel and we run it on a machine with 4 processing cores?
	A) 0.7 B) 1.90 C) 1.43 D) 0.525
6)	Which of the following scheduling algorithms could result in starvation? ①First-come, first served ②Shortest job first (non-preemptive) ③Round Robin ④Priority
	A) ①② B) ②④ C) ①③ D) ③④
7)	In multilevel feedback scheduling algorithm,
	A) a process can be moved to a different classified ready queue.B) classification of ready queue is permanentC) processes are not classified into groupsD) None of the mentioned.
8)	Dispatch latency is
	A) the speed of dispatching a process from running to the ready state B) the time of dispatching a process from running to ready state and keeping the CPU idle C) the time to stop one process and start running another one D) None of the mentioned.
9)	LWP is
	 A) short for lightweight processor B) placed between user and kernel threads C) placed between system and kernel threads D) common in systems implementing one-to-one multithreading models

- 10) Thread-local storage is data that
 - A) is unique to each thread
 - B) has been modified by the thread, but not yet updated to the parent process
 - C) is generated by the thread independent of the thread's process
 - D) is not associated with any process
- 2. (15 points) Please answer the following question briefly.
 - 1) (5 points) How is clone() used in Linux in comparing with fork()?

2) (10 points) Consider the following code segment:

How many unique processes are created? How many unique threads are created? Please briefly explain your answer.

3.	(20	points) Please answer the following question briefly.
	1)	(5 points) What are the two primary reasons for introducing threads?
	2)	(5 points) Please name three distinctive parameters of threads within a process? Please explain that a process does not have its own state if the process has multiple threads?
	3)	(3 points) How is a dispatcher related to and different from the short-term scheduler?

4)	(5 points)	What is the difference	between response	time and	turnaround time?
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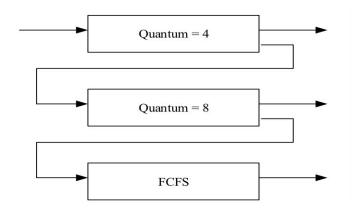
4. (45 points) CPU Scheduling

1) (15 points) Given the arrival time and CPU-burst of 5 processes shown in the following diagram:

Process	Arrival Time (ms)	Burst Time (ms)
P1	0	10
P2	2	15
P3	5	2
P4	12	14
P5	18	6

Suppose the OS uses a 3-level feedback queue to schedule the above 5 processes. Round-Robin scheduling strategy is used for the queue with the highest priority and the queue with the second highest priority, but the time quantum used in these two queues is different. First-come-first serve scheduling strategy is used for the queue with the lowest priority. The scheduling is **preemptive**.

(Note: In this scenario, the scheduling is preemptive, which means that the execution of the current job may be preempted by another job with **higher** priority.)



Construct a Gantt chart depicting the scheduling for the set of processes specified in the above diagram using this 3-level feedback queue.

2) (30 points) Consider the following set of processes, with the length of the CPU burst time given in milliseconds:

<u>Process</u>	Arrival Time(ms)	Burst Time(ms)
P1	0	4
P2	2	5
P3	3	2
P4	5	1
P5	7	8
P6	11	4

- a) (16 points) Draw four Gantt charts that illustrate the execution of these processes using the scheduling algorithms listed below:
- (i) FCFS
- (ii) SJF (non-preemptive)

(iii) Preemptive priority (a smaller priority number implies a higher priority), with the priorities listed here:

Priority
3
4
2
1
5
3

(iv) RR (quantum = 4)

b) (6 points) What is the turnaround time of each process for each of the scheduling algorithms in part a?

<i>Turnaround time</i> (ms)	P1	P2	P3	P4	P5	P6
FCFS						
SJF						
Preemptive priority						
RR						

c) (6 points) What is the waiting time of each process for each of these scheduling algorithms in part a?

Waiting time (ms)	P1	P2	P3	P4	P5	P6
FCFS						
SJF						
Preemptive priority						
RR						

d) (2 points) Which of the algorithms results in the minimum average waiting time (over all processes) mentioned above?