



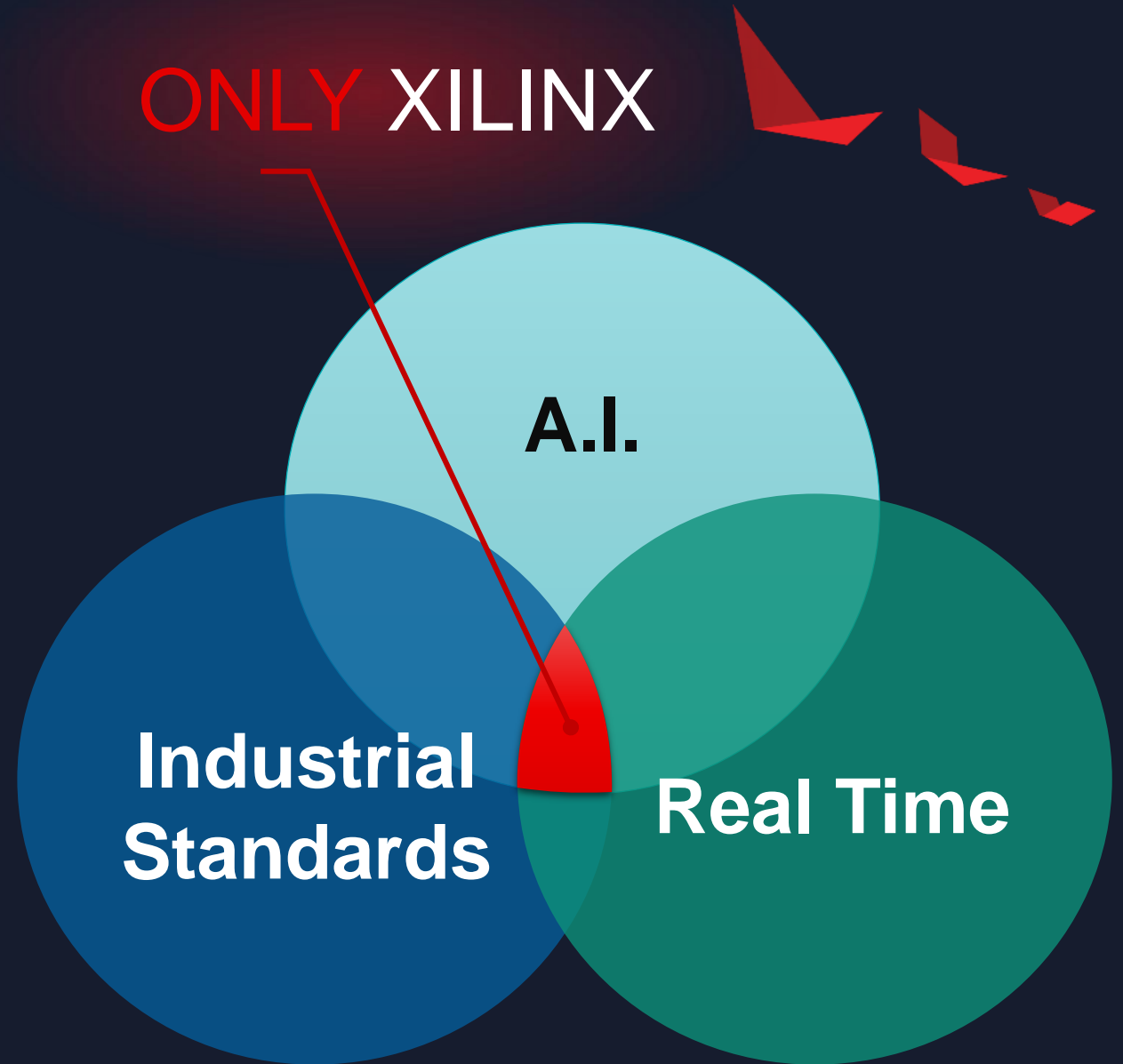
All Aboard with Xilinx: Exploring the Future of Trains and Railways

Webinar
23-Jun-2020

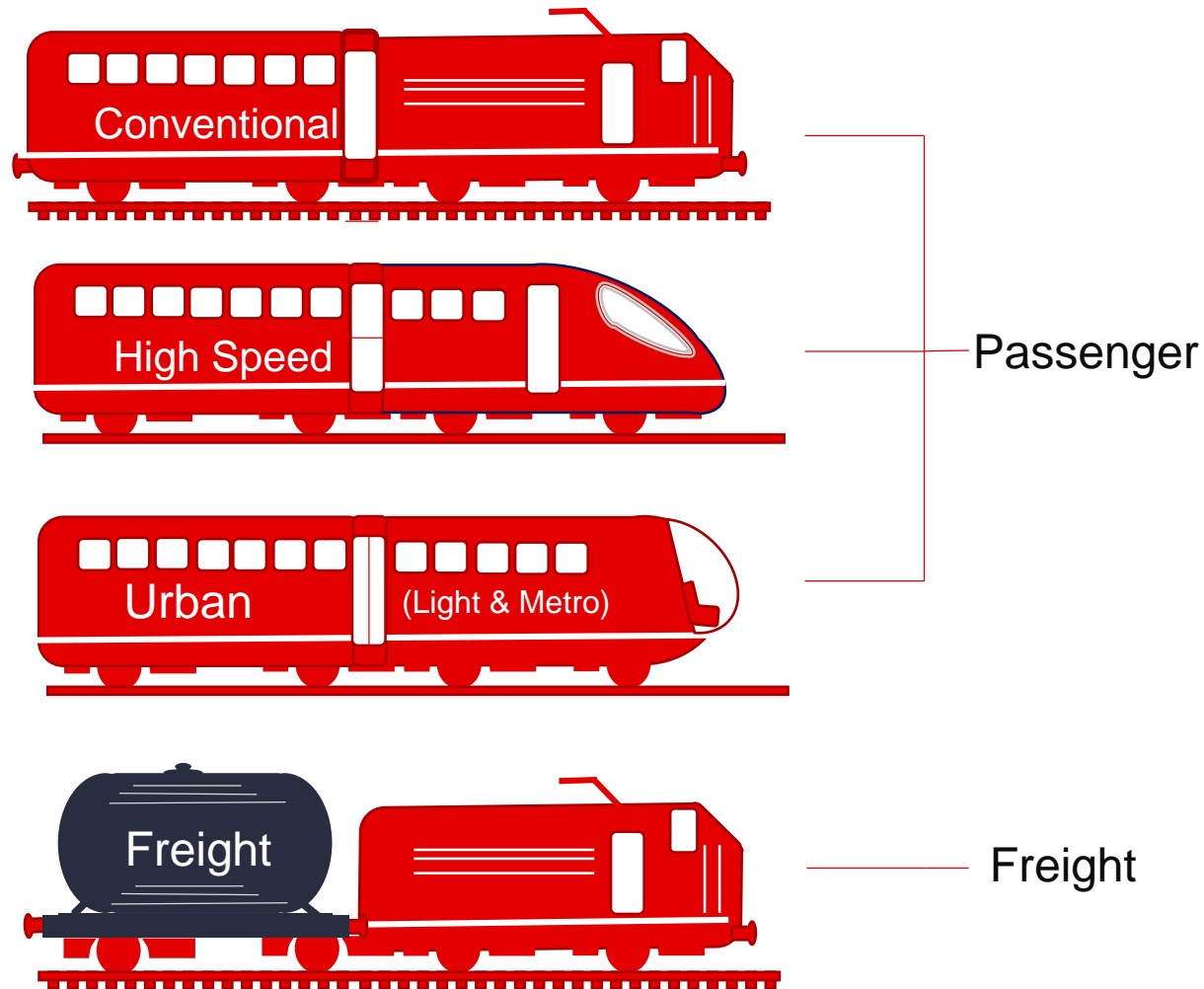


Value add with Xilinx SoCs for Trains&Railways

Giulio Corradi, Xilinx
Principal Architect
Industrial, Vision, Healthcare & Sciences



Rail challenges and requirements



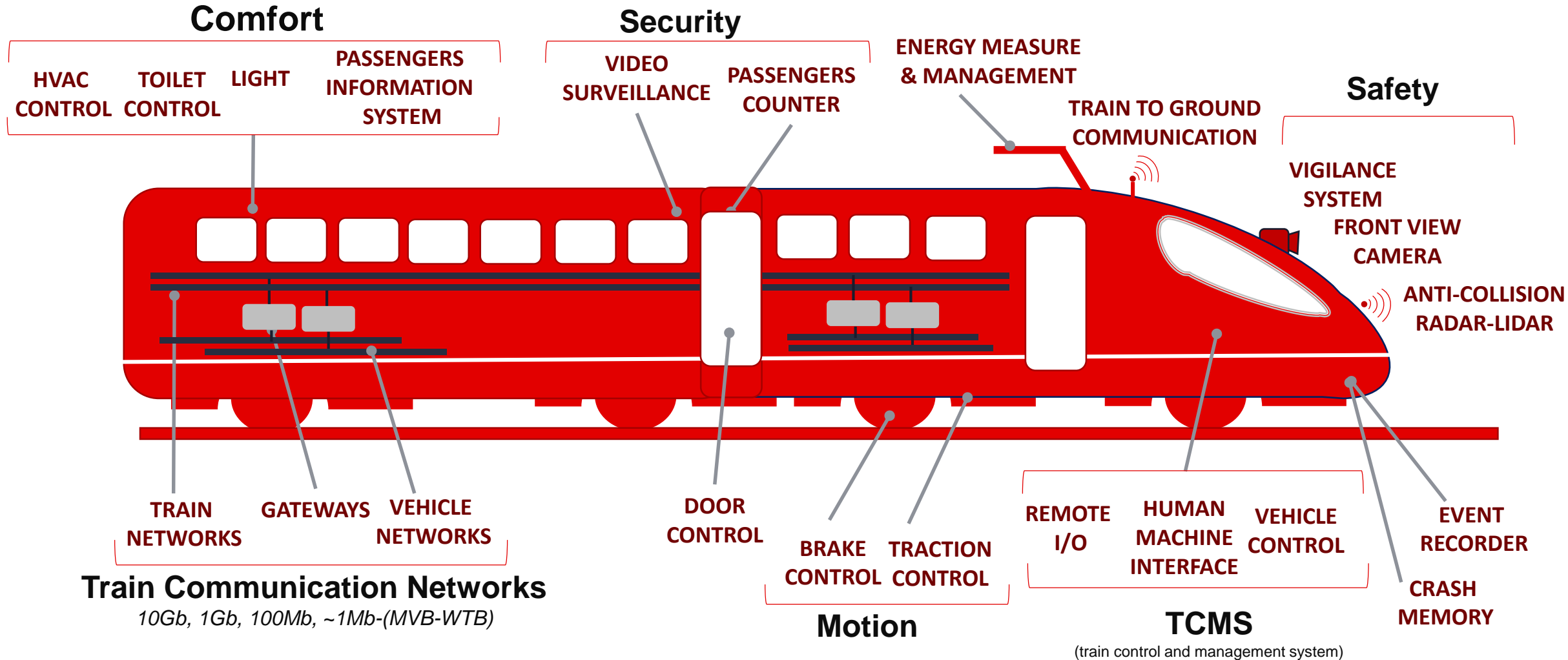
Infrastructure
(Signalling and Tracks and Stations)

The 3 M of Rail

- ▶ Minimising costs per
 - Passenger-kilometer
 - Tonne-kilometer
 - Better energy efficiency
- ▶ Maximising usage
 - More passengers
 - More goods
 - Higher throughput
- ▶ Monetize
 - Infrastructure
 - Services

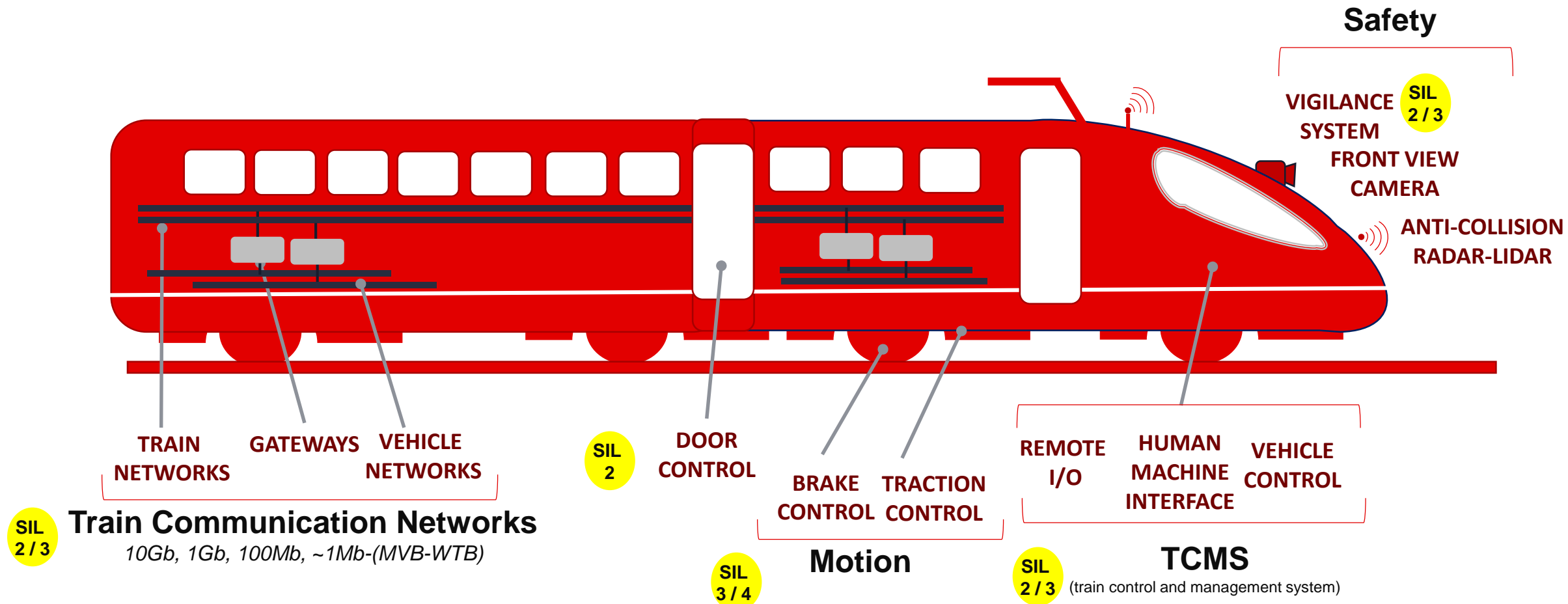
+
Automation
Digital Technologies

Train automation & functions

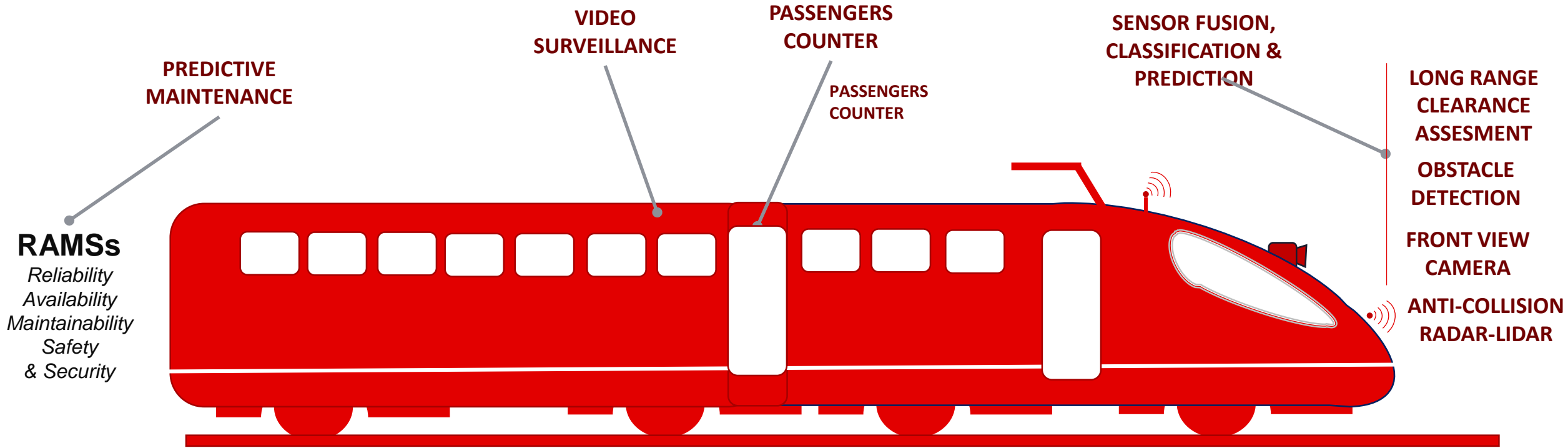


RAMS Requirements

Reliability Availability Maintainability Safety & Security



A.I. and Machine Learning for Rolling Stock



Rail Electronic Equipment Challenges

Traditional Challenges

- ▶ Small or medium equipment quantity
 - No mass market like automotive and strong cost pressure on manufacturers
"give me 3% discount / year..."
- ▶ High variability and customization for end user
 - No standard products (depend on rolling stock builder & railway undertaker)
- ▶ Extreme long life 15...30 years
- ▶ Many Functional Safety equipment
- ▶ Regulated market & specific standards (EN50155, EN50657, EN 50126.. UIC,...)

New Challenges

- ▶ More automation
 - Old processors are insufficient for new tasks
- ▶ Digitalization and A.I.
 - Higher computing performances
- ▶ More connectivity and networking
 - Wired, Wireless, 10G, Edge-Cloud
- ▶ Long term Cybersecurity

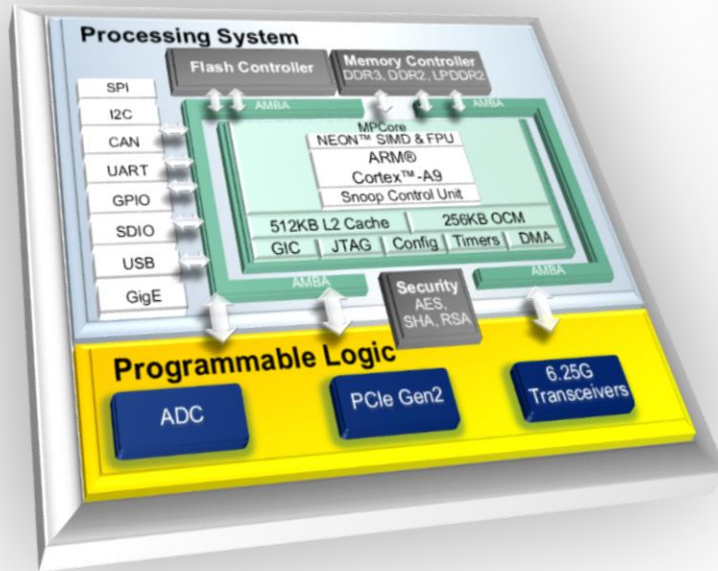
PLATFORM AS SOLUTION

A **product** is something you sell. A **platform** is common infrastructure that you build products on.

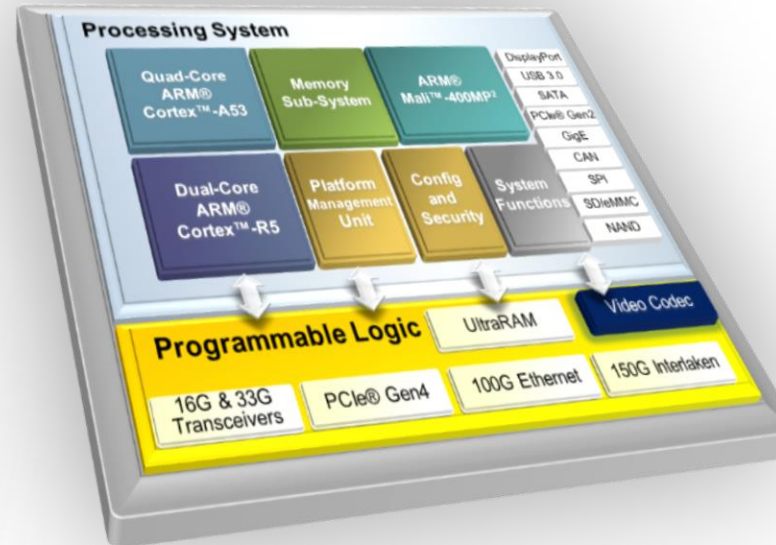
Silicon Architecture: Xilinx Platform Devices

ZYNQ®

ZYNQ®
UltraSCALE+

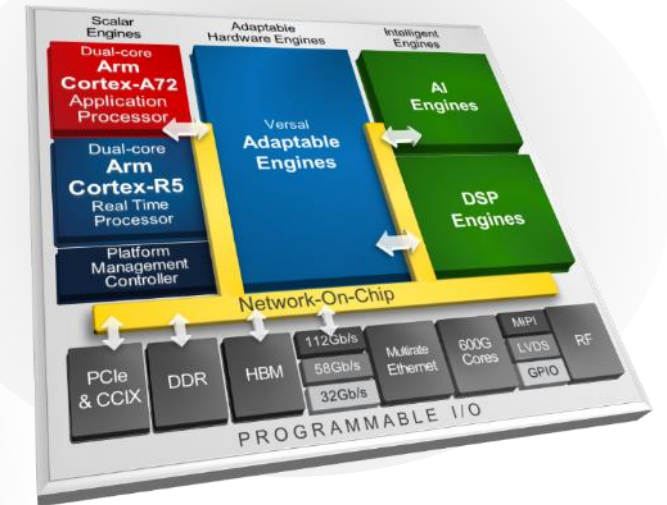


2012



2016

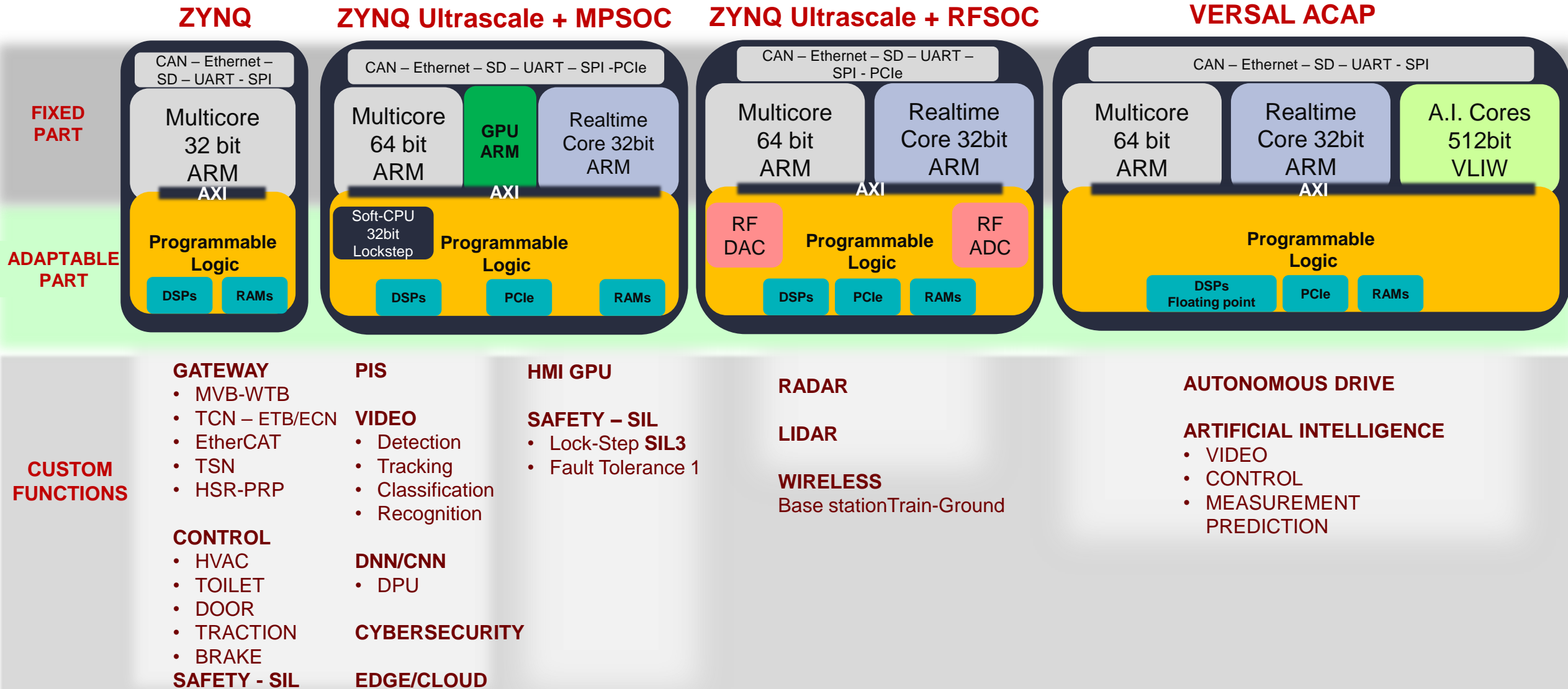
XILINX
VERSAL™



2020

Xilinx Platforms for Rail

A platform is common infrastructure that you build products on.



Vitis Unified Software Platform



Build



Embedded
Developers



Enterprise
Application Developers



Enterprise Infrastructure
Developers



Data & AI
Scientists



Deploy



Zynq-7000



Zynq UltraScale+ MPSoC



Alveo



Versal ACAPs

IEC 61508 Defines Functional Safety on SoC Level



MACHINERY



PROCESS
INDUSTRY



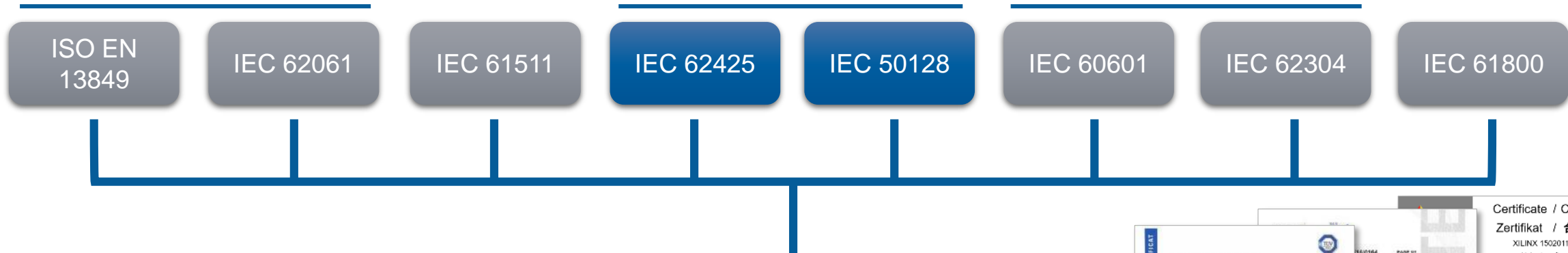
RAILWAY



MEDICAL



POWER DRIVE
SYSTEMS



IEC 61508

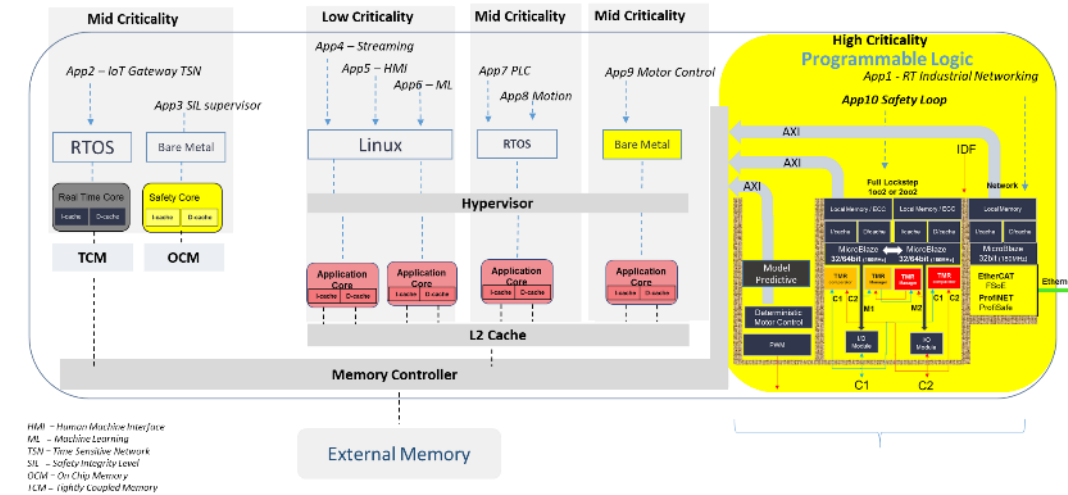
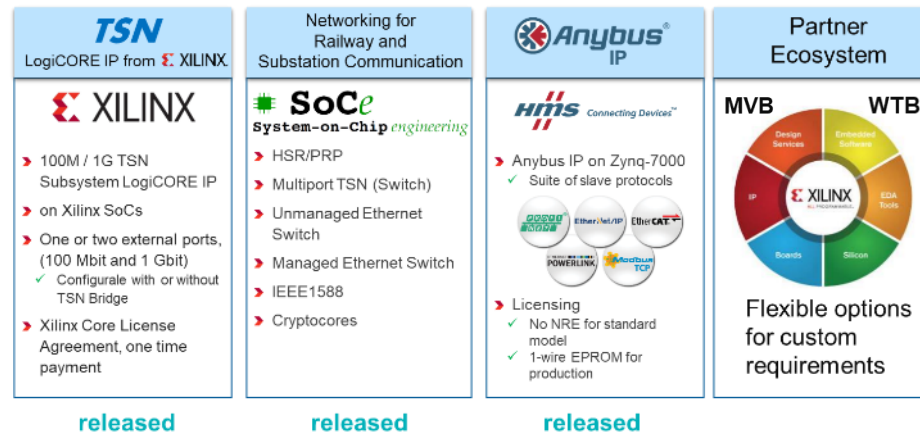
Electric/Electronic/Programmable
Electronic Functional Safety



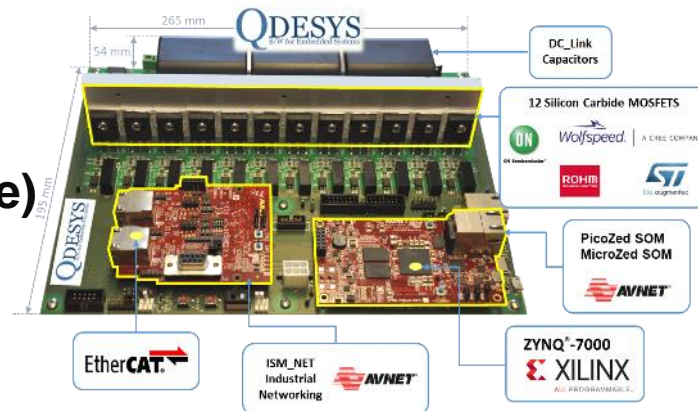
Snapshot on some ready to use solutions

MIXED CRITICALITY & SAFETY

NETWORKING



TRACTION (Silicon Carbide)



SMART CAMERA



ZYNQ-7000, SoMs, Design Kits & Reference designs



- ▶ Wide distribution of Zynq®-7000 SoC based boards/kits
 - 13,000+ boards/kits shipped between Xilinx and Avnet
- ▶ 25+ Development Boards and SoM's from Xilinx and its partners
 - Application & Market Specific Boards

Zynq® UltraScale+™ MPSoC Development Boards

Starter Kits

Ultra96-V2
Embedded Linux
Starter Kit



ZU3EG

[Available Now](#)

UltraZed-EG
Rapid Prototyping
Starter Kit



ZU3EG

[Available Now](#)

UltraZed-EV
Video Application
Starter Kit



ZU7EV

[Available Now](#)

Full Evaluation Boards

ZCU104
Embedded Vision
Platform



ZU7EV

[Available Now](#)

ZCU106
Multimedia &
Connectivity Platform



ZU7EV

[Available Now](#)

ZCU102
General Purpose
Development



ZU9EG

[Available Now](#)



Presentations from

MIOS
ELETTRONICA



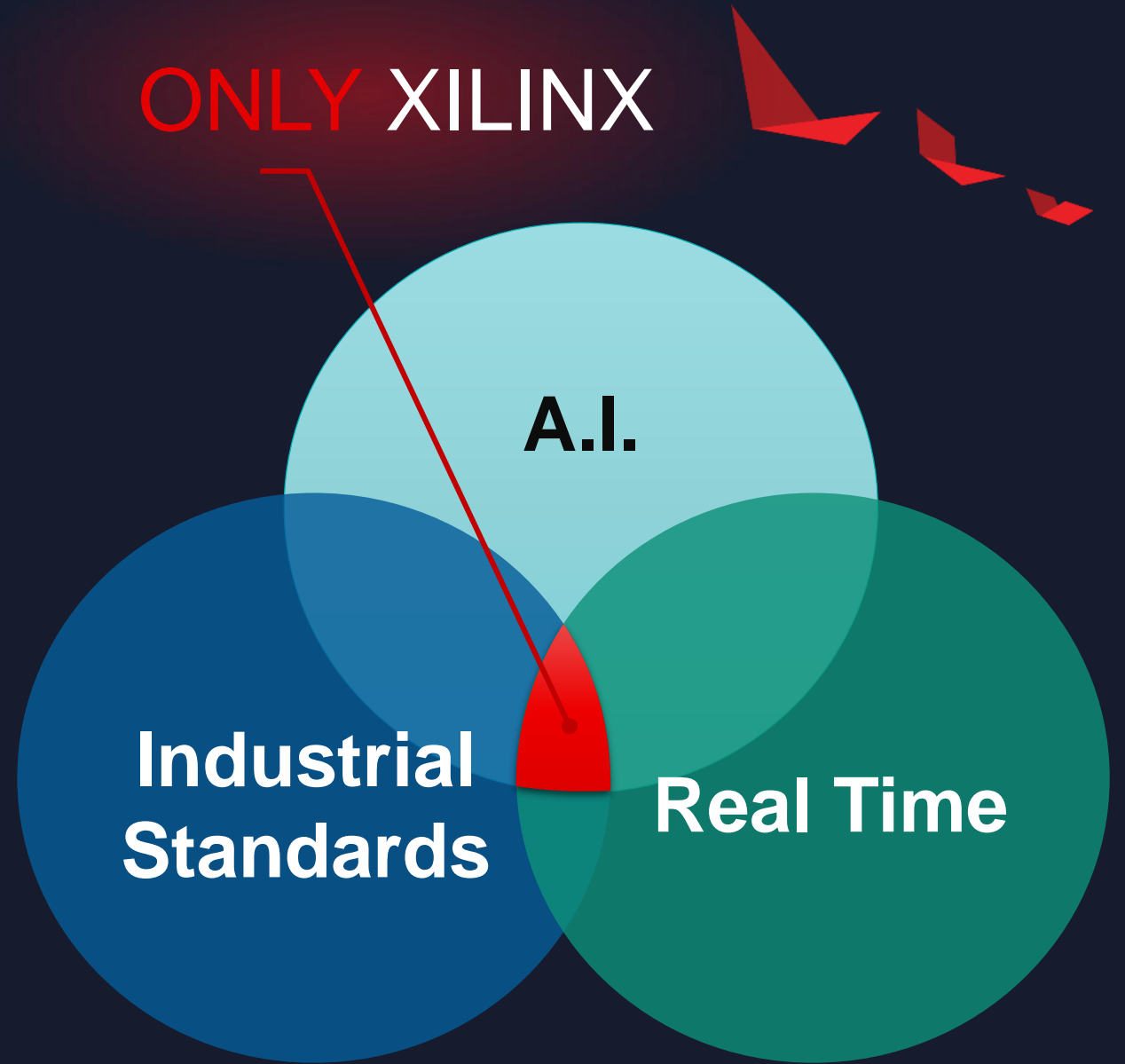
SoCe
System-on-Chip *engineering*

see separate decks

Summary

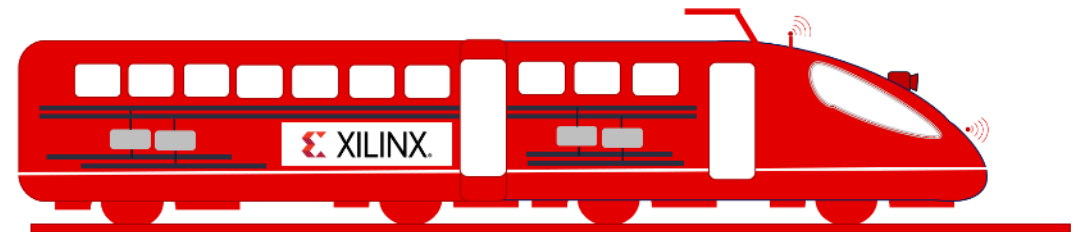
Xilinx SoCs for Trains & Railways

Giulio Corradi, Xilinx
Principal Architect
Industrial, Vision, Healthcare & Sciences



Xilinx SoCs for Trains & Railways Summary

- ▶ Differentiate with Xilinx SoCs in Rolling Stock and Wayside applications
 - Deploy Scalable Platform with Predictability, Safety, Security, Deterministic Control, Video and Artificial Intelligence
- ▶ A Zynq-7000 hardware has a proven track record in MIOS Elettronica's product development and service business
 - Full set of rugged, affordable, state of the art Railway Gateways, Controllers, Event and Data recorders
- ▶ Benefit from proven IP cores for reliable and future-proof connectivity and security
 - Creating deterministic and customizable Ethernet switch is easy.
 - SoC's IP Cores are ready for TSN, HSR, PRP, MRP, Ethernet Switching and wire-speed cryptography in hardware
- ▶ Start immediately with Development Boards
- ▶ Engage with one of the contacts on the next page



Contact us!



Via Archimede, 10 - 37036 San Martino Buon Albergo (Verona) - Italy
Phone: + 39 045 5117691 – Telefax: +39 045 5118088

info@mioselettronica.com

www.mioselettronica.com



3 Place Ville Marie, suite 400, Montreal – QC H3B 2E3 – Canada
Phone: +1 438-392-2716

info@mioselettronica.com

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Contact Information

- soc-e.com
- david.modrono@soc-e.com
- armando.astarloo@soc-e.com



ALLIANCE PROGRAM
CERTIFIED MEMBER – BASE



INDUSTRIAL INTERNET
CONSORTIUM
www.iiconsortium.org



Industrial, Vision,
Healthcare & Sciences

<https://www.xilinx.com/applications/industrial.html>

Michael Zapke michael.zapke@xilinx.com

Giulio Corradi giulio.corradi@xilinx.com

To know more see our Solutions Video Library

Edge AI and Edge to Cloud

- [Smart Camera development with Vitis & Vitis AI](#)
- [AWS Edge - to - Cloud Cooperation](#)

Safety / Cybersecurity

- [Industrial Functional Safety with Xilinx SoCs](#)
- [IEC 62443 Industrial Cybersecurity](#)

Embedded Vision and AI

- [ISP](#)
- [xfOpenCV](#)
- [Deep Dive into VITIS AI](#)
- [Accelerating AI Camera Development with Xilinx Vitis](#)

On - Premises Computing

- [Alveo for Industrial PC Acceleration](#)

Mixed Criticality & Soft Processors

- [Cache Coloring](#)
- [Xilinx MicroBlaze processors are for everyone](#)

Any - to - Any Connectivity

- [TSN LogiCORE IP](#)
- [Switches for Industrial Ethernet and TSN](#)
- [OPC UA and TSN](#)
- [DDS and TSN](#)
- [Sony SLVS - EC](#)

Smarter Control & Predictive Maintenance

- [EDDP and SPYN](#)
- [Predictive Maintenance](#)
- [SiC and Model Predictive Control](#)
- [Operator Panels / HMI](#)

Xilinx Mission

**Building the Adaptable,
Intelligent World**

