

Programa	init_pgrm	S → [init_pgrm] [id] [e_stmt] SS
;	e_stmt	SS → VG FD M
!	char	M → [main_f] [s_par] [e_par] B
(s_par	B → [s_bck] ST* [e_bck]
)	e_par	VG → [var_dec] TD &
{	s_bck	TD → [var_type] [definir] TDL1 [e_stmt] TDR
}	e_bck	TDL1 → DIMID TDL2
%% \w	—	Si no else { TDL2 → [separ] [id] TDL2 &
funcion	func	mientras while { TDR → FD &
principal	main_f	haz do { FD → [func] FTYPE [id]
var	var_dec	desde from { [s_par] PDL1 [e_par] [e_stmt]
int / float / char	var_type	hasta to { VG B F &
[s_corch	hacer do { PDL1 → [var_type] [id] PDL2 &
]	e_corch	* - op - + { PDL2 → [separ] [var_type] [id] PDL2
,	separ	* / op - + { & op - + { & &
(+ -)?[0-9]+	integer	& op - + { ST → ST DEF e_stmt ST &
:	definir	!> > = < < > = op - + { ST DEF → ASI CALL RET
=	eq	(+ -)?[0-9]+ [0-9]+(e[0-9]+)? ST DEF → ASI CALL RET
int / float / char / void	func_type	float → REF WRT DEF REP
[a-zA-Z_\$]	[a-zA-Z0-9_\$]*	id CALL → [id] [s_par] CALA [e_par]
regresa	ret	CALA → XPO CALA2 &
lee	read	CALA2 → [separ] XPO CALA2 &
escribe	write	ASI → DIMID [eq] XPO
"	comillasXD	RET → [ret] {s_par} XPO {e_par}
si	if	REF → [read] [s_par] DIMID REF - {e_par}
entonces	then	REF - → [separ] DIMID REF - &
!	simple_com	

$XP0 \rightarrow XP1 \mid XP0_$

$XP0_ \rightarrow [op-14] XP0 \mid \epsilon$

$XP1 \rightarrow XP2 \mid XP1_$

$XP1_ \rightarrow [op-13] XP1 \mid \epsilon$

$XP2 \rightarrow XP3 \mid XP2_$

$XP2_ \rightarrow [op-12] XP2 \mid \epsilon$

$XP3 \rightarrow XP4 \mid XP3_$

$XP3_ \rightarrow [op-11] XP3 \mid \epsilon$

$XP4 \rightarrow XPP \mid DIMID \mid CALL \mid char \mid integer \mid float$

$XPP \rightarrow [s-par] XP0 [e-par]$

$DIMID \rightarrow [id] DIMID_$

$DIMID_ \rightarrow [s-corch] [XP0] [s-corch] \mid \epsilon$

$WRT \rightarrow [write] [s-par] WL [e-par]$

$W.C \rightarrow STR \mid XP0$

$STR \rightarrow [comillasXD] [string] [comillasXD]$

$WL \rightarrow W.C \mid WL1$

$WL1 \rightarrow [separ] W.C \mid WL1 \mid \epsilon$

$DEC \rightarrow if [s-par] XP0 [e-par] [then] B \mid DEC_$

$DEC_ \rightarrow [else] B \mid \epsilon$

$REP \rightarrow COND \mid NCOND$

$COND \rightarrow [while] [s-par] XP0 [e-par] [do] B$

$NCOND \rightarrow [from] ASI [to] XP0 [do] B$

$FTYPE \rightarrow [var-type] \mid [void]$