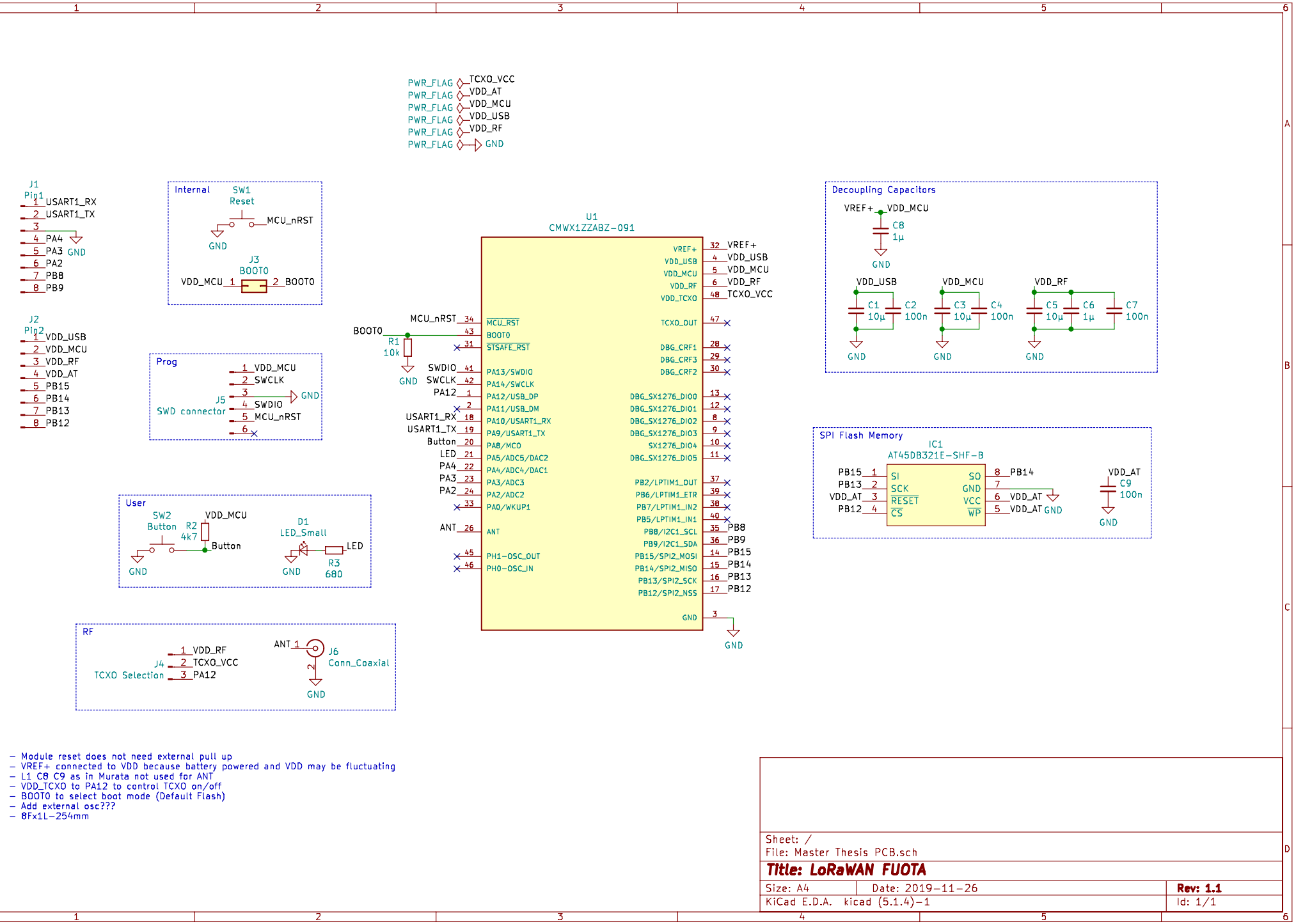

- Module reset does not need external pull up
- VREF+ connected to VDD because battery powered and VDD may be fluctuating
- L1 C8 C9 as in Murata not used for ANT
- VDD_TCXO to PA12 to control TCXO on/off
- BOOT0 to select boot mode (Default Flash)
- Add external osc???
- 8Fx1L-254mm

Sheet: /		
File: Master Thesis PCB.sch		
Title: LoRaWAN FUOTA		
Size: A4	Date: 2019-11-26	**Rev: 1.1**
KiCad E.D.A. kicad (5.1.4)-1		Id: 1/1