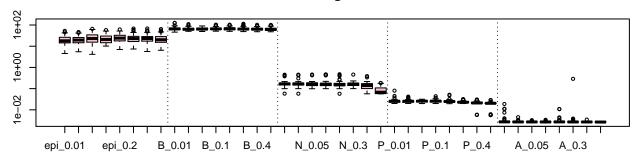
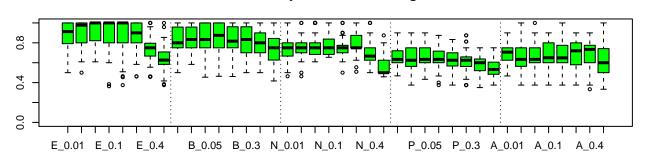
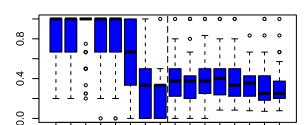
## running time



## accuracy of the inferred edges



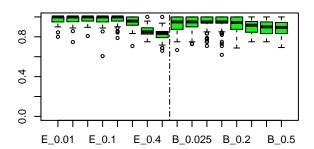


E\_0.4 B\_0.025 B\_0.2

E\_0.01

E\_0.1

accuracy of the inferred logic gate



accuracy of the inferred expected data