PISO-SIPO

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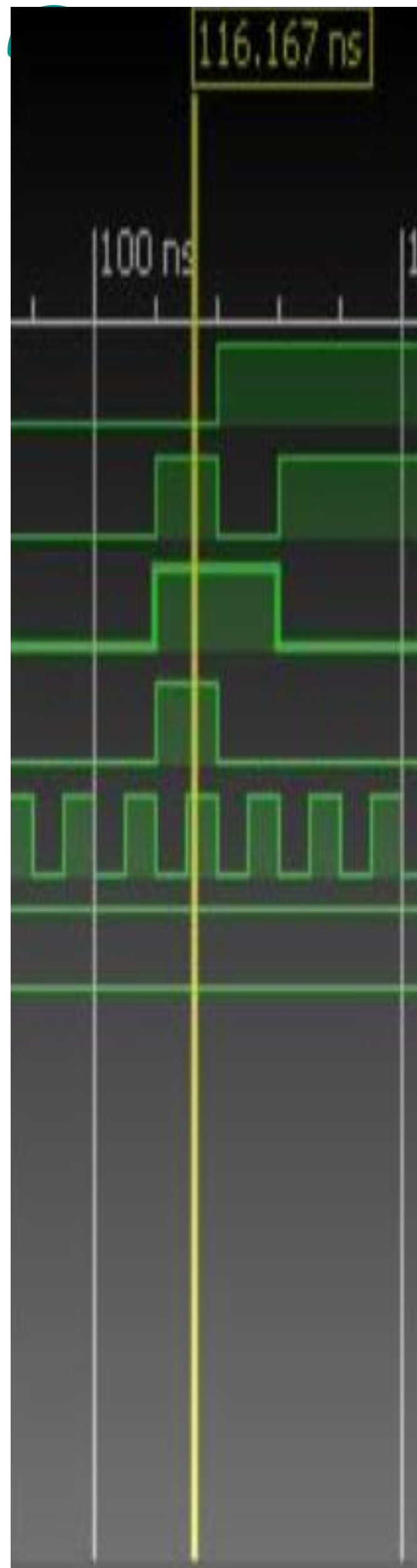
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Agenda

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o PISO-SIPO

PISO-SIPO

LAB6: PISO-SIPO

- The circuit must include an **8-bit register** that allows **parallel data loading** through input switches.
- The circuit must have a **load input** to enable parallel loading of input data.
- The input data must be transmitted **serially** to a second **8-bit register**.
- The circuit must include a **transmit input** to control the serial transmission of data from one register to another.
- Once the **8 bits** are transmitted to the second register, they must be transferred in **parallel** to a third register, which should be connected to **8 output LEDs**.
- The **output register** should only be updated when new data is transmitted.

PISO: Shifter register with parallel load and enable.
SIPO: Shifter register with enable.

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