

Coppelia Payload: Automatically Generating Hardware Attacks

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Introduction

Hardware Attacks

- Attack examples will help designers better understand security threats.
- Developing hardware attacks manually is challenging





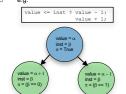
Goal

Given identified triggers, automatically generate hardware attacks.

Background

Symbolic Execution

- Explore all possible paths of a program with symbolic inputs.
- Associated with a symbolic exploration tree.



Recursive, Backward Strategy

- Usage: find assertion violations in hardware. Apply symbolic execution to find the
- assertion violation in one cycle.
- Recursively find the previous states until the initial state is found.

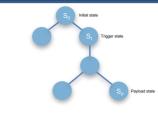


Dependency Analysis

Program dependence graph: represents data and control dependences of a program.



Problem Formulation



Given a processor design and a trigger state S. define a payload state S., (e.g. npc == &foo)

- find symbolic exploration trees To. To
- such that the root node of To is S.
 - the leaf node n_k of T_k is S_n, and
 - the leaf node n of T is the root
 - node of n_{i+1} of T_{i+1}.

Design

Inputs: processor design + property.

Preprocessing



Choosing Payload Assertion

Payload assertion depends on the class of the property in the trigger state.

CF	Control Flow
XR	Exception Related
MA	Memory Access
IE	Correct Instructions
CR	Correctly Update Results

Building the Payload Run the recursive, backward search to find a

- sequence of instructions from St to So.
 - Use dependency analysis to prune the search towards utilizing the signals in the trigger.



Example

Bug:

assign a_lt_b = comp_op[3] ? ((a[width-1] & !b[width-1]) | (!a[width-1] & !b[width-1] & result_sum[width-1]) | (a[width-1] & b[width-1] & result_sum[width-1])) : result_sum[width-1]; // Buggy Version

Property:

((or1200 ctrl.ex insn & 'hFFE00000) >> 21 == 1826) (operand_a > operand_b) || (or1200 sprs.to sr(9) == 1)

Trigger:

r16 0x8000 1.sfqtu r16 r0

Pavload:

attack function

Undesired payload:



