

## Microcontroller Supervisory Circuit with Push-Pull Output

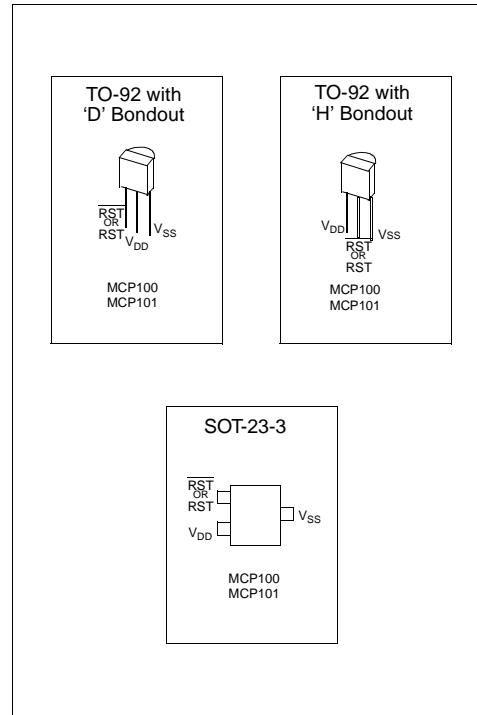
### FEATURES

- Holds microcontroller in reset until supply voltage reaches stable operating level
- Resets microcontroller during power loss
- Precision monitoring of 3V, 3.3V, and 5V systems
- 7 voltage trip points available
- Active low RESET pin (MCP100) or active high RESET (MCP101)
- Push-pull output
- Holds RESET/RESET for 350 ms (typical)
- Guaranteed RESET/RESET to  $V_{DD} = 1.0V$
- Accuracy of  $\pm 125mV$  for 5V systems and  $\pm 75mV$  for 3V systems over temperature
- 45  $\mu A$  typical operating current
- Temperature range:
  - Industrial (I): -40°C to +85°C

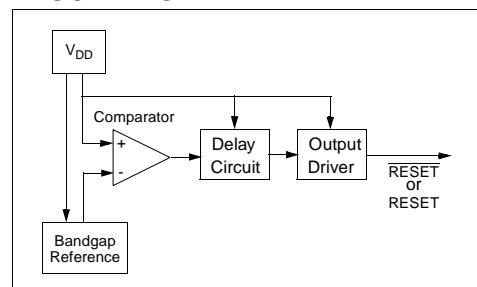
### DESCRIPTION

The Microchip Technology Inc. MCP100/101 is a voltage supervisory device designed to keep a microcontroller in reset until the system voltage has reached the proper level and stabilized. It also operates as protection from brown-out conditions when the supply voltage drops below a safe operating level. Both devices are available with a choice of seven different trip voltages and both have push-pull outputs. The MCP100 has a low active RESET pin and the MCP101 has a high active RESET pin. The MCP100/101 will assert the RESET/RESET signal whenever the voltage on the  $V_{DD}$  pin is below the trip-point voltage.

### PACKAGES



### BLOCK DIAGRAM



## 1.0 ELECTRICAL CHARACTERISTICS

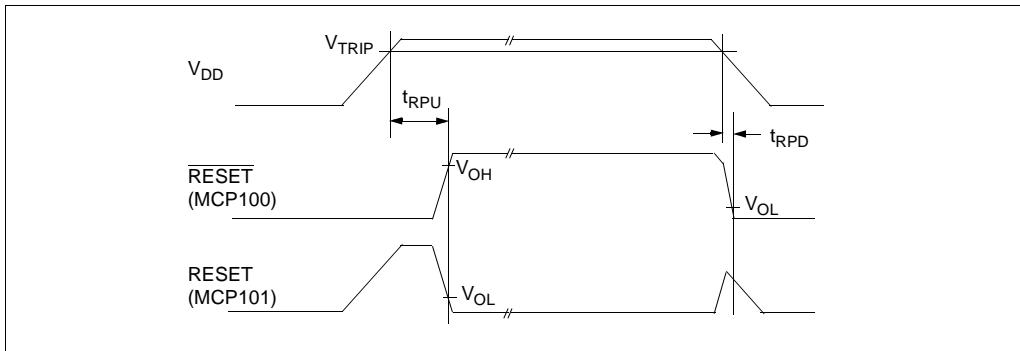
### 1.1 Maximum Ratings\*

$V_{DD}$ .....	7.0V
All inputs and outputs w.r.t. $V_{SS}$ .....	-0.6V to $V_{DD}$ +1.0V
Storage temperature .....	-65°C to +150°C
Ambient temp. with power applied .....	-65°C to +125°C
Soldering temperature of leads (10 seconds) .....	+300°C
ESD protection on all pins .....	$\geq 2\text{ kV}$

**\*Notice:** Stresses above those listed under "Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational listings of this specification is not implied. Exposure to maximum rating conditions for extended periods may affect device reliability.

## DC AND AC CHARACTERISTICS

All parameters apply at the specified temp and voltage ranges unless otherwise noted.		$V_{DD} = 1.0 - 5.5\text{V}$ Industrial (I): -40°C to +85°C					
Operating Voltage Range		$V_{DD}$	1.0		5.5	V	
$V_{DD}$ Value to Guarantee $\overline{\text{RESET}}$ /RESET		$V_{DDMIN}$	1.0			V	
Operating current		$I_{DD}$		45	60	$\mu\text{A}$	$V_{DD} = 5.5\text{V}$ (no load)
$V_{DD}$ Trip Point	MCP10X-270	$V_{TRIP}$	2.55	2.625	2.7	V	
	MCP10X-300		2.85	2.925	3.0		
	MCP10X-315		3.0	3.075	3.15		
	MCP10X-450		4.25	4.375	4.50		
	MCP10X-460		4.35	4.475	4.60		
	MCP10X-475		4.50	4.625	4.75		
	MCP10X-485		4.60	4.725	4.85		
RESET Low Level Output Voltage (MCP100)	MCP100-270	$V_{OL}$			0.4	V	$I_{OL} = 3.2\text{mA}$ , $V_{DD} = V_{TRIPMIN}$
	MCP100-300						
	MCP100-315				0.6		$I_{OL} = 8.5\text{mA}$ , $V_{DD} = V_{TRIPMIN}$
	MCP100-450						
RESET High Level Output Voltage (MCP100)	MCP100-460	$V_{OH}$					
	MCP100-475						
	MCP100-485						
	MCP100-XXX (All $V_{TRIP}$ Points)		$V_{DD}-0.7$			V	$I_{OH} = 3\text{mA}$ , $V_{DD} > V_{TRIPMAX}$
RESET Low Level Output Voltage (MCP101)	MCP101-270	$V_{OL}$			0.4	V	$I_{OL} = 3.2\text{mA}$ , $V_{DD} > V_{TRIPMAX}$
	MCP101-300						
	MCP101-315				0.6		$I_{OL} = 8.5\text{mA}$ , $V_{DD} > V_{TRIPMAX}$
	MCP101-450						
RESET High level Output Voltage (MCP101)	MCP101-460	$V_{OH}$					
	MCP101-475						
Threshold Hysteresis	MCP101-485						
$V_{DD}$ Detect to RESET/RESET Inactive		$t_{RPU}$	150	350	700	ms	
$V_{DD}$ Detect to $\overline{\text{RESET}}$ /RESET		$t_{RPD}$		10		$\mu\text{s}$	$V_{DD}$ ramped from $V_{TRIPMAX} + 250\text{mV}$ down to $V_{TRIPMIN} - 250\text{mV}$

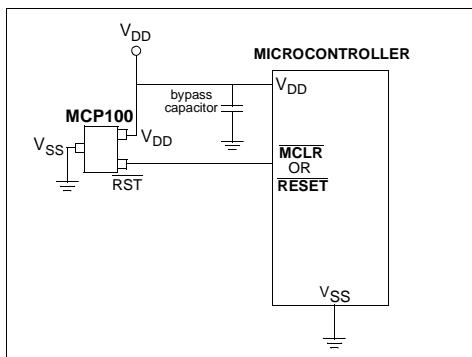


**FIGURE 1-1:** MCP100/101 Timing Diagram

## 2.0 APPLICATIONS INFORMATION

### 2.1 The Need for Supervisory Circuits

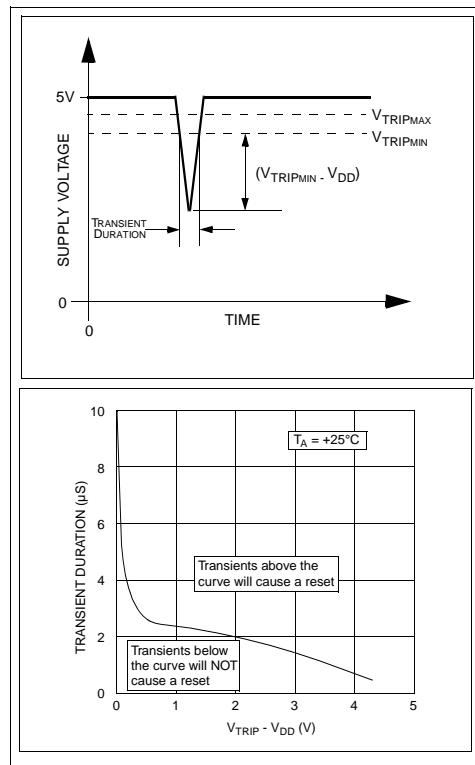
For many of today's microcontroller applications, care must be taken to prevent low power conditions that can cause many different system problems. The most common causes are brown-out conditions where the system supply drops below the operating level momentarily, and the second, is when a slowly decaying power supply causes the microcontroller to begin executing instructions without enough voltage to sustain SRAM and producing indeterminate results.



**FIGURE 2-1:** Typical Application

### 2.2 Negative Going $V_{DD}$ Transients

Many system designers implementing POR circuits are concerned about the minimum pulse width required to cause a reset. Figure 2-2 shows typical transient duration vs. reset comparator overdrive for which the MCP100/101 will not generate a reset pulse. It shows that the farther below the trip point the transient pulse goes, the duration of the pulse required to cause a reset gets shorter. A 0.1  $\mu$ F bypass cap mounted as close as possible to the  $V_{DD}$  pin provides additional transient immunity.



**FIGURE 2-2:** Typical Transient Response

## 2.3 Effect of Temperature on Timeout Period ( $t_{RPU}$ )

The timeout period ( $t_{RPU}$ ) determines how long the device remains in the reset condition. This is controlled by an internal RC timer and is effected by both  $V_{DD}$  and temperature. The graph shown in Figure 2-3 shows typical response for different  $V_{DD}$  values and temperatures.

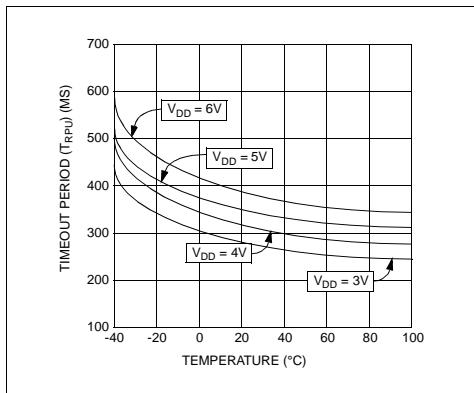


FIGURE 2-3: Typical  $t_{RPU}$  vs. Temperature

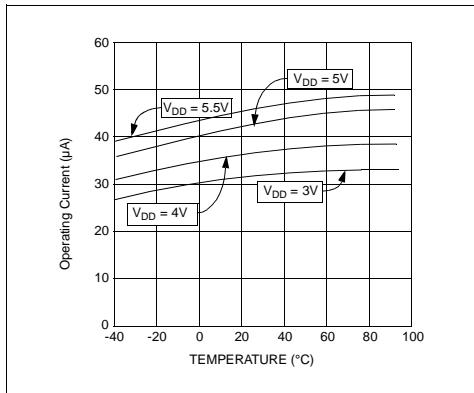


FIGURE 2-4:  $I_{DD}$  vs. Temperature

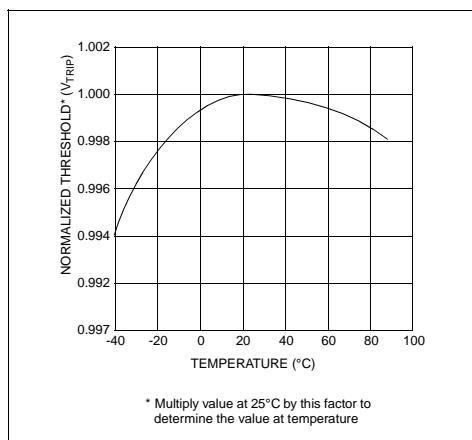


FIGURE 2-5: Normalized  $V_{TRIP}$  vs. Temperature

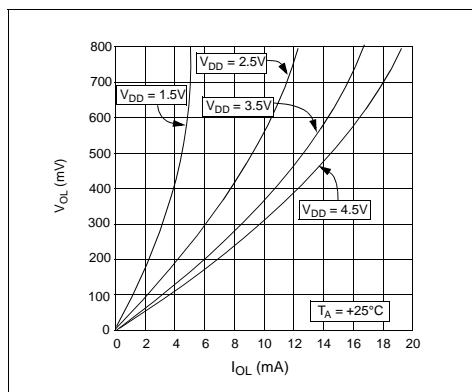


FIGURE 2-6:  $V_{OL}$  vs.  $I_{DL}$

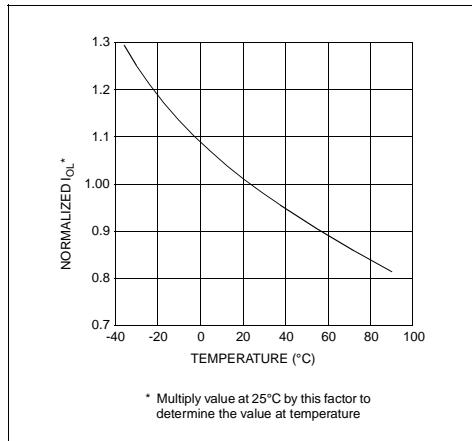


FIGURE 2-7: Normalized  $I_{DL}$  vs. Temperature

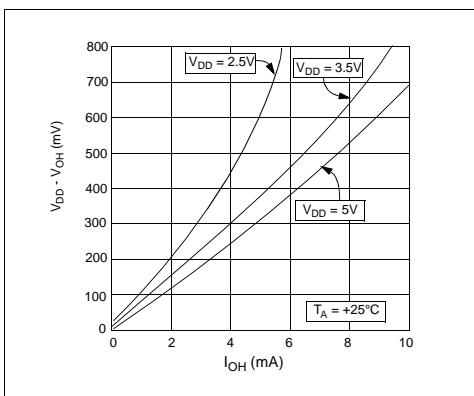


FIGURE 2-8:  $V_{DD} - V_{OH}$  vs.  $I_{OH}$

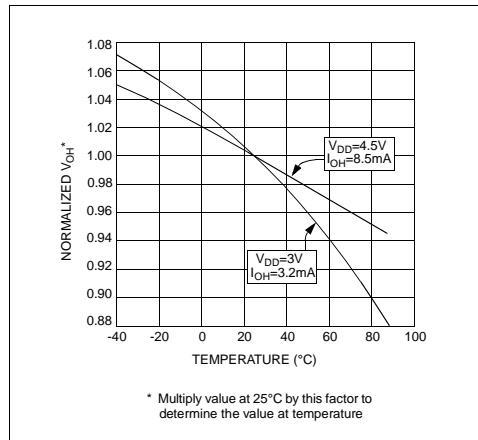


FIGURE 2-9: Normalized  $V_{OH}$  vs. Temperature

# MCP100/101

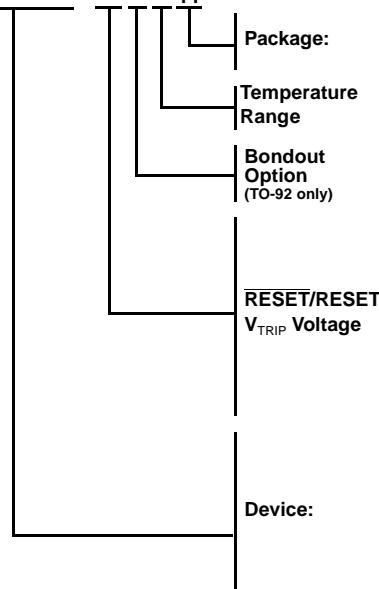
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## NOTES:

## MCP100/101 PRODUCT IDENTIFICATION SYSTEM

MCP100/101—xxx b I /pp



TO = TO-92 (3-lead) [offered in bags only]  
TT = SOT-23 (3-lead) [offered in tape & reel only]

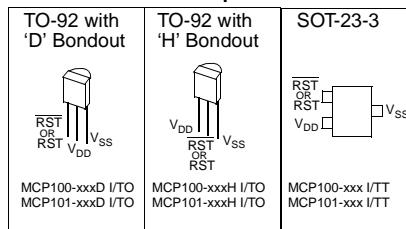
I = -40°C to +85°C (only offered in I)

D = D Bond Option (see bond option chart below)  
H = H Bond Option

270 = 2.55 ≤ V<sub>TRIP</sub> ≤ 2.70  
300 = 2.85 ≤ V<sub>TRIP</sub> ≤ 3.00  
315 = 3.00 ≤ V<sub>TRIP</sub> ≤ 3.15  
450 = 4.25 ≤ V<sub>TRIP</sub> ≤ 4.50  
460 = 4.35 ≤ V<sub>TRIP</sub> ≤ 4.60  
475 = 4.50 ≤ V<sub>TRIP</sub> ≤ 4.75  
485 = 4.60 ≤ V<sub>TRIP</sub> ≤ 4.85

MCP100 = Supervisor circuit with active low  $\overline{\text{RESET}}$  output  
MCP100T = Supervisor circuit with active low  $\overline{\text{RESET}}$  output (tape & reel)  
MCP101 = Supervisor circuit with active high RESET output  
MCP101T = Supervisor circuit with active high RESET output (tape & reel)

### Product Identification Options for MCP100/101



#### Example of Product Identification:

For the MCP100 (active low  $\overline{\text{RESET}}$  pin) with V<sub>TRIP</sub> range of 2.55V - 2.70V in TO-92 package with 'H' bond option in industrial temp range, the part number would be: MCP100-270H I/TO.

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