
Archipelago - An Open Source FPGA with Toolflow Support

by Hao Jun Liu

Research Project

Submitted to the Department of Electrical Engineering and Computer Sciences,
University of California at Berkeley, in partial satisfaction of the requirements for
the degree of **Master of Science, Plan II**.

Approval for the Report and Comprehensive Examination:

Committee:

Prof. John Wawrzynek, Research Advisor

(Date)

* * * * *

Prof. Elad Alon, Second Reader

(Date)