SET - 1

III B. Tech II Semester Supplementary Examinations, November -2019 SOFTWARE TESTING METHODOLOGIES

(Common to Computer Science and Engineering, Information Technology)

Time: 3 hours Max. Marks: 70

Note: 1. Question Paper consists of two parts (**Part-A** and **Part-B**)

2. Answer ALL the question in Part-A

3. Answer any FOUR Questions from Part-B

		$\underline{PART - A} \tag{14}$	4 Marks)
1.	a)	Differentiate Beta testing from Alpha testing.	[2M]
	b)	Define Slicing.	[2M]
	c)	Compare open and closed domains.	[2M]
	d)	What is the possibility of getting unreachable states?	[3M]
	e)	What is the role of predicate in path expression?	[3M]
	f)	At what level non functional requirement testing is performed?	[2M]
		$\underline{PART - B} \tag{5}$	6 Marks)
2.	a)	List out various types of Bugs possible in executing a program and discuss their emedies.	r [7M]
	b)	Describe the role of control flow graph in testing a software.	[7M]
3.	a)	Write about the components of transaction flow testing.	[7M]
	b)	What are the differences between static and dynamic anomaly detection Explain.	? [7M]
4.	a)	Relate Bug assumption with domain testing.	[7M]
	b)	Discuss the importance of regular expression in software testing.	[7M]
5.	a)	Represent the path expression: ab(cde)*(f+kba)*(a+acd)*(g+c)* using graph.	[7M]
	b)	How decision tables will be helpful in logic based testing gives variou components of it? Explain.	s [7M]
6.	a)	Demonstrate cyclomatic complexity with an example.	[7M]
	b)	How to identify good and bad state graphs? Explain.	[7M]
7.	a)	Explain the features of test automation. Give its merits and demerits over manual testing.	1 [7M]
	b)	How to record test and set check points in win runner? Explain.	[7M]

Time: 3 hours

7.

a)

b)

testing applications.

Max. Marks: 70

[7M]

[7M]

III B. Tech II Semester Regular Examinations, April/May- 2019 SOFTWARE TESTING METHODOLOGIES

(Common to Computer Science and Engineering, Information Technology)

Note: 1. Question Paper consists of two parts (Part-A and Part-B) 2. Answer ALL the question in Part-A 3. Answer any FOUR Questions from Part-B PART -A 1. What are integration bugs? [2M] a) b) What is meant by a program slice? [2M] What is Floating-Point Zero Check? c) [2M] Write short notes on delimiter errors. d) [3M] What is dead state? [3M] e) f) Write the benefits of automated testing. [2M] PART -B 2. What is control flow graph? Explain how to generate control flow graph with an [7M] a) example. b) Explain different types of testing and when they need to be carried out. [7M] 3. What is data flow model? Explain the various components of data flow model. a) [7M] b) Write about static versus dynamic anomaly detection. [7M] 4. Write the role of path expression and path predicates in testing. a) [7M] b) State and explain various restrictions at domain testing processes. [7M] 5. Minimize the following function using KV charts: [7M] a) F(A,B,C,D) = P(1,2,3,8,9,10,11,14) + Pd(7,15)Write about Test Case Design process. [7M] b) 6. a) What are the principles of state testing? Discuss advantages and disadvantages. [7M] b) What are the matrix operations used in tool building? Give their significance. [7M]

Explain the different windows that are available in WinRunner and their usage in

What is checkpoint? Describe the role of checkpoints in testing.

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Note: 1. Question Paper consists of two parts (Part-A and Part-B)

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2. Answer **ALL** the question in **Part-A** 3. Answer any **FOUR** Questions from **Part-B** PART –A 1. What is integration testing? a) [2M]List the steps involved in transaction for an online information retrieval system. b) [2M]What are simple domain boundaries? c) [2M] d) Write about categories of string errors. [3M] e) What is unreachable state? [3M] What are factors to be considered before automate testing? f) [2M] **PART-B** Explain the model of testing with neat sketch. 2. a) [7M] How to go about selecting paths for testing? Explain with an example. b) [7M] What is meant by transaction flow testing? Explain it with an example. 3. [7M] a) Explain data-flow testing with an example. Give its generalizations and limitations. b) [7M] 4. What is bug assumption? Elaborate different bugs that can result in domain errors. [7M] a) Explain the important properties of boundaries. How they will be used in identifying b) [7M] test cases? Use KV chart to minimize 5. [7M] a) F= B'C'D'+A'B'C'D'+ABC'D+A'BCD+ABD+B'CD'+A'BC'D b) Explain in detail problems related to delimiter in path expressions. [7M] 6. Write short notes on: a) [7M] (i) Transition Bugs (ii) Dead States Discuss node reduction algorithm with suitable example. b) [7M] 7. Explain different menus and toolbars that exist within WinRunner and the a) [7M]

How do analyze the results provided by WinRunner and load runner tools? Explain.

[7M]

functionality that these items provide.

b)

2. Answer **ALL** the question in **Part-A**

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SET - 3

(Common to Computer Science and Engineering, Information Technology)

Note: 1. Question Paper consists of two parts (Part-A and Part-B)

Time: 3 hours Max. Marks: 70

		3. Answer any FOUR Questions from Part-B				
PART -A						
1.	a)	What is component testing?	[2M]			
	b)	Define dynamic slicing.	[2M]			
	c)	What is path sum?	[2M]			
	d)	Write about BNF operators.	[3M]			
	e)	What are equivalent states? Give an example.	[3M]			
	f)	Describe the steps involved in manual testing.	[2M]			
		PART -B				
2.	a)	Write about remedies for test bugs.	[7M]			
	b)	What are the different kinds of loops? Explain the different test cases for a single 'for' loop.	[7M]			
3.	a)	Explain in detail the transaction flow testing techniques.	[7M]			
	b)	What are data-flow anomalies? Write about data flow anomalies that may occur in a flow graph.	[7M]			
4.	a)	Explain with example node-by-node removal algorithm.	[7M]			
	b)	Explain various properties related to Ugly-domains.	[7M]			
5.	a)	Demonstrate reduction of the following functions using KV chart: $F(A, B, C, D) = \pi (4,5,6,7,8,12,13) + d(1,15)$	[7M]			
	b)	Write about (i) Execution Automation (ii) Design Automation	[7M]			
6.	a)	What are the software implementation issues in state testing? Explain how to handle them.	[7M]			
	b)	Write about equivalence relation and partial ordering relation.	[7M]			
7.	a)	Explain the steps involved in automated testing process.	[7M]			
	b)	Explain the features of Jmeter Testing environment.	[7M]			

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Note: 1. Question Paper consists of two parts (Part-A and Part-B)

2. Answer **ALL** the question in **Part-A**

3. Answer any **FOUR** Questions from **Part-B**

PART –A

1.	a)	What is correction cost?	[2M]
	b)	What is data flow testing?	[2M]
	c)	What is ambiguous domain and over specified domain?	[2M]
	d)	List the steps in syntax testing.	[3M]
	e)	How to identify a state? List appropriate testing tools for them.	[3M]
	f)	What are different types of software applications? PART -B	[2M]
2.	a)	Explain the consequences of bugs.	[7M]
	b)	Write about path-selection and path-testing criteria.	[7M]
3.	a)	Illustrate the differences between Control Flow and Transaction flow.	[7M]
	b)	Explain the terms slicing, dicing, data flow and debugging with reference to testing.	[7M]
4.	a)	Discuss Path Sums and Path Product with examples.	[7M]
	b)	Discuss with example the equal - span range/Doman compatibility bugs.	[7M]
5.	a)	Illustrate the following functions using K-Maps $F(A,B,C,D) = P(4,5,6,7,8,12,13) + d(1,15)$	[7M]
	b)	Explain the test case design for ATM example.	[7M]
6.	a)	Explain about good state and bad state graphs. How to handle bad state graphs.	[7M]
	b)	How can a relation be represented and what are the properties of relations?	[7M]
7.	a)	Explain the process to be followed when doing testing using WinRunner.	[7M]
	b)	Explain about Rapid Test Script Wizard. How will it assist the tester?	[7M]
