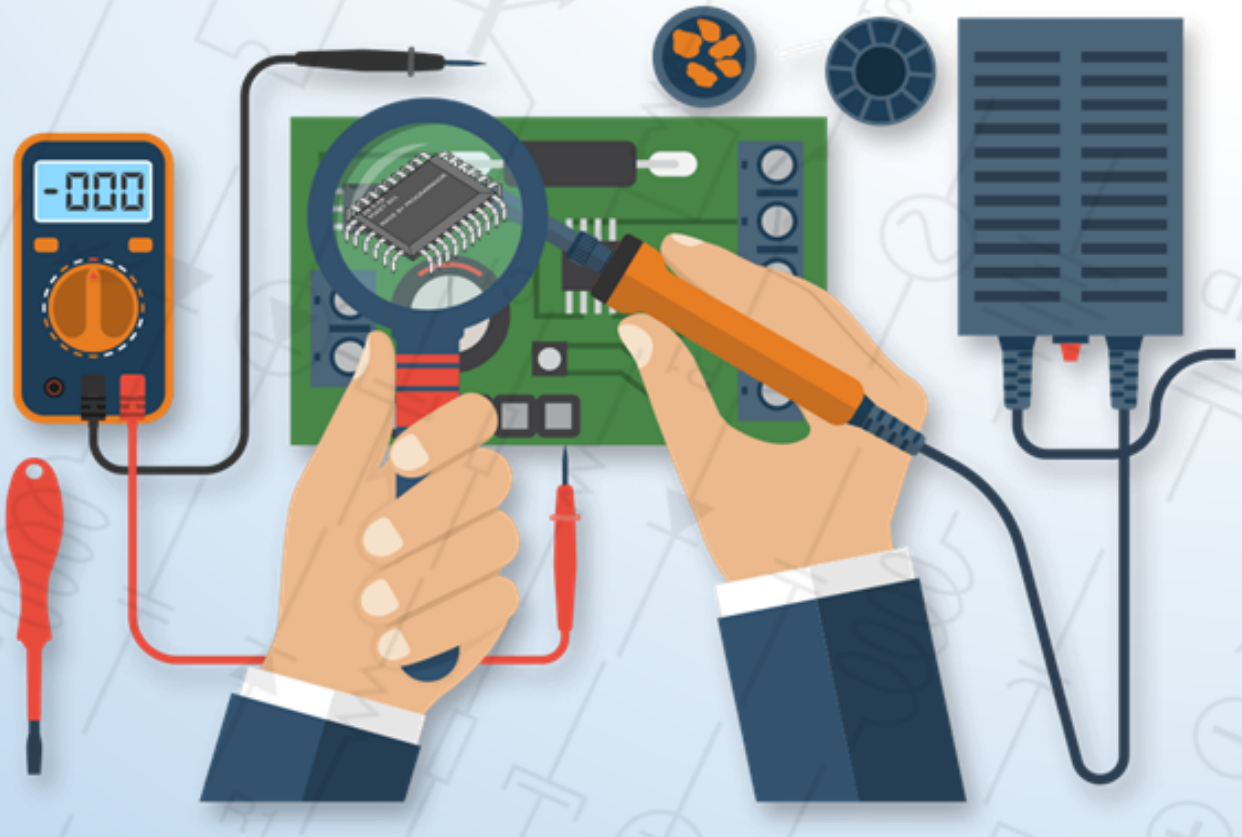
HO CHI MINH CITY UNIVERSITY OF TECHNOLOGY
COMPUTER ENGINEERING

Electronic Device Component



Dr. Le Trong Nhan

Contents

Chapter 1. Operational Amplifier - OPAMP **7**
1 Introduction . . . 8
2 Closed Loop Operation . . . 9
2.1 Non inverting configuration . . . 9
2.2 Inverting configuration . . . 9
3 Exercise and Report . . . 10
3.1 Voltage Follower . . . 10
3.2 High-Current Voltage Follower . . . 12
3.3 Voltage Follower with Gain . . . 13
3.4 Summing Amplifier . . . 15
3.5 Low Pass Filter . . . 16
3.6 High Pass Filter . . . 19
3.7 Comparator with Hysteresis (Schmitt Trigger) . . . 19
4 Altium Designer . . . 19
4.1 LED Driver . . . 19
4.1.1 Schematic design . . . 19
4.1.2 PCB layout . . . 20
4.1.3 Schematic design . . . 20
4.1.4 PCB layout . . . 20

CHAPTER 1

Operational Amplifier - OPAMP

1 Introduction

Operational Amplifiers, also known as Op-amps, are basically a voltage amplifying device designed to be used with components like capacitors and resistors, between its in/out terminals. They are essentially a core part of analog devices. Feedback components like these are used to determine the operation of the amplifier. The amplifier can perform many different operations, giving it the name Operational Amplifier.

One key to the usefulness of these little circuits is in the engineering principle of feedback, particularly negative feedback, which constitutes the foundation of almost all automatic control processes. The principles presented in this section, extend well beyond the immediate scope of electronics. It is well worth the electronics student's time to learn these principles and learn them well.

Operational amplifiers can have either a closed-loop operation or an open-loop operation. The operation (closed-loop or open-loop) is determined by whether or not feedback is used. Without feedback the operational amplifier has an open-loop operation. This open-loop operation is practical only when the operational amplifier is used as a cooperator (a circuit which compares two input signals or compares an input signal to some fixed level of voltage). As an amplifier, the open-loop operation is not practical because the very high gain of the operational amplifier creates poor stability. (Noise and other unwanted signals are amplified so much in open-loop operation that the operational amplifier is usually not used in this way.) Therefore, most operational amplifiers are used with feedback (closed-loop operation).

2 Closed Loop Operation

Operational amplifiers are used with degenerative (or negative) feedback which reduces the gain of the operational amplifier but greatly increases the stability of the circuit. In the closed-loop configuration, the output signal is applied back to one of the input terminals. This feedback is always degenerative (negative). In other words, the feedback signal always opposes the effects of the original input signal. One result of degenerative feedback is that the inverting and non-inverting inputs to the operational amplifier will be kept at the same potential.

Closed-loop circuits can be of the inverting configuration or non-inverting configuration.

2.1 Non inverting configuration

The typical circuit for this configuration is shown in the figure bellow:

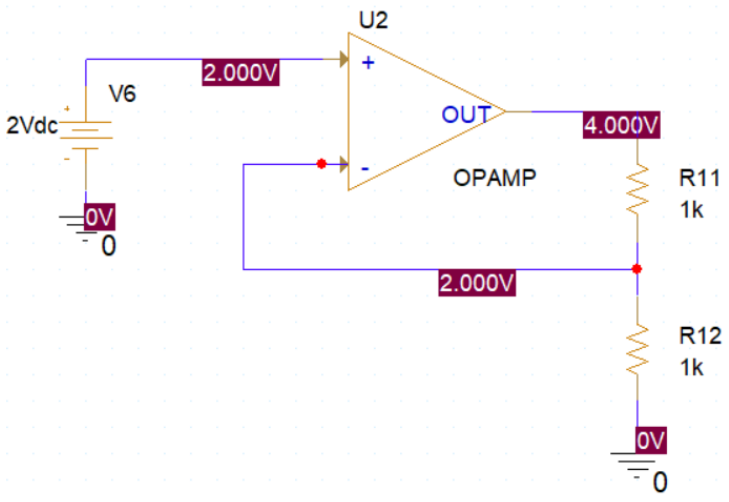


Figure 1.1: Non inverting configuration

The new component, named also OPAMP (Operational Amplifier) is easily found in the favorite list of the PSPICE.

In order to explain the 4V at the ouput, it is obviously that $V(+) = V(-) = 2V$ in a closed loop configuration. Therefore, from a resistor bridge at the output, $V_{OUT} = 4V$.

2.2 Inverting configuration

In this configuration, the output is connected directly to a pin of the opamp as follow:

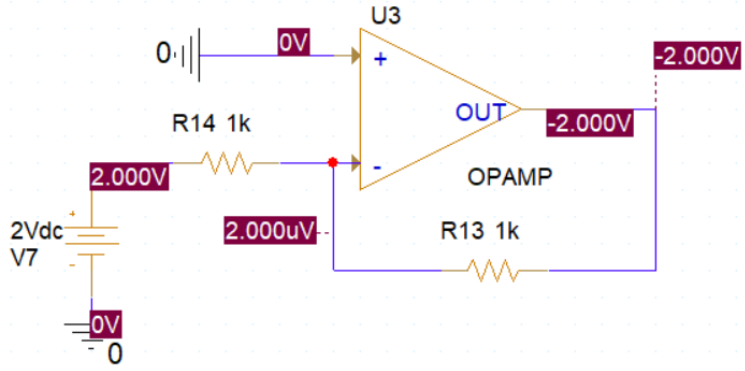


Figure 1.2: Inverting configuration

As the output voltage is negative, which is inverted to the input, the name of this circuit is the invert connection. Students are proposed to perform calculations to confirm the output, which is $-2V$.

3 Exercise and Report

3.1 Voltage Follower

Voltage follower is one of the simplest uses of an operational amplifier, where the output voltage is exactly same as the input voltage applied to the circuit. In other words, the gain of a voltage follower circuit is unity. The connections are proposed as follows:

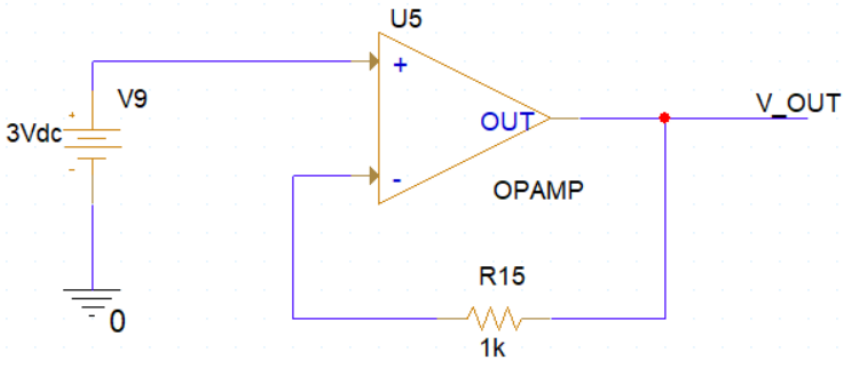


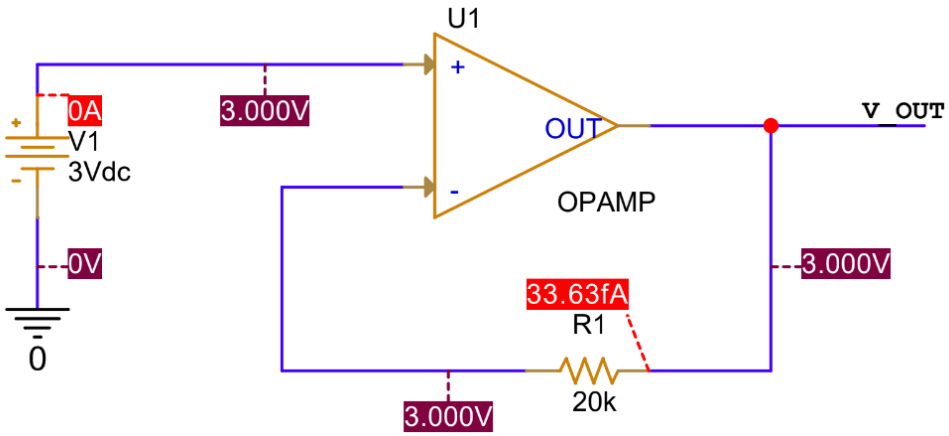
Figure 1.3: Opamp follower circuit

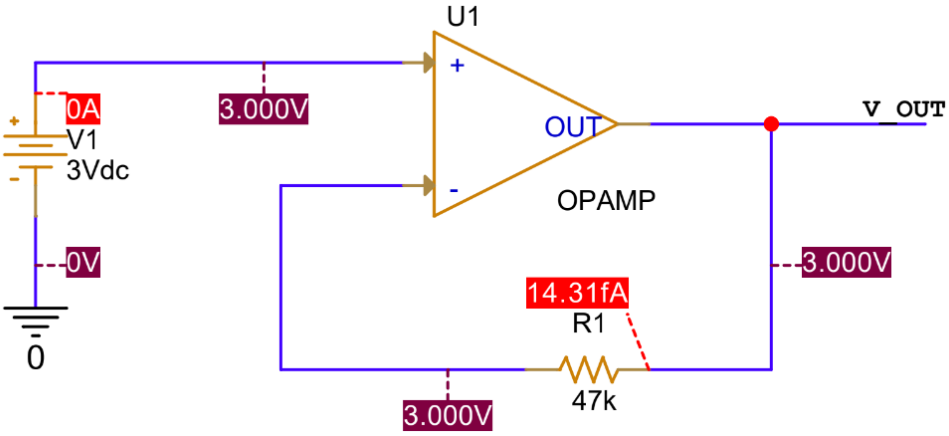
A voltage follower has low output impedance and extremely high input impedance, and this makes it a simple and effective solution to problematic impedance relationships. If a high-output-impedance sub-circuit must transfer a signal to a low-input-impedance sub-circuit, a voltage follower placed between these two sub-circuits will ensure that the full voltage is delivered to the load.

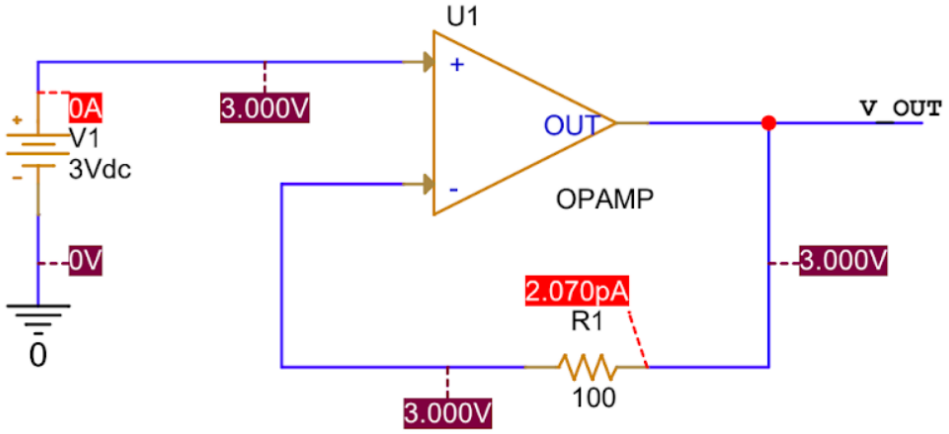
Students are propose to run the simulation with bias mode to confirm that $V_{OUT} = V(+)$. The feedback resistance is also required to change.

Your calculations are presented here to prove $V_{OUT} = V(+)$ with any value of R15.

Simulation:







Explanation:
$V_{pos} = V_{neg} = 3$ V and $I_{R_{15}} = 0$ (A) When $R_{15} = 1kOhm$:
$\frac{V_{out} - V_{neg}}{R_{15}} = 0 \Rightarrow V_{neg} = V_{out} = 3V$

3.2 High-Current Voltage Follower

The voltage follower's low output impedance makes it a good circuit for driving current into a low-impedance load, but it's important to remember that most op-amps are not designed to deliver large output currents. The most basic circuit for buffering an op-amp's output current is the following:

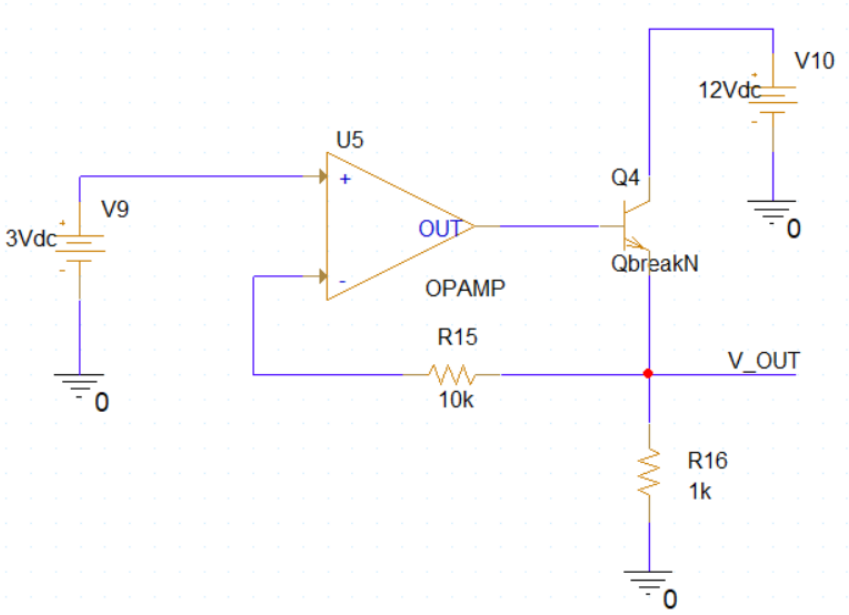


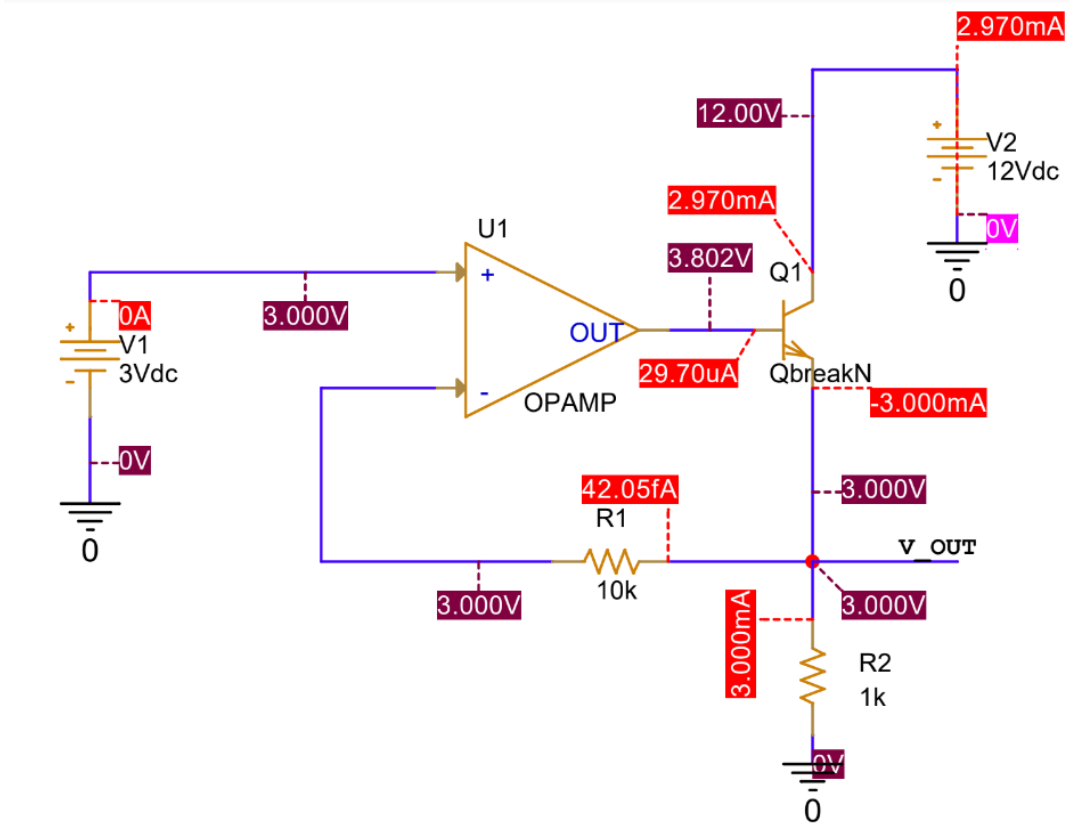
Figure 1.4: Opamp follower circuit

The voltage at the positive pin of the Opamp is copied to V_{OUT}. In this schematic, R16 is used to simulate a load device, which can be a motor or an high power LED. However, in this case, there is a high current can pass the load.

Students are proposed to run the simulation with bias configuration, capture the results and place them in the report.

Finally, your computations go here to explain the results.

Simulation:



Explanation:

$V_{out} = V_{neg} = 3$ V

$I_{R_2} = I_E = \dfrac{V_{out}}{R_2} = 3$ (mA)

$I_B = \dfrac{I_E}{1+\beta} = \dfrac{3}{101} = 0.03$ (mA)

$I_C = I_E - I_B = 2.97$ (mA)

3.3 Voltage Follower with Gain

This basic circuit is not limited to the unity-gain configuration. As with a non-buffered op-amp, you can insert resistors into the feedback path to create overall gain from the input to the load voltage. Here is the non-unity-gain version of the circuit:

Students are proposed to implement this circuit on PSPICE with input is 2V and the gain is 3. The voltage supply for the load side is 12VDC. Value of R_{LOAD} is 1K.

The simulation results in PSPICE (bias configuration) are presented here. Moreover, a short explanations are required in this report to explain the gain of the output follower voltage.

Simulation:

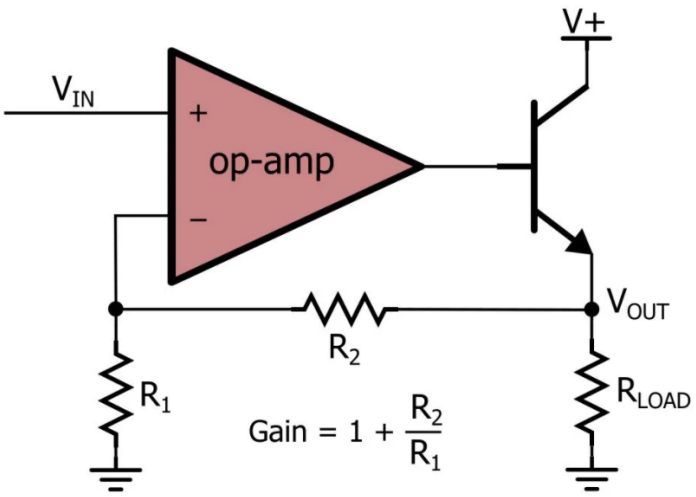
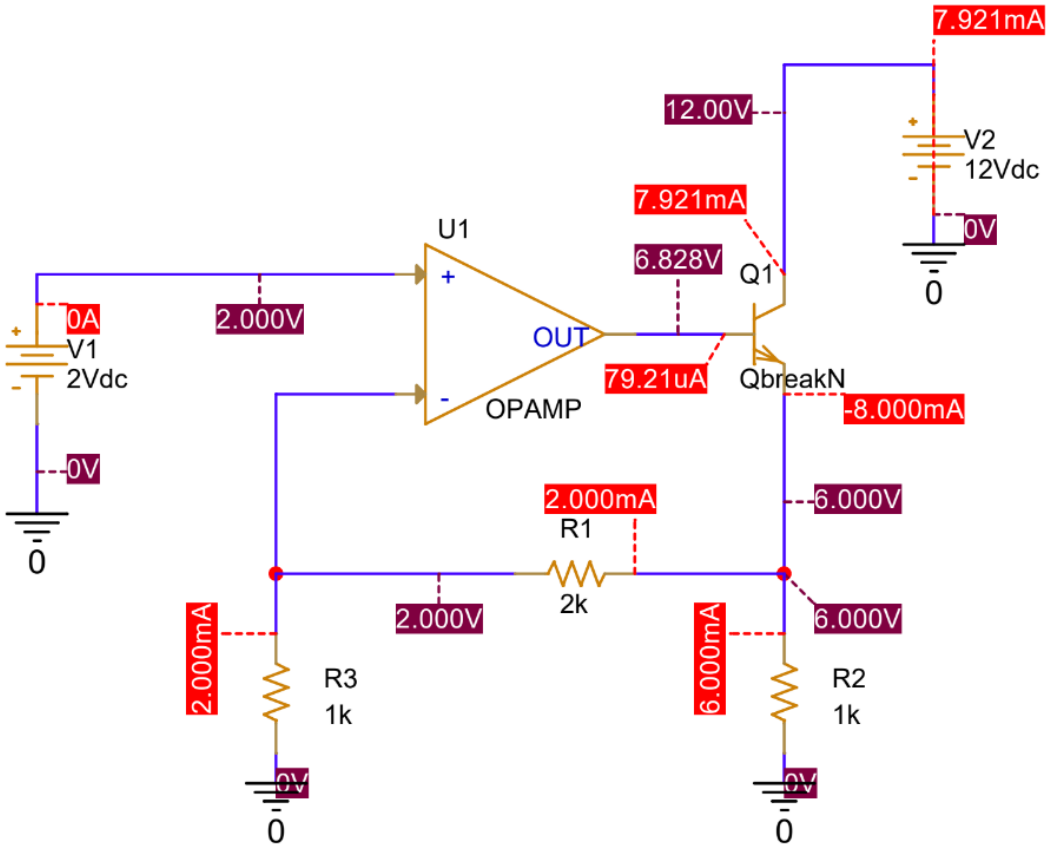


Figure 1.5: Opamp follower with gain for the output



Explanation:

$$I_{R_1} = I_{R_2} \Leftrightarrow \frac{V_{in}}{R_1} = \frac{V_{out} - V_{in}}{R_2} \Leftrightarrow \frac{V_{out}}{V_{in}} = 1 + \frac{R_1}{R_2}$$

$$GAIN = 1 + \frac{R_1}{R_2}$$

3.4 Summing Amplifier

Students are proposed to implement following schematic in PSPICE and run the simulation with R1 = 1K, R2 = 2K, R3 = 5K, Rf = 9K, Ri = 1K. There inputs are V1 = 1V, V2 = 2V and V3 = 3V. This circuit is a non inverting summing configuration using opamp.

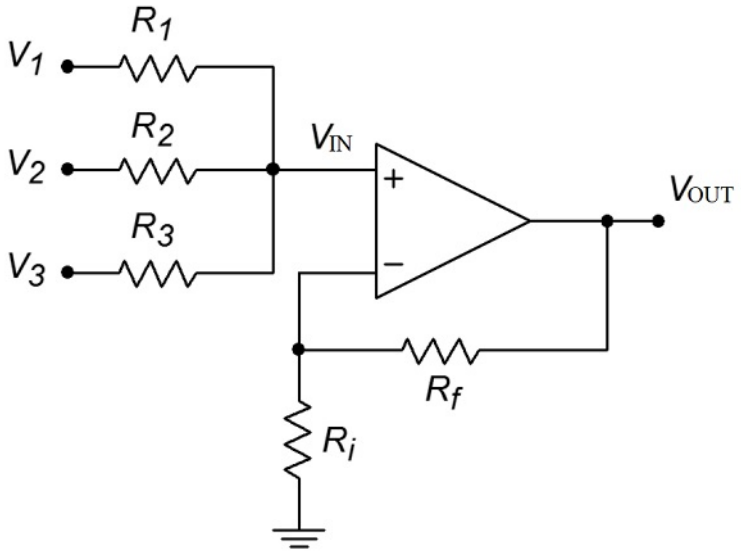
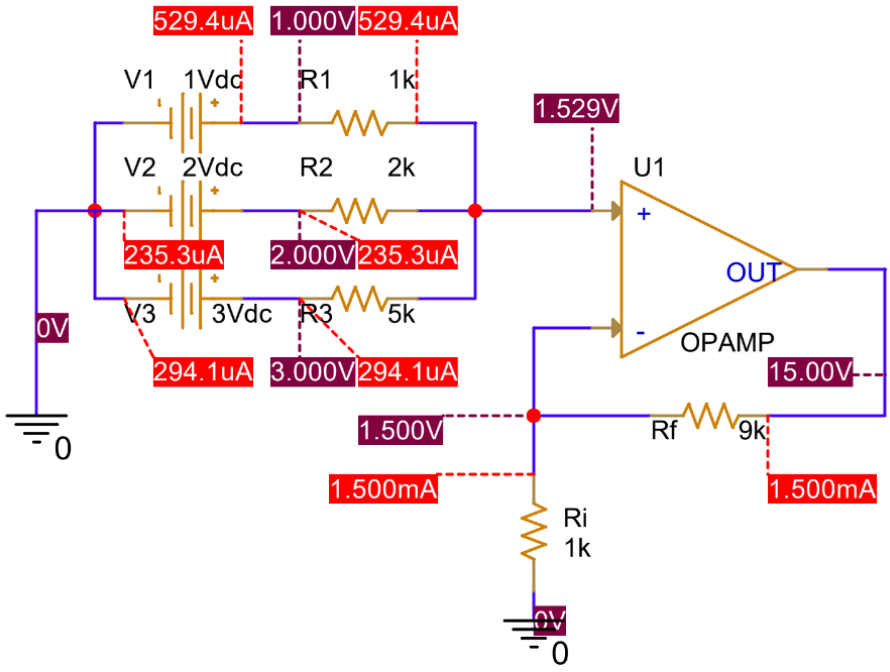


Figure 1.6: Non inverse summing using OPAMP

Students are proposed to design the schematic and place the results in this report.

Your image goes here



Your calculations go here to explain the value of V_{OUT}

Explanation:

$V_{out} = V_{neg} = 3$ V

$I_{R_2} = I_E = \dfrac{V_{out}}{R_2} = 3$ (mA)

$$I_B = \frac{I_E}{1+\beta} = \frac{3}{101} = 0.03 \text{ (mA)}$$
$$I_C = I_E - I_B = 2.97 \text{ (mA)}$$

The second type of the summing amplifier is proposed as follows:

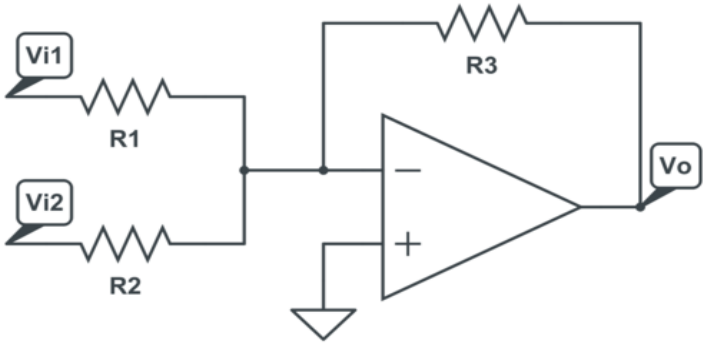
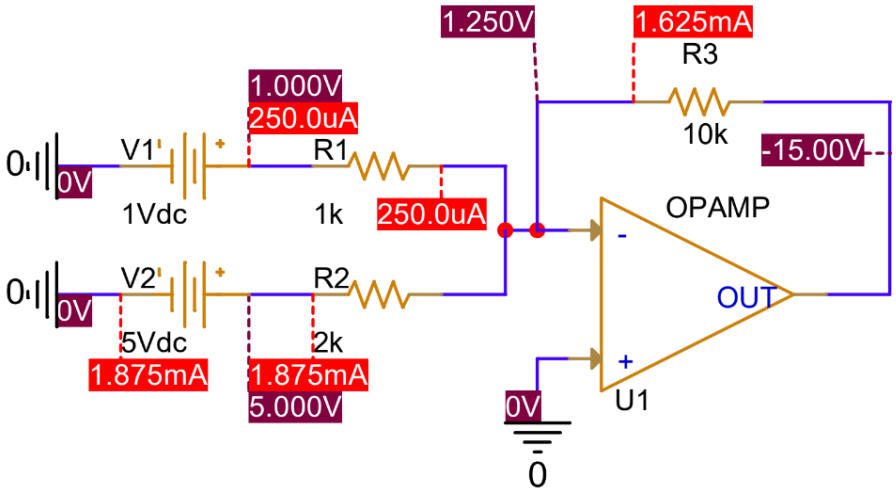


Figure 1.7: Inverse summing using OPAMP

Students are proposed to do the same steps above, with R1 = 1K, R2 = 2K, R3 = 10K and V1 = 1V, V2 = 5V.



3.5 Low Pass Filter

Low pass filter is a filter which passes all frequencies from 0Hz (DC current) to upper cut-off frequency f_H and rejects any signals above this frequency. A picture to demonstrate a low pass filter behavior is shown in the figure bellow:
Similar to the closed loop configuration, there also 2 types of low pass filter, including the inverting and non-inverting low pass filter. The figure bellow is an inverting low pass filter. The cut-off frequency is determined by this equation:

$$f_H = \frac{1}{2\pi R_2 C}$$

By applying the value of $R2 = 10KOhm$ and $C = 1nF$, the cut-off frequency is around $16KHz$. In order to see the results, students are proposed to run the AC Sweep simulation profile (**Linear Type, Start and Stop frequency are 1Hz and 50kHz, 200 points**), as follows:

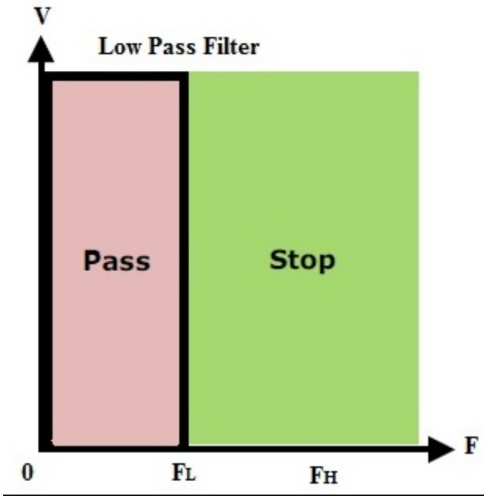


Figure 1.8: Low pass filter principles

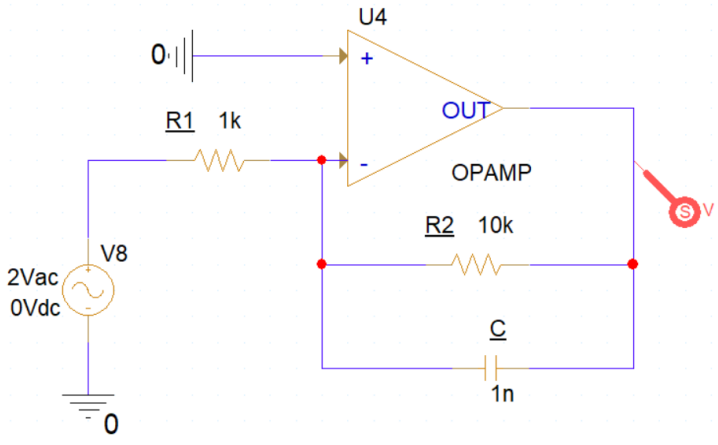


Figure 1.9: Inverting low pass filter

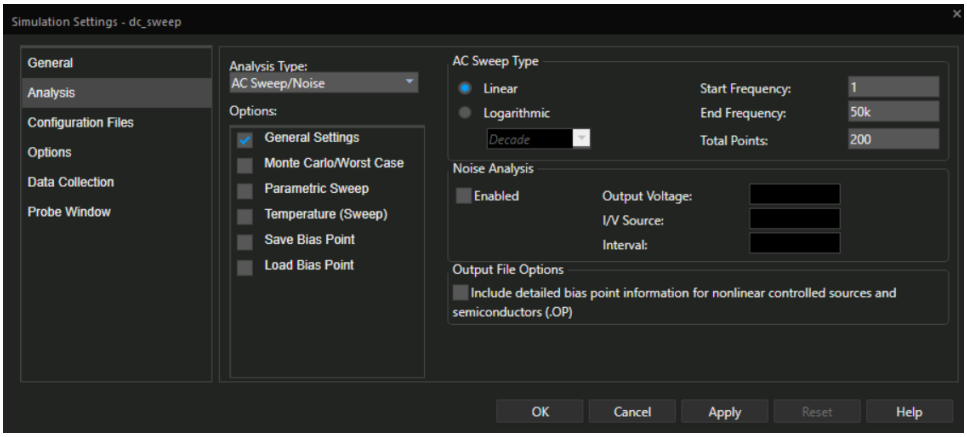


Figure 1.10: AC Sweep simulation profile

The final results can be archived like the figure bellow:

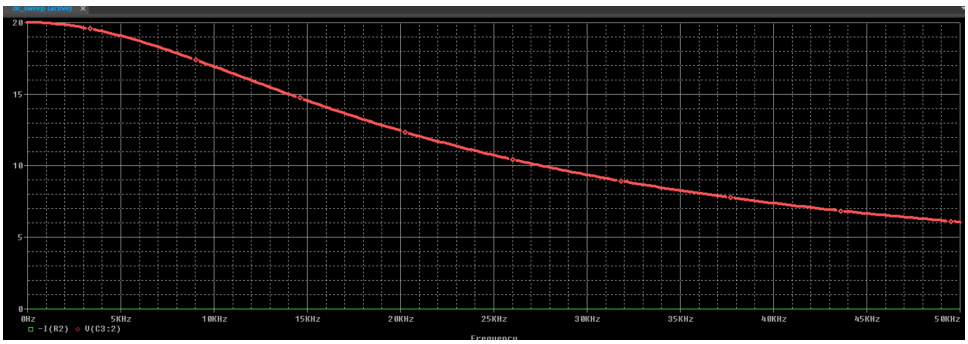


Figure 1.11: Simulation results

It is said that the cut-off frequency point having the gain reduced 3dB. The gain at 0Hz is 10 (input voltage is 2V and output voltage is 20V), or $20log(10) = 20dB$, meanwhile, the gain at 16kHz is 7 (input voltage is 2V and output voltage is 14V), or $20log(7) = 16.9$.

The second type of a low pass filter, the non-inverting configuration, is presented as follows:

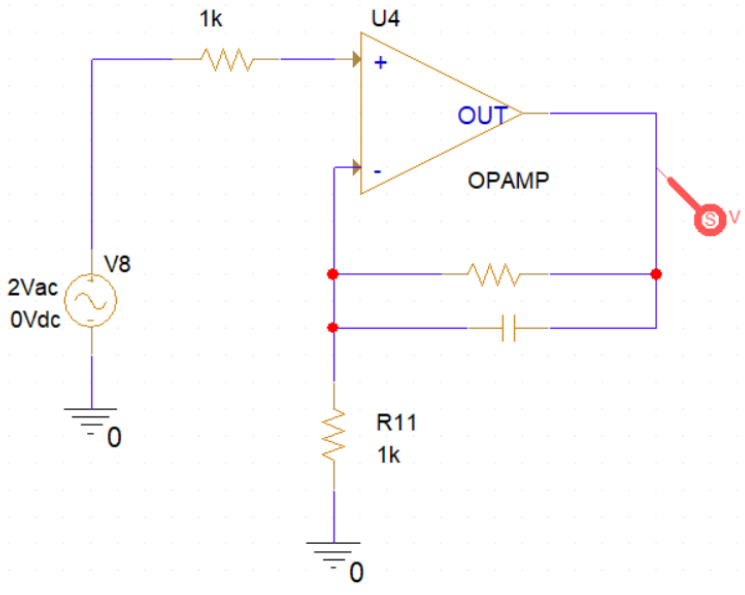
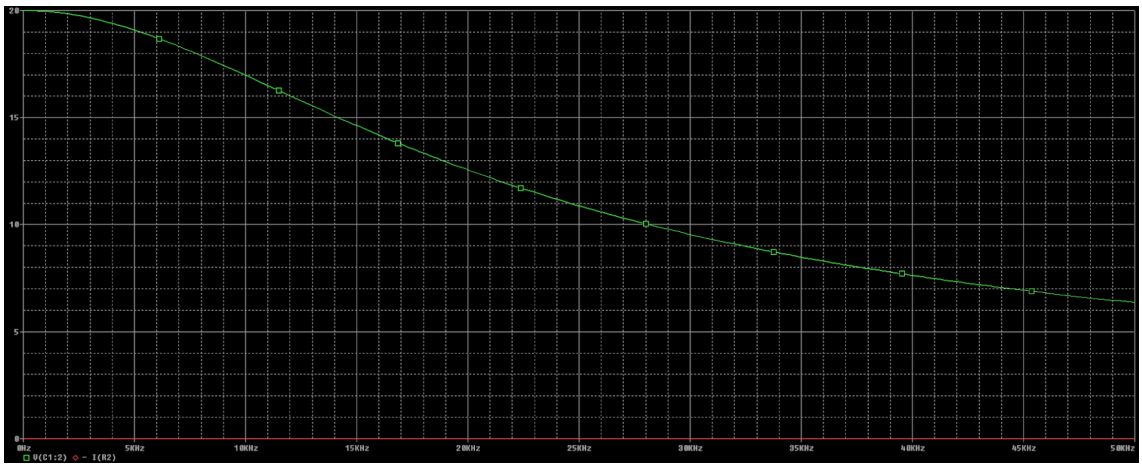


Figure 1.12: Non-inverting low pass filter

Students are proposed to calculate the value of R and C to have the amplifier factor equal to 10 and the cut-off frequency is the same as the previous example. The simulation result with AC Sweep mode is required to plot in this report as well.

Simulation:



Explanation:

$$I_{R_{11}} = I_Z \Rightarrow \frac{V_{in}}{R_{11}} = \frac{V_{out} - V_{in}}{Z}$$

$$\Rightarrow \frac{V_{out}}{V_{in}} = 1 + \frac{Z}{R_{11}} = 1 + \frac{R}{R_{11}}.\frac{Z_C}{R + Z_C} = 1 + \frac{R}{R_{11}}.\frac{1}{1 + \frac{R}{Z_C}}$$

$$\Rightarrow \frac{V_{out}}{V_{in}} = 1 + \frac{R}{R_{11}}.\frac{1}{1 + 2.\pi.f.R.C}$$

$$R//Z_C \Rightarrow Z = \frac{R.Z_C}{R + Z_C}$$

$$10 = 1 + \frac{R}{R_1 1} \Rightarrow R = 9k\Omega$$

$$f_H = \frac{1}{2.\pi.R.C} = 16k \Rightarrow C = 1.11 \text{ (nF)}$$

3.6 High Pass Filter

3.7 Comparator with Hysteresis (Schmitt Trigger)

A comparator with hysteresis has a nonlinear response with different switching threshold voltages depending upon whether the input is transitioning from low to high, or high to low. The circuit employs positive feedback to generate the nonlinear response. The lowto-high transition input voltage is called the high threshold voltage (VTH). Similarly, the high-to-low transition input voltage is called the low threshold voltage (VTL). This calculator assumes that the output can swing from the positive supply rail (VP) to the negative supply rail (VN).

4 Altium Designer

4.1 LED Driver

In this project, we will show how to build a simple LED driver circuit. A simple driver based on BJT is proposed in this section.

4.1.1 Schematic design

The manual for the schematic is posted in this link:

https://www.youtube.com/watch?v=ftiX8peTsiw

Students are proposed to design the schematic and place the results in this report.

Your image goes here

4.1.2 PCB layout

The manual for PCB layout is posted in this link:

https://www.youtube.com/watch?v=btpAoh3nmBU

Your image goes here

4.1.3 Schematic design

The manual for the schematic is posted in this link:

https://www.youtube.com/watch?v=VcO_F97ydFM

Students are proposed to design the schematic and place the results in this report.

Your image goes here

4.1.4 PCB layout

The manual for PCB layout is posted in this link:

https://www.youtube.com/watch?v=Dbqcb0zQ0E8

Your image goes here