THALES AVS Mérignac

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| **Functional specification** | |
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| **Ref.: V1.1** |  |
| **Information: Analog chain acquisition ASIC integration project.** | |

# Subject: Analog chain acquisition (nFCC/eFCC)

This document aims to fix the functional specification for the analog chain project. The document will evolve until a final version will be reach. The first goal is to determine which part will be change in ASIC to have an interesting project in terms of feasibility, performance, surface and cost.

Two solutions studied by THALES will be addressed in this document. The first one is a solution already approved and ready to be embedded in the eFCC project. The second is a solution still study currently, which aims to improve the current solution by adding multiplexing and other components.

# Functional diagrams



Figure 1: Solution eFCC



Figure 2: Solution with multiplexing

Figure 1&2 are the functional diagram of the two solutions mentioned earlier. Within the framework of this project, the integration will approximately the same for the both, except that the second solution will introduce a multiplexing stage to integrate in the ASIC.

At this point, the idea is to integrate at least the differential amplifier + the antialiasing filter + follower amplifier to solve impedance issues. The EMI filter could also be probably integrated as it seems not really hard to do.

Of course, here is a first iteration and the circuits, which will be integrated if it is possible, will probably change as the technology constraints become clearer with time.

# Specifications

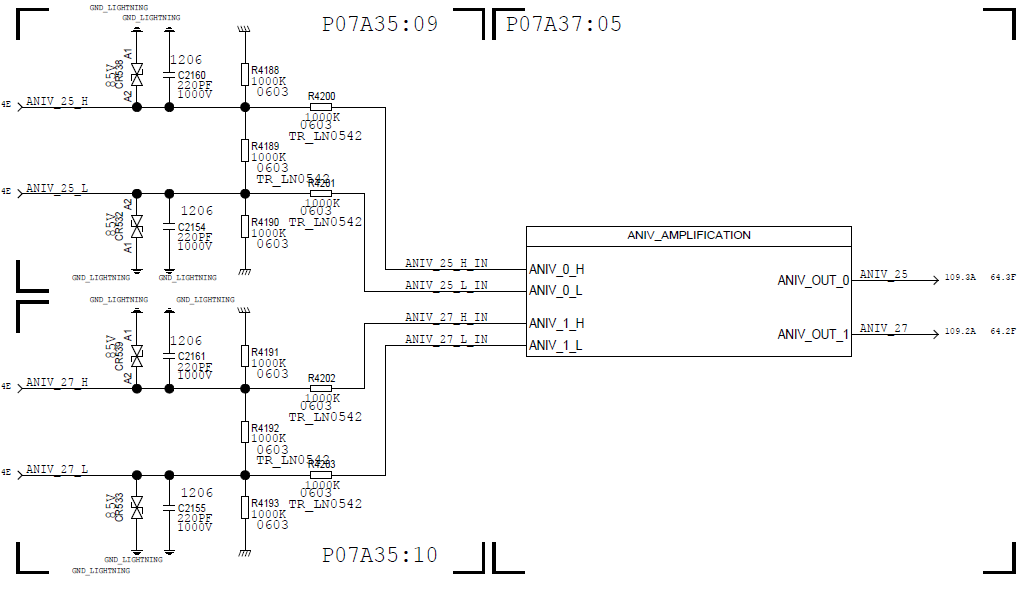
Below is the first version of the specification, which contains the main points, underline during the first meeting about this subject.

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| --- | --- | --- | --- |
| Parameters | Value | Units | State |
| Input signal voltage AC differential | 7.7 | VRMS |  |
| Input signal voltage DC | -10 to +10 | V |  |
| Common mode | From +/-3 up to +/-6 | V |  |
| Power supply | +/-16 | V |  |
| Accuracy (resolution) | <1.57 | mV |  |
| Input signal frequency | DC or 1853 to 5000 | Hz |  |
| ADC acquisition | 100 | KHz |  |
| Input impedance | 100 min (900-1000 if possible) | KΩ |  |
| Input differential capacitance | 3 | nF |  |
| Latency | 1.6 | ms |  |
| Temperature | -55 to 105 | °C |  |
| Short circuit resistance | 28 and 0 (ground) | V |  |

All these information come from the sensors specifications, which gave the input signal voltage and frequency. The others parameters like accuracy, latency…etc, are given by the specifications already fixed by THALES to respect the expectations and need of the final product.

# Schematic details

The following figures are the schematic of the first solution for eFCC and allows having a first idea of what could be possibly integrated.



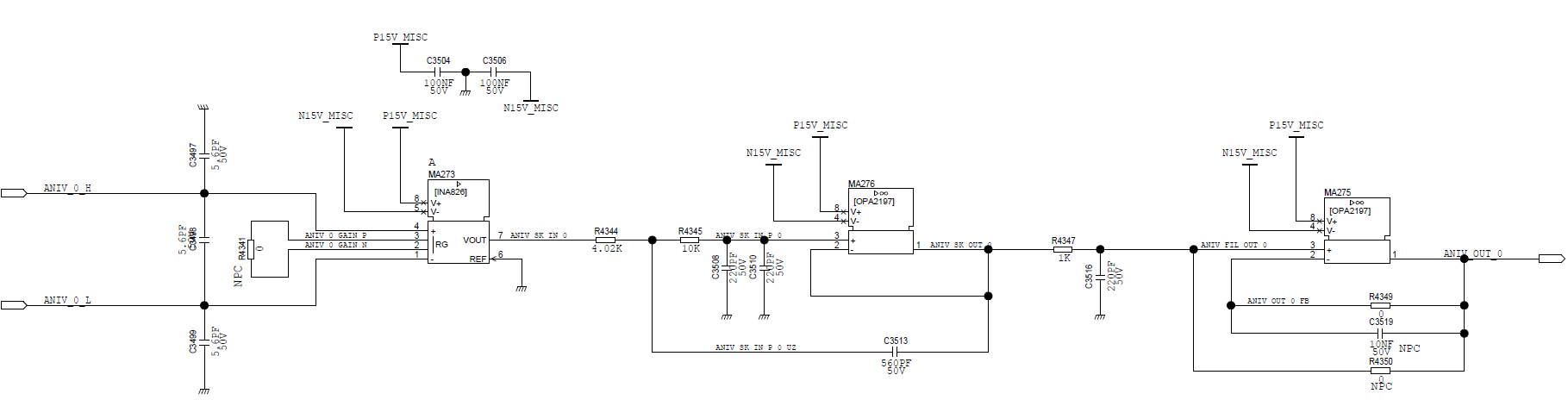


Figure 3: Schematics of the analog chain of the eFCC solution