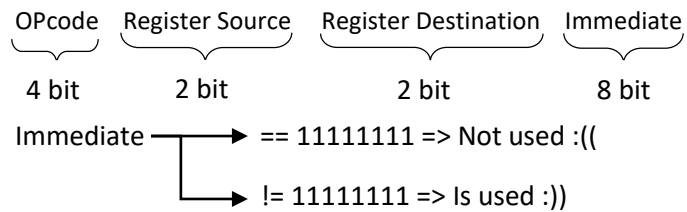


# Instruction Set

- **Instruction Format**



- **Register File**

Name	Address
RegA	00
RegB	01
RegC	10
RegD	11

- **Instructions**

OPcode	Instruction	Action
0000	add rd,rs/imm	rd <= rd + rs / imm
0001	sub rd,rs/imm	rd <= rd - rs / imm
0010	mul rd,rs/imm	rd <= rd * rs / imm
0011	div rd,rs/imm	rd <= rd / rs / imm
0100	and rd,rs/imm	rd <= rd & rs / imm
0101	or rd,rs/imm	rd <= rd   rs / imm
0110	xor rd,rs/imm	rd <= rd ^ rs / imm
0111	not rd	rd <= ~rd
1000	load rd,rs	rd <= MEM[rs]
1001	store rd,rs	MEM[rd] <= rs
1010	mov rd,rs	rd <= rs
1011	movi rd,imm	rd <= imm
1100	jzer rd / label	If rd == 0
1101	jpoz rd / label	If rd > 0
1110	jneg rd / label	If rd < 0
1111	halt	Timeout