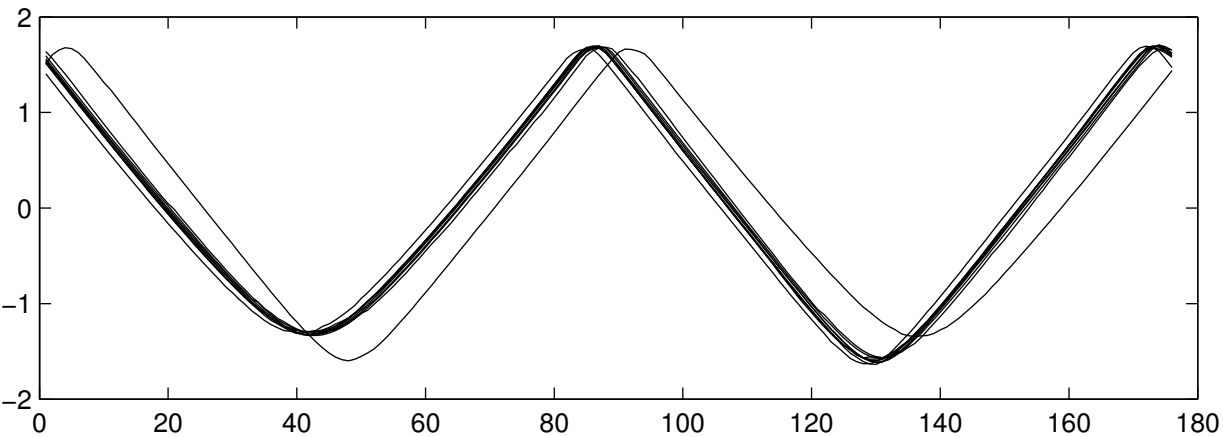


TRN Adiac class: 16 count: 10



TST Adiac class: 16 count: 10

