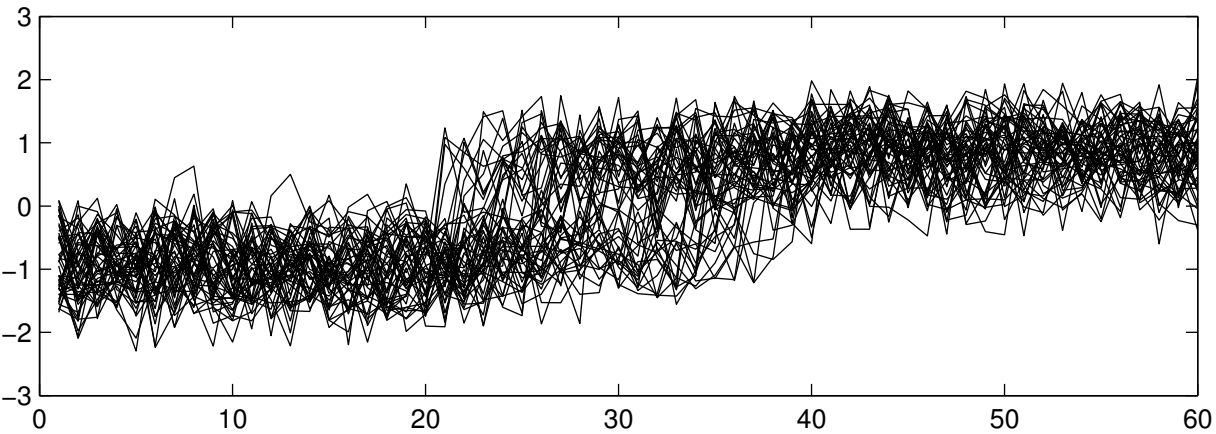


TRN synthetic_control class: 5 count: 50



TST synthetic_control class: 5 count: 50

