

USB1  
MUSB-5B-NE-S175

MP1 5V  
MP2 D-  
MP3 D+  
MP4 ID  
GND

+USB-BUS-POWER-UP  
USB2.0\_DN  
USB2.0\_DP

GND

D1  
PGBl010603MR  
GND

D2  
PGBl010603MR  
GND

The image contains two circuit diagrams, one for USB2 and one for USB3, showing the connection of a USB device to a system. Both diagrams include a 5V regulator (USB2 or USB3) connected to +UB and GND. The regulator's output (5V) is connected to the VCC pin of the USB device. The USB device's GND pin is connected to GND. The USB device's DP+ and DM- pins are connected to the USB2-DP1 and USB2-DM1 pins (for USB2) or USB3-DP2 and USB3-DM2 pins (for USB3). The USB device's D+ and D- pins are connected to the USB2-DP1 and USB2-DM1 pins (for USB2) or USB3-DP2 and USB3-DM2 pins (for USB3). The USB device's VCC and GND pins are connected to the 5V and GND pins of the USB device. The USB device's DP+ and DM- pins are connected to the USB2-DP1 and USB2-DM1 pins (for USB2) or USB3-DP2 and USB3-DM2 pins (for USB3). The USB device's D+ and D- pins are connected to the USB2-DP1 and USB2-DM1 pins (for USB2) or USB3-DP2 and USB3-DM2 pins (for USB3). The USB device's VCC and GND pins are connected to the 5V and GND pins of the USB device. The USB device's DP+ and DM- pins are connected to the USB2-DP1 and USB2-DM1 pins (for USB2) or USB3-DP2 and USB3-DM2 pins (for USB3). The USB device's D+ and D- pins are connected to the USB2-DP1 and USB2-DM1 pins (for USB2) or USB3-DP2 and USB3-DM2 pins (for USB3).

Pin header connection diagram for the ICM-42688-P module (M1). The diagram shows a 16-pin header with pins labeled JP6 through JP9. Pin JP6 is connected to +3V3. Pin JP7 is connected to GND. Pin JP8 is connected to +3V3. Pin JP9 is connected to GND. The module pins are labeled: SPI0\_MISO, I2C1\_SDA, SPI0\_MOSI, I2C1\_SCL, SPI0\_SCK, and +3V3. The module pin headers are labeled: INT2/FSYNC/CLKIN, VDD, VDDIO, AP\_SCLK/SCLK, AP\_SDO/SDA, NC1, AP\_SDO/AD0, and AP\_CS. The module is labeled M1 and ICM-42688-P Module Pin Header.

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