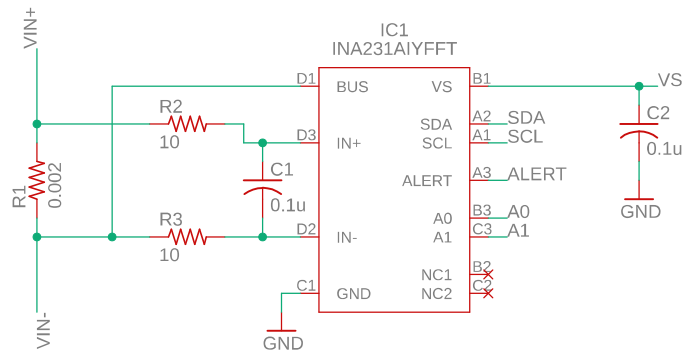
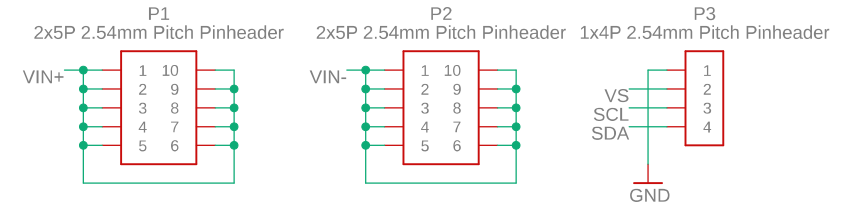


Current-shunt and power monitor



Pinhedders

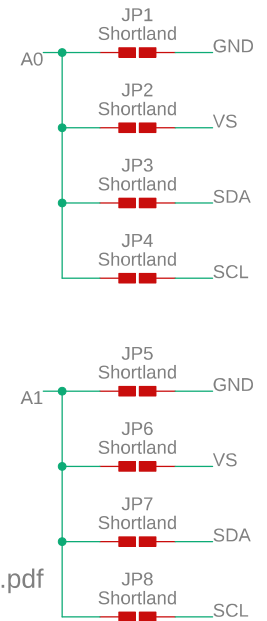


I2C Address Selection by Shortland

Address Pins and Slave Addresses

A1	A0	SLAVE ADDRESS
GND	GND	1000000 (0x64)
GND	VS	1000001 (0x65)
GND	SDA	1000010 (0x66)
GND	SCL	1000011 (0x67)
VS	GND	1000100 (0x68)
VS	VS	1000101 (0x69)
VS	SDA	1000110 (0x70)
VS	SCL	1000111 (0x71)
SDA	GND	1001000 (0x72)
SDA	VS	1001001 (0x73)
SDA	SDA	1001010 (0x74)
SDA	SCL	1001011 (0x75)
SCL	GND	1001100 (0x76)
SCL	VS	1001101 (0x77)
SCL	SDA	1001110 (0x78)
SCL	SCL	1001111 (0x79)

Source: <https://www.ti.com/lit/ds/symlink/ina231.pdf>



Released under the Creative Commons
License: Attribution-NonCommercial-ShareAlike 4.0 International License
<http://creativecommons.org/licenses/by-nc-sa/4.0/>

Title: Power Monitor Module

Design by: Masato Kubotera

Rev.: 1

Date: April 22, 2023

Sheet: 1/1