

Exam Revisio

☰ Tags	ARM
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```
add
sub
rsb
adc
sbc
rsc
op{cond}{s} Rd, Rn, Operand2
-----
and
orr
eor
bic
op{cond}{s} Rd, Rn, Operand2
-----
mov
mvn
mrs
op{cond}{s}, Rd, Operand2
-----
cmp
cmn
op{cond} Rn, Operand2
-----
tst
teq
op{cond}, Rn, Operand2
-----
lsl
lsr
asr
ror
op Rd, Rs
op{cond}{S} Rd, Rm, #expr
-----
-----
-----
mul{cond}{s} Rd, Rm, Rs
mla{cond}{s} Rd, Rm, Rs, Rn
rd can't not be the same as rm
can't use barrerl shift
-----
B{cond} label
BL{cond} label
-----
LDR
STR
```

```

op{cond}{B}{T}, Rd, [Rn]
op{cond}{B} Rd, [Rn, FlexOffset]{!}
op{cond}{B} Rd, label
op{cond}{B}{T}, Rd, [Rn], FlexOffset
FlexOffset:
#expr
{-}Rm{, shift}
-----

```

```

LDM
STM
op{cond}mode Rn{!}, reglist{^}
mode: IA, IB, DA, DB | FD, ED, FA, EA
-----

```

```

float mul and div
mul: 24.8 * 24.8 = 16.16
div: 16.16 / 24.8 = 16.8

```