AXIS_DUMP_GEN

CLK
ENABLE
PAUSE
WORD_LIMIT

FSM

FIFO_OUT_SYNC_XPM

FIFO_OUT_ASYNC_XPM

NBYTES*8
NBYTES

M_AXIS_TDATA
M_AXIS_KEEP
M_AXIS_TVALID
M_AXIS_TREADY
M_AXIS_TLAST
M_AXIS_CLK

ASYNC = FALSE

ASYNC = TRUE

GENERIC
- N_BYTES : width of data bus in bytes.
- ASYNC   : CDC from generator of data and M_AXIS_TDATA

AXI-Stream support TREADY, TLAST, TKEEP, where TKEEP
presented always as '1' for all vector

WORD_CNT_LIMIT : Limit for **number of words** in packet
PAUSE   : Pause between packets

Packet structure presented as array of 8 bit counters.
Example(N_BYTES = 4, WORD_LIMIT = x"00000003", PAUSE = x"00000100"):

		BYTES			
		3	2	1	0
WORDS	0	x"03"	x"02"	x"01"	x"00"
	1	x"07"	x"06"	x"05"	x"04"
	2	x"0b"	x"0a"	x"09"	x"08"
	3	x"0f"	x"0e"	x"0d"	x"0c"

NOT_VALID_DATA   PAUSE

WORDS	0	x"13"	x"12"	x"11"	x"10"
	1	x"17"	x"16"	x"15"	x"14"
	2	x"1b"	x"1a"	x"19"	x"18"
	3	x"1f"	x"1e"	x"1d"	x"1c"

Packet structure
between packets in
sequence mode for
current configuration

M_AXIS_TDATA
M_AXIS_KEEP
M_AXIS_TVALID   M_AXIS_CLK clock domain*
M_AXIS_TREADY
M_AXIS_TLAST

ENABLE
PAUSE   CLK clock domain*
WORD_LIMIT

*If ASYNC = false CLK and M_AXIS_CLK has one clock domain source