

# Fries 81 Cheat Sheet

Instruction Num	Binary	Instruction	Input1	Input2	Input3	Description
0	00000	BNK	N/A	N/A	N/A	do nothing
1	00001	SET	Register (2bit)	Data (8bit)	N/A	Set register value
2	00010	SWP	Register 1 (2bit)	Register 2 (2bit)	N/A	Swap registers values
3	00011	LET	Register (2bit)	Memaddr (9bit)	N/A	Set memory equal to register
4	00100	GET	Register (2bit)	Memaddr (9bit)	N/A	Set register equal to memory
5	00101	ADD	Register 1 (2bit)	Register 2 (2bit)	Register 3 (2bit)	Add registers 1 and 2. Store value in register 3
6	00110	SUB	Register 1 (2bit)	Register 2 (2bit)	Register 3 (2bit)	Subtract register 2 from 1. Store value in register 3
7	00111	MUL	Register 1 (2bit)	Register 2 (2bit)	Register 3 (2bit)	Multiply registers 1 and 2. Store value in register 3
8	01000	DIV	Register 1 (2bit)	Register 2 (2bit)	Register 3 (2bit)	Divide register 2 from 1. Store value in register 3
9	01001	INC	Register (2bit)	N/A	N/A	Increment register value by 1
10	01010	DNC	Register (2bit)	N/A	N/A	Decrement register value by 1
11	01011	AND	Register 1 (2bit)	Register 2 (2bit)	Register 3 (2bit)	Perform "And" on registers 1 and 2. Store value in register 3
12	01100	OR	Register 1 (2bit)	Register 2 (2bit)	Register 3 (2bit)	Perform "Or" on registers 1 and 2. Store value in register 3
13	01101	NOT	Register 1 (2bit)	Register 2 (2bit)	N/A	Perform "Not" on registers 1. Store value in register 2
14	01110	XOR	Register 1 (2bit)	Register 2 (2bit)	Register 3 (2bit)	Perform "Xor" on registers 1 and 2. Store value in register 3
15	01111	JMP	Line (11bit)	N/A	N/A	Jump to line in program
16	10000	CMP	Operator (3bit)	Register 1 (2bit)	Register 2 (2bit)	If condition is false skip the next line in program

17	10001	RED	Register (2bit)	N/A	N/A	Read value from input pins and store in register
18	10010	WRT	Register (2bit)	N/A	N/A	Write value in register to output pins
19	10011	SHD	N/A	N/A	N/A	shutdown system

Operator Num	Binary	Operator	Description
0	000	>	greater than
1	001	<	less than
2	010	==	equal to
3	011	>=	greater than or equal to
4	100	<=	less than or equal to
5	101	!=	not equal to

Register Num	Binary	Register Name
0	00	RA
1	01	RB
2	10	RC
3	11	RD