

Name of Student:

Names of Assessors:

Date of Assessment:

BASED ON THE EXPERIMENT

Overall Experiment Grade:

1. How much has the student done?

☐

Beyond expectation

☐

Some extra

☐

Compulsory

☐

Some missing

☐

Lots missing

2. Ability to give answers (notes allowed)

☐

Confident

☐

Mostly

☐

Acceptable

☐

Sometimes

☐

Unsure

3. Understanding of materials inside experiments

☐

Complete

☐

Mostly

☐

Acceptable

☐

Sometimes

☐

Unsure

BASED ON UNDERSTANDING

Overall Understanding Grade:

4. Mastery of Verilog

☐

Excellent

☐

V. Good

☐

Acceptable

☐

Poor

☐

V. Poor

5. Understand of FPGA and Quartus tools

☐

Excellent

☐

V. Good

☐

Acceptable

☐

Poor

☐

V. Poor

6. Depth of understanding in general

☐

Broad and Deep

☐

Good

☐

Average

☐

Less than average

☐

Poor

FEEDBACK TO STUDENTS:

Overall Grade: