

IDc:1

imm\_generate

instruction(31:0)

instruction(31:0)

out(63:0)

imm(63:0)

g

reg\_file

r1(4:0)

out1(63:0)

out1(63:0)

r2(4:0)

w(4:0)

wd(63:0)

wd(63:0)

write\_control

write\_control

out2(63:0)

out2(63:0)

s

IDc

