## **Drill Problems**

2.1 Perform the following number system conversions:

(a) 
$$1101011_2 = ?_{16}$$

(b) 
$$174003_8 = ?_2$$

100 4 101

ret

1001 9 1010 A

1100 6 liot D 1110

Titte 🕝

1011

(c) 
$$10110111_2 = ?_{16}$$

(d) 
$$67.24_8 = ?_2$$

(e) 
$$10100.1101_2 = ?_{16}$$
 (f)  $F3A5_{16} = ?_2$ 

(f) 
$$F3A5_{16} = ?_2$$

(g) 
$$11011001_2 = ?_8$$
 (h)  $AB3D_{16} = ?_2$ 

(h) 
$$AB3D_{16} = ?_2$$

(i) 
$$101111.0111_2 = ?_8$$
 (j)  $15C.38_{16} = ?_2$ 

(j) 
$$15C.38_{16} = ?_2$$

2.2 Convert the following octal numbers into binary and hexadecimal:

(a) 
$$1234_8 = ?_2 = ?_{16}$$

(b) 
$$174637_8 = ?_2 = ?_{16}$$

(c) 
$$365517_8 = ?_2 = ?_{16}$$

(d) 
$$2535321_8 = ?_2 = ?_{16}$$

(e) 
$$7436.11_8 = ?_2 = ?_{16}$$

(e) 
$$7436.11_8 = ?_2 = ?_{16}$$
 (f)  $45316.7414_8 = ?_2 = ?_{16}$ 

Convert the following hexadecimal numbers into binary and octal: 2.3

(a) 
$$1023_{16} = ?_2 = ?_8$$
 (b)  $7E6A_{16} = ?_2 = ?_8$ 

(b) 
$$7E6A_{16} = ?_2 = ?_8$$

(c) 
$$ABCD_{16} = ?_2 = ?_8$$
 (d)  $C350_{16} = ?_2 = ?_8$ 

(d) 
$$C350_{16} = ?_2 = ?_8$$

(e) 
$$9E36.7A_{16} = ?_2 = ?_8$$

(e) 
$$9E36.7A_{16} = ?_2 = ?_8$$
 (f)  $DEAD.BEEF_{16} = ?_2 = ?_8$ 

- 2.4 What are the octal values of the four 8-bit bytes in the 32-bit number with octal representation 32107654321<sub>8</sub>?
- Convert the following numbers into decimal: 2.5

(a) 
$$1101011_2 = ?_{10}$$

(b) 
$$174003_8 = ?_{10}$$

(c) 
$$10110111_2 = ?_{10}$$

(d) 
$$67.24_8 = ?_{10}$$

(e) 
$$10100.1101_2 = ?_{10}$$

(f) 
$$F3A5_{16} = ?_{10}$$

(g) 
$$12010_3 = ?_{10}$$

(h) 
$$AB3D_{16} = ?_{10}$$

(i) 
$$7156_8 = ?_{10}$$

(j) 
$$15C.38_{16} = ?_{10}$$

Perform the following number-system conversions: 2.6

(a) 
$$125_{10} = ?_2$$

(b) 
$$3489_{10} = ?_8$$

(c) 
$$209_{10} = ?_2$$

(d) 
$$9714_{10} = ?_8$$

(e) 
$$132_{10} = ?_2$$

(f) 
$$23851_{10} = ?_{16}$$

(g) 
$$727_{10} = ?_5$$

(h) 
$$57190_{10} = ?_{16}$$

(i) 
$$1435_{10} = ?_8$$

(j) 
$$65113_{10} = ?_{16}$$

2.7 Add the following pairs of binary numbers, showing all carries:

2.8 Repeat Drill 2.7 using subtraction instead of addition, and showing borrows instead of carries.

2.9 Add the following pairs of octal numbers:

2.10 Add the following pairs of hexadecimal numbers:

$$+27E6$$

2.11 Write the 8-bit signed-magnitude, two's-complement, and ones'-complement representations for each decimal number: +25, +120, +82, -42, -6, -111.

2.12 Indicate whether or not overflow occurs when adding the following 8-bit two's-complement numbers:

2.16 Here's a problem to help keep you awake at night. What is the hexadecimal equivalent of 12648430<sub>10</sub>?

- 4.6 Use switching-algebra theorems to simplify each of the following logic functions:
- $F = W \cdot X \cdot Y \cdot Z \cdot (W \cdot X \cdot Y \cdot Z' + W \cdot X' \cdot Y \cdot Z + W' \cdot X \cdot Y \cdot Z + W \cdot X \cdot Y' \cdot Z)$ (a)
- $F = A \cdot B + A \cdot B \cdot C' \cdot D + A \cdot B \cdot D \cdot E' + A' \cdot B \cdot C' \cdot E + A' \cdot B' \cdot C' \cdot E$ (b)
- $F = M \cdot R \cdot P + Q \cdot O' \cdot R' + M \cdot N + O \cdot N \cdot M + Q \cdot P \cdot M \cdot O'$ (c)
- Write the truth table for each of the following logic functions: 4.7
- $F = X' \cdot Y + X' \cdot Y' \cdot Z$ (a)
- (b)  $\mathbf{F} = \mathbf{W}' \cdot \mathbf{X} + \mathbf{Y}' \cdot \mathbf{Z}' + \mathbf{X}' \cdot \mathbf{Z}$
- (c)
- $F = W' \cdot X + W \cdot (Y' + Z)$  (d)  $F = A \cdot B' + B' \cdot C + C \cdot D' + C \cdot A'$
- (e)  $F = V \cdot W' + X \cdot Y' \cdot Z$  (f)  $F = (A' + B' \cdot C \cdot D) \cdot (B' + C' + D \cdot E')$
- (g)  $F = (W \cdot Z)' \cdot (X' + Y')'$  (h) F = (((A + B')' + C)' + D)'
- (i)  $F = (A' + B + C') \cdot (A' + B' + D) \cdot (B + C + D') \cdot (A + B + C + D)$
- 4.8 Write the truth table for each of the following logic functions:
  - (a)  $F = X' \cdot Y' \cdot Z' + X \cdot Y \cdot Z + X \cdot Y' \cdot Z$
  - (b)  $F = M' \cdot N' + M \cdot P + N' \cdot P$
  - (c)  $F = A \cdot B + A \cdot B' \cdot C' + A' \cdot B \cdot C$
  - (d)  $F = A' \cdot B \cdot (C \cdot B \cdot A' + B \cdot C')$
  - (e)  $F = X \cdot Y \cdot (X' \cdot Y \cdot Z + X \cdot Y' \cdot Z + X \cdot Y \cdot Z' + X' \cdot Y' \cdot Z)$
  - (f)  $F = M \cdot N + M' \cdot N' \cdot P'$
  - (g)  $F = (A + A') \cdot B + B \cdot A \cdot C' + C \cdot (A + B') \cdot (A' + B)$
  - (h)  $F = X \cdot Y' + Y \cdot Z + Z' \cdot X$
- 4.14 Using Karnaugh maps, find a minimal sum-of-products expression for each of the following logic functions. Indicate the distinguished 1-cells in each map.

  - (a)  $F = \Sigma_{X,Y,Z}(1,3,5,6,7)$  (b)  $F = \Sigma_{W,X,Y,Z}(1,4,5,6,7,9,14,15)$

  - $\begin{array}{lll} \text{(c)} & \mathsf{F} = \prod_{\mathsf{W},\mathsf{X},\mathsf{Y}} (1,4,5,6,7) & \text{(d)} & \mathsf{F} = \Sigma_{\mathsf{W},\mathsf{X},\mathsf{Y},\mathsf{Z}} (0,1,6,7,8,9,14,15) \\ \text{(e)} & \mathsf{F} = \prod_{\mathsf{A},\mathsf{B},\mathsf{C},\mathsf{D}} (4,5,6,13,15) & \text{(f)} & \mathsf{F} = \Sigma_{\mathsf{A},\mathsf{B},\mathsf{C},\mathsf{D}} (4,5,6,11,13,14,15) \end{array}$
- Using Karnaugh maps, find a minimal sum-of-products expression for each of the following logic functions. Indicate the distinguished 1-cells in each map.

  - (a)  $F = \Sigma_{A,B,C}(0,1,2,4)$  (b)  $F = \Sigma_{W,X,Y,Z}(1,4,5,6,11,12,13,14)$

  - (c)  $F = \prod_{A,B,C} (1,2,6,7)$  (d)  $F = \sum_{W,X,Y,Z} (0,1,2,3,7,8,10,11,15)$

  - (e)  $F = \Sigma_{WX,YZ}(1,2,4,7,8,11,13,14)$  (f)  $F = \prod_{A,B,C,D}(1,3,4,5,6,7,9,12,13,14)$

- 4.18 Using Karnaugh maps, find a minimal sum-of-products expression for each of the following logic functions. Indicate the distinguished 1-cells in each map.
  - (a)  $F = \Sigma_{WXYZ}(0,1,3,5,14) + d(8,15)$
  - (b)  $F = \Sigma_{WXYZ}(0,1,2,8,11) + d(3,9,15)$
  - (c)  $F = \Sigma_{ABCD}(4, 6, 7, 9, 13) + d(12)$
  - (d)  $F = \Sigma_{ABCD}(1, 5, 12, 13, 14, 15) + d(7, 9)$
  - (e)  $F = \Sigma_{WX,YZ}(4, 5, 9, 13, 15) + d(0, 1, 7, 11, 12)$
- 4.19 For each of the following logic expressions, find all of the static hazards in the corresponding two-level AND-OR or OR-AND circuit, and design a hazard-free circuit that realizes the same logic function.
  - (a)  $F = W \cdot X + W' \cdot Y'$
  - (b)  $F = W \cdot X' \cdot Y' + X \cdot Y' \cdot Z + X \cdot Y$
  - (c)  $F = W \cdot Y + W' \cdot Z' + X \cdot Y' \cdot Z$
  - (d)  $F = W \cdot X' + Y' \cdot Z + W' \cdot X \cdot Y \cdot Z + W \cdot X \cdot Y \cdot Z'$
  - (e)  $F = (W' + X + Y') \cdot (X' + Z')$
  - (f)  $F = (W + Y' + Z') \cdot (W' + X' + Z') \cdot (X' + Y + Z)$
  - (g)  $F = (W + Y + Z') \cdot (W + X' + Y + Z) \cdot (X' + Y') \cdot (X + Z)$
- 6.20 Show how to build each of the following single- or multiple-output logic functions using one or more 74x138 or 74x139 binary decoders and NAND gates. (Hint: Each realization should be equivalent to a sum of minterms.)
  - (a)  $F = \sum_{X Y \neq 7} (2,4,7)$
- (b)  $F = \prod_{A B C} (3,4,5,6,7)$
- (c)  $F = \sum_{A,B,C,D}(0,2,10,12)$  (d)  $F = \sum_{W,X,Y,Z}(2,3,4,5,8,10,12,14)$
- (e)  $F = \sum_{WX,Y} (0,2,4,5)$
- (f)  $F = \sum_{A.B.C} (2,6)$
- $G = \sum_{W,X,Y} (1,2,3,6)$

- $G = \sum_{C,D,F} (0,2,3)$
- 6.21 What's terribly wrong with the circuit in Figure X6.21? Suggest a change that eliminates the terrible problem.

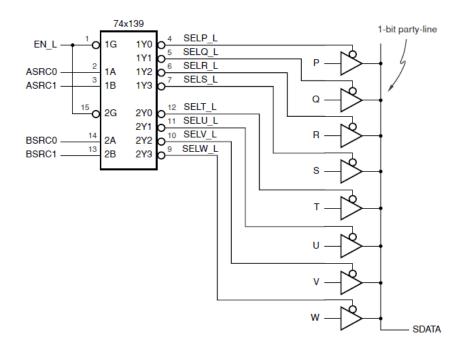


Figure X6.21

## **SOLUTIONS:**

2.1 (a) 
$$1101011_2 = 6B_{16}$$
 (b)  $174003_8 = 11111000000000011_2$ 

(c) 
$$10110111_2 = B7_{16}$$
 (d)  $67.24_8 = 110111.0101_2$ 

(e) 
$$10100.1101_2 = 14.D_{16}$$
 (f)  $F3A5_{16} = 1111001110100101_2$ 

(g) 
$$11011001_2 = 331_8$$
 (h)  $AB3D_{16} = 1010101100111101_2$ 

(i) 
$$101111.0111_2 = 57.34_8$$
 (j)  $15C.38_{16} = 101011100.00111_2$ 

$$2.2$$
 (a)  $1234_8 = 10100111100_2 = 29C_{16}$ 

(b) 
$$174637_8 = 1111100110011111_2 = F99F_{16}$$

(c) 
$$365517_8 = 11110101101001111_2 = 1EB4F_{16}$$

(d) 
$$2535321_8 = 10101011101011010001_2 = ABAD1_{16}$$

(e) 
$$7436.11_8 = 111100011110.001001_2 = F1E.24_{16}$$

(f) 
$$45316.7414_8 = 100101011001110.111100001100_2 = 4ACE.F0C_{16}$$

2.3 (a) 
$$1023_{16} = 1000000100011_2 = 10043_8$$

(b) 
$$7E6A_{16} = 1111111001101010_2 = 77152_8$$

(c) 
$$ABCD_{16} = 1010101111001101_2 = 125715_8$$

(d) 
$$C350_{16} = 1100001101010000_2 = 141520_8$$

(e) 
$$9E36.7A_{16} = 1001111000110110.0111101_2 = 117066.364_8$$

(f) 
$$DEAD.BEEF_{16} = 1101111010101101.10111110111101111_2 = 157255.575674_8$$

$$2.4 \quad 32107654321_8 = 11010001\ 00011111\ 01011000\ 11010001_2$$
  
=  $(011\ 010\ 001)\ (000\ 011\ 111)\ (001\ 011\ 000)\ (011\ 010\ 001)_2 = (321)\ (037)\ (130)\ (321)_8$ 

2.5 (a) 
$$1101011_2 = 107_{10}$$
 (b)  $174003_8 = 63491_{10}$ 

(c) 
$$10110111_2 = 183_{10}$$
 (d)  $67.24_8 = 55.3125_{10}$ 

(e) 
$$10100.1101_2 = 20.8125_{10}$$
 (f)  $F3A5_{16} = 62373_{10}$ 

(g) 
$$12010_3 = 138_{10}$$
 (h)  $AB3D_{16} = 43837_{10}$ 

(i) 
$$7156_8 = 3694_{10}$$
 (j)  $15C.38_{16} = 348.21875_{10}$ 

$$2.6$$
 (a)  $125_{10} = 11111101_2$ 

(b) 
$$3489_{10} = 6641_8$$

(c) 
$$209_{10} = 11010001_2$$

(d) 
$$9714_{10} = 22762_8$$

(e) 
$$132_{10} = 1000100_2$$

(f) 
$$23851_{10} = 5D2B_{16}$$

(g) 
$$727_{10} = 10402_5$$

(h) 
$$57190_{10} = DF66_{16}$$

(i) 
$$1435_{10} = 2633_8$$

(j) 
$$65113_{10} = FE59_{16}$$

1110010

2.11

## 2.16 C0FFEE

4.6 The answers for parts (a), (b), (c) are as follows.

$$\begin{split} W \cdot X \cdot Y \cdot Z \cdot (W \cdot X \cdot Y \cdot Z' + W \cdot X' \cdot Y \cdot Z + W' \cdot X \cdot Y \cdot Z + W \cdot X \cdot Y' \cdot Z) \\ &= W \cdot X \cdot Y \cdot Z \cdot W \cdot X \cdot Y \cdot Z' + W \cdot X \cdot Y \cdot Z \cdot W \cdot X' \cdot Y \cdot Z \\ &+ W \cdot X \cdot Y \cdot Z \cdot W' \cdot X \cdot Y \cdot Z + W \cdot X \cdot Y \cdot Z \cdot W \cdot X \cdot Y' \cdot Z \\ &= 0 + 0 + 0 + 0 (T6', T5', T2') \\ &= 0 (A4') \end{split}$$

$$A \cdot B + A \cdot B \cdot C' \cdot D + A \cdot B \cdot D \cdot E' + A' \cdot B \cdot C' \cdot E + A' \cdot B' \cdot C' \cdot E$$

$$= A \cdot B + A \cdot B \cdot D \cdot E' + A' \cdot B \cdot C' \cdot E + A' \cdot B' \cdot C' \cdot E$$

$$= A \cdot B + A' \cdot B \cdot C' \cdot E + A' \cdot B' \cdot C' \cdot E$$

$$= A \cdot B + A' \cdot C' \cdot E$$

$$= A \cdot B + A' \cdot C' \cdot E$$

$$= A \cdot B + A' \cdot C' \cdot E$$

$$= A \cdot B + A' \cdot C' \cdot E$$

$$= A \cdot B + A' \cdot C' \cdot E$$

$$= A \cdot B + A' \cdot C' \cdot E$$

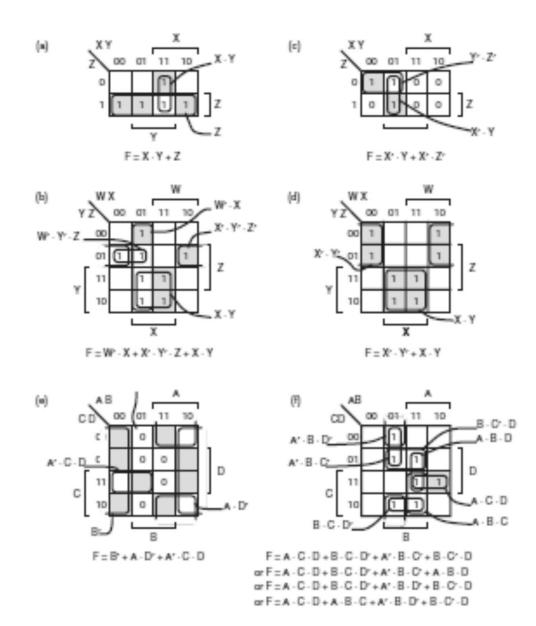
$$= A \cdot B + A' \cdot C' \cdot E$$

$$\begin{split} &M \cdot R \cdot P + Q \cdot O' \cdot R' + M \cdot N + Q \cdot P \cdot M \cdot O' + O \cdot N \cdot M \\ &= M \cdot R \cdot P + Q \cdot O' \cdot R' + Q \cdot P \cdot M \cdot O' + M \cdot N + O \cdot N \cdot M \; (T6) \\ &= M \cdot R \cdot P + Q \cdot O' \cdot R' + Q \cdot P \cdot M \cdot O' + M \cdot N \; (T9) \\ &= R \cdot (M \cdot P) + R' \cdot (Q \cdot O') + (M \cdot P) \cdot (Q \cdot O') + M \cdot N \; (T6', T7') \\ &= R \cdot (M \cdot P) + R' \cdot (Q \cdot O') + M \cdot N \; (T11) \\ &= R \cdot M \cdot P + R' \cdot Q \cdot O' + M \cdot N \; (T7') \end{split}$$

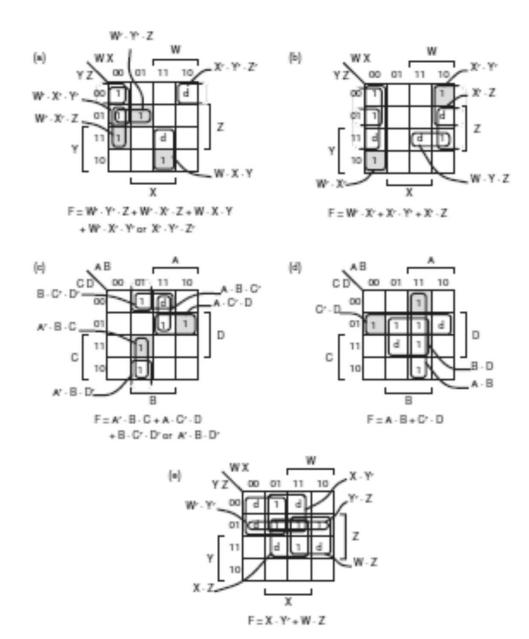
| Z F | (b)   | W   | Χ   | Υ   | Z  | F   | (c)   | W  | Χ   | Υ   | Z   | F   | (d)   | Α   | В   | С   | D   | F   | +   |
|-----|---|---|---|---|--|---|---|--|---|---|---|---|---|---|---|---|---|---|---|
| 0 0 |   | 0   | 0   | 0   | 0  | 1   |   | 0  | 0   | 0   | 0   | 0   |   | 0   | 0   | 0   | 0   | 0   |   |
| 1 1 |   | 0   | 0   | 0   | 1  | 1   |   | 0  | 0   | 0   | 1   | 0   |   | 0   | 0   | 0   | 1   | 0   |   |
| 0 1 |   | 0   | 0   | 1   | 0  | 0   |   | 0  | 0   | 1   | 0   | 0   |   | 0   | 0   | 1   | 0   | 1   |   |
| 1 1 |   | 0   | 0   | 1   | 1  | 1   |   | 0  | 0   | 1   | 1   | 0   |   | 0   | 0   | 1   | 1   | 1   |   |
| 0 0 | 1   | 0   | 1   | 0   | 0  | 1   |   | 0  | 1   | 0   | 0   | 1   |   | 0   | 1   | 0   | 0   | 0   |   |
| 1 0 |   | 0   | 1   | 0   | 1  | 1   |   | 0  | 1   | 0   | 1   | 1   |   | 0   | 1   | 0   | 1   | 0   |   |
| 0 0 |   | 0   | 1   | 1   | 0  | 1   |   | 0  | 1   | 1   | 0   | 1   |   | 0   | 1   | 1   | 0   | 1   |   |
| 1 0 |   | 0   | 1   | 1   | 1  | 1   |   | 0  | 1   | 1   | 1   | 1   |   | 0   | 1   | 1   | 1   | 1   |   |
|     | •   | 1   | 0   | 0   | 0  | 1   |   | 1  | 0   | 0   | 0   | 1   |   | 1   | 0   | 0   | 0   | 1   |   |
|     |   | 1   | 0   | 0   | 1  | 1   |   | 1  | 0   | 0   | 1   | 1   |   | 1   | 0   | 0   | 1   | 1   |   |
|     |   | 1   | 0   | 1   | 0  | 0   |   | 1  | 0   | 1   | 0   | 0   |   | 1   | 0   | 1   | 0   | 1   |   |
|     |   | 1   | 0   | 1   | 1  | 1   |   | 1  | 0   | 1   | 1   | 1   |   | 1   | 0   | 1   | 1   | 1   |   |
|     |   | 1   | 1   | 0   | 0  | 1   |   | 1  | 1   | 0   | 0   | 1   |   | 1   | 1   | 0   | 0   | 0   |   |
|     |   | 1   | 1   | 0   | 1  | 0   |   | 1  | 1   | 0   | 1   | 1   |   | 1   | 1   | 0   | 1   | 0   |   |
|     |   | 1   | 1   | 1   | 0  | 0   |   | 1  | 1   | 1   | 0   | 0   |   | 1   | 1   | 1   | 0   | 1   |   |
|     |   | 1   | 1   | 1   | 1  | 0   |   | 1  | 1   | 1   | 1   | 1   |   | 1   | 1   | 1   | 1   | 0   |   |
|     | 0 0<br>1 1<br>0 1<br>1 1<br>0 0<br>1 0<br>0 0 | 0 0<br>1 1<br>0 1<br>1 1<br>0 0<br>1 0<br>0 0 | 0 0 0 0 1 1 0 0 0 1 1 0 0 0 0 0 1 0 0 1 | 0       0       0       0         1       1       0       0         0       1       1       0       0         1       1       0       0       1         1       0       0       1       1         1       0       0       1       1         1       0       1       0       1       0         1       0       1       0       1       1       0         1       0       1 | 0       0       0       0       0         1       1       0       0       0       1         1       1       0       0       1       0       0       1       0       0       1       0       0       1       0       0       1       1       0       0       1       1       1       0       0       1       1       1       0       0       1       1       1       0       0       1       1       1       0       0       1       1       1       0       0       1       1       1       0       0      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0 0 0 0         0 0 0 0         0 0 0 0         0 0 0 0         0 0 0 0         0 0 0 0         0 0 0</td></td> | 0       0       0       0       0       1       0         1       1       0       0       0       1       1       0         0       1       0       0       1       0       0       0       0       0       0       0       1       0       0       1       1       0       0       0       1       1       0       0       1       1       0       0       1       1       0       0       1       1       1       0       0       1       1       1       0       0       1       1       1       0       0       1       1       1       0       0       1       1       1       1       0       0       1       1       1       1       0       0       1       1       1       1       0       0       1 <td>0       0       0       0       0       1       0       0         1       1       0       1       0       0       1       0       1       0       1       0       1       0       1       0       1  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0 1 1 1 1 1 0 1       0 1 1 1 1 1 0 1         1 0       0 1 1 1 1 1 1 0 0 1 1 1 1 1 0 0 1       0 1 1 1 1 1 0 0       0 1 1 1 1 1 0 0         1 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 | 0         0 | 0 0         0 0 0 0 1         0 0 0 0 0 0         0 0 0 0 0 0         0 0 0 0 0 0         0 0 0 0 0 0         0 0 0 0 0 0         0 0 0 0 0 0 0         0 0 0 0 0 0 0         0 0 0 0 0 0 0         0 0 0 0 0 0 0         0 0 0 0 0 0 0         0 0 0 0 0 0 0         0 0 0 0 0 0         0 0 0 0 0 0         0 0 0 0 0 0 0         0 0 0 0 0 0         0 0 0 0 0 0         0 0 0 0 0 0         0 0 0 0 0 0         0 0 0 0 0 0         0 0 0 0 0 0         0 0 0 0 0 0         0 0 0 0 0 0         0 0 0 0 0         0 0 0 0 0         0 0 0 0 0         0 0 0 0 0         0 0 0 0 0         0 0 0 0 0         0 0 0 0 0         0 0 0 0 0         0 0 0 0 0         0 0 0 0 0         0 0 0 0 0         0 0 0 0 0         0 0 0 0 0         0 0 0 0 0         0 0 0 0 0         0 0 0 0         0 0 0 0         0 0 0 0         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| (e)         | (f)         | (g)       | (i)       |
|-------------|-------------|-----------|-----------|
| VWXYZF      | ABCDEF      | WXYZF     | A B C D F |
| 0 0 0 0 0 0 | 0 0 0 0 0 1 | 0 0 0 0 0 | 00000     |
| 000010      | 0 0 0 0 1 1 | 0 0 0 1 0 | 0 0 0 1 0 |
| 0 0 0 1 0 0 | 0 0 0 1 0 1 | 0 0 1 0 0 | 0 0 1 0 1 |
| 0 0 0 1 1 0 | 0 0 0 1 1 1 | 0 0 1 1 0 | 0 0 1 1 1 |
| 0 0 1 0 0 0 | 0 0 1 0 0 1 | 0 1 0 0 0 | 0 1 0 0 1 |
| 0 0 1 0 1 1 | 0 0 1 0 1 1 | 0 1 0 1 0 | 0 1 0 1 1 |
| 0 0 1 1 0 0 | 0 0 1 1 0 1 | 0 1 1 0 1 | 0 1 1 0 1 |
| 0 0 1 1 1 0 | 0 0 1 1 1 1 | 0 1 1 1 1 | 0 1 1 1 1 |
| 010000      | 0 1 0 0 0 1 | 10000     | 1 0 0 0 1 |
| 0 1 0 0 1 0 | 0 1 0 0 1 1 | 1 0 0 1 0 | 10010     |
| 0 1 0 1 0 0 | 0 1 0 1 0 1 | 10100     | 1010 0    |
| 0 1 0 1 1 0 | 0 1 0 1 1 1 | 10110     | 1011 0    |
| 01100 0     | 0 1 1 0 0 0 | 11000     | 1100 0    |
| 0 1 1 0 1 1 | 0 1 1 0 1 0 | 1 1 0 1 0 | 1 1 0 1 1 |
| 0 1 1 1 0 0 | 0 1 1 1 0 1 | 11101     | 1110 0    |
| 0 1 1 1 1 0 | 0 1 1 1 1 0 | 1 1 1 1 0 | 11111     |
| 100001      | 100000      |           |           |
| 100011      | 100010      | ( (h)     |           |
| 100101      | 100100      | A B C D F |           |
| 100111      | 100110      | 0 0 0 0 0 |           |
| 101001      | 101000      | 0 0 0 1 0 |           |
| 101011      | 101010      | 0 0 1 0 1 |           |
| 101101      | 101101      | 0 0 1 1 0 |           |
| 101111      | 101111      | 0 1 0 0 1 |           |
| 110000      | 110000      | 0 1 0 1 0 |           |
| 110010      | 110010      | 0 1 1 0 1 |           |
| 11010 0     | 11010 0     | 0 1 1 1 0 |           |
| 11011 0     | 11011 0     | 1 0 0 0 0 |           |
| 111000      | 111000      | 10010     |           |
| 111011      | 11101 0     | 10101     |           |
| 111100      | 1111100     | 10110     |           |
| 1 1 1 1 1 0 | 1 1 1 1 1 0 | 1 1 0 0 0 |           |
|             |             | 1 1 0 1 0 |           |
|             |             | 1 1 1 0 1 |           |
|             |             | 1 1 1 1 0 |           |

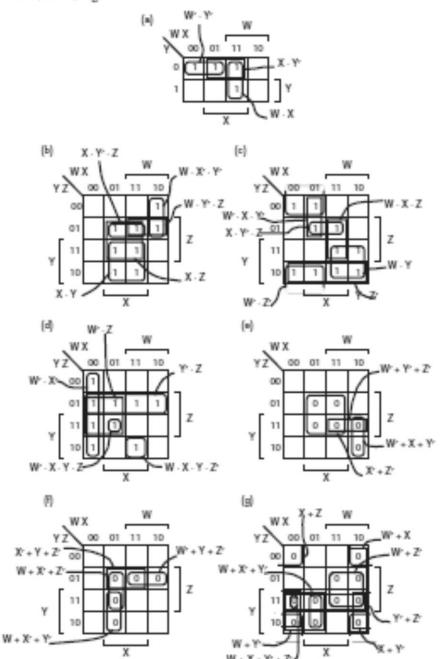
4.8 est absent des solutions disponibles chez Wakerly.

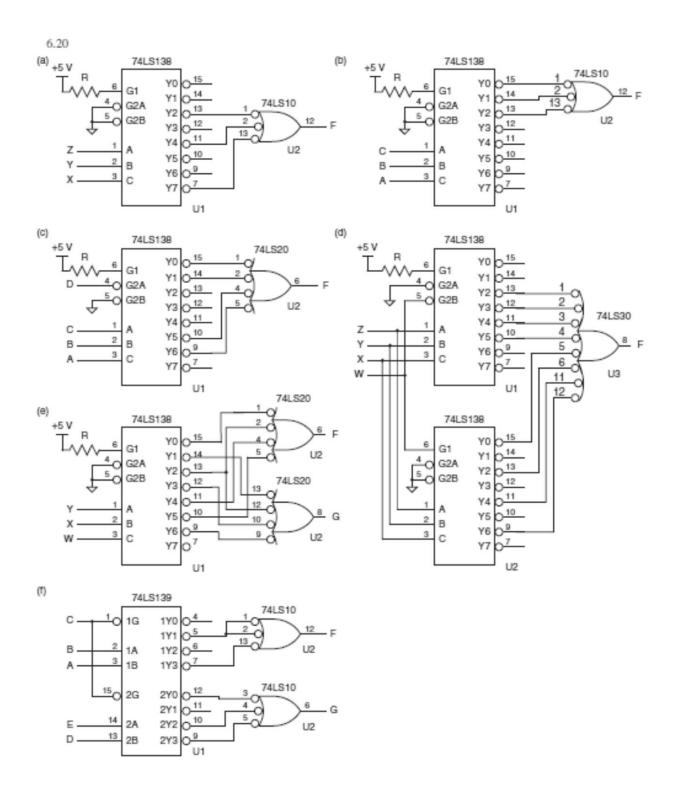


4.15 est absent des solutions disponibles chez Wakerly.

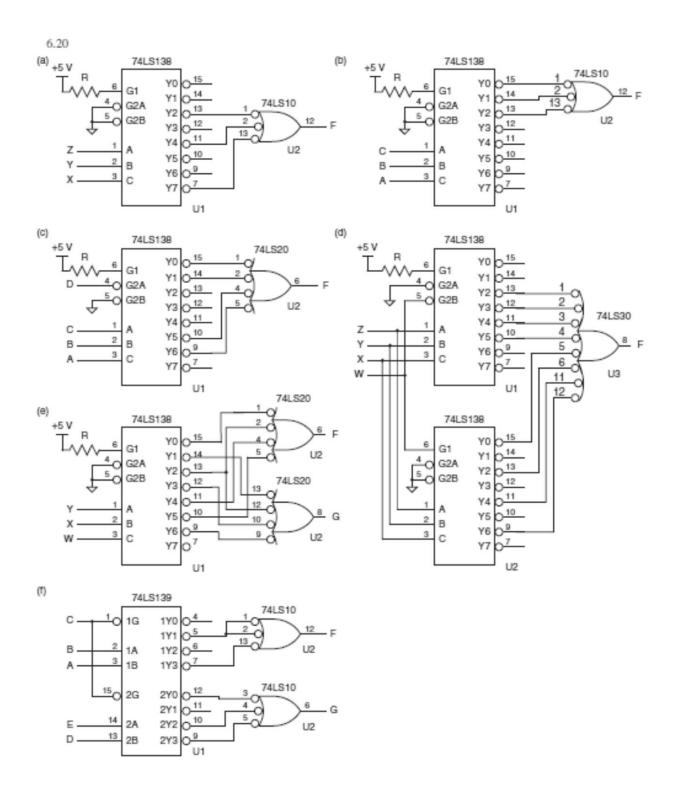


4.19 Consensus terms that must be added to cover the hazards are "circled" with rectangles. In (d), the W'-X-Y-Z term may be eliminated in the hazard-free design. In (g), terms W + X' + Y' and W + X + Y' + Z' may be eliminated in the hazard-free design.





6.21 Both halves of the '139 are enabled simultaneously when EN\_L is asserted. Therefore, two three-state drivers will be enabled to drive SDATA at the same time. Perhaps the designer forgot to put an extra inverter on the signal going to 1G or 2G, which would ensure that exactly one source drives SDATA at all times.



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