











DO->RX

DT<-TX

T_{PS}
D₂

D₃

D₄

D₅

D₆

D₇

D₈

D₉

D₁₀

D₁₁

D₁₂

D₁₃

D₁₄

GND

NC

I₂C_SDA

I₂C_CLK

GDO-D₂

GD₁-D₃

3V₃

GND

T_{P1}

A₅
GND

RXD
A₄

TXD
A₃

DTR
A₂

RTS
A₁

3V₃
AO

ANALOG IN

5V

GND

5V

GND

3V₃

RESET

POWER

NC

NC

5V

NC

NC

5V