

Gabarito Exercícios Área 1 – Parte II – Circuitos CMOS

Questão 1

a) Função Booleana

$$F = \sim(A.B)$$

Tabela Verdade

A	B	F
0	0	1
0	1	1
1	0	1
1	1	0

b) Função Booleana

$$F = \sim(A.B.C)$$

Tabela Verdade

A	B	C	F
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0

c) Função Booleana

$$F = \sim(A+B)$$

Tabela Verdade

A	B	F
0	0	1
0	1	0
1	0	0
1	1	0

d) Função Booleana

$$F = \sim((A+B).C.D)$$

Tabela Verdade

A	B	C	D	F
0	0	0	0	1
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	1
0	1	0	1	1
0	1	1	0	1
0	1	1	1	0
1	0	0	0	1
1	0	0	1	1
1	0	1	0	1
1	0	1	1	0
1	1	0	0	1
1	1	0	1	1
1	1	1	0	1
1	1	1	1	0

e) Função Booleana

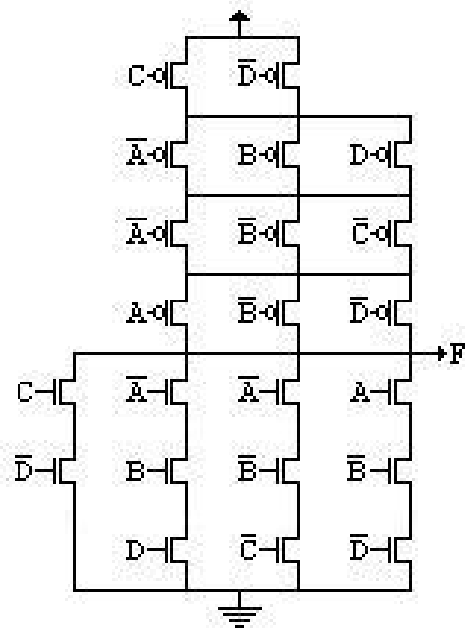
$$F = \sim(((A+B).C)+D)$$

Tabela Verdade

A	B	C	D	F
0	0	0	0	1
0	0	0	1	0
0	0	1	0	1
0	0	1	1	0
0	1	0	0	1
0	1	0	1	0
0	1	1	0	0
0	1	1	1	0
1	0	0	0	1
1	0	0	1	0
1	0	1	0	0
1	0	1	1	0
1	1	0	0	1
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0

Questão 2

a)



b)

