# Laboratory Report 5, JFET curves and attenuators

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### Introduction:

In this lab, the focus will be on the JFET transistor and we going to measure the JFET curves. There will be 3 circuits in which we are going to gather data from and put them in a tabular format and graph them.

### **Results**

# A1)

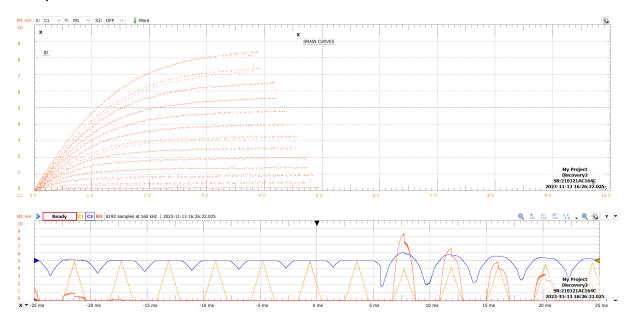


Figure 1 Both graphs are for part 1 of the experiment.

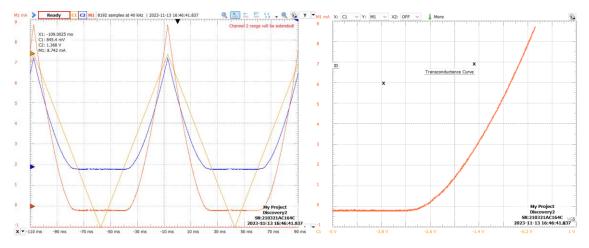
Idss = 8.45mA

**Vp** = 4V

rds(on) = Vp/2Idss

 $rds(on) = 470.6\Omega$ 

### A2)



Graphs for the Transconductance curbe

The **Idss** for the transconductance is **8.6mA** which is a slight difference compared to last value.

Gm = 8.6mA/5V = 1.72

Vgs(off) = -2Idss/Gm0 = -0.01V

**Vp** = -Vgs(off) = 0.01V.

 $\textbf{Rds(on)} = 0.01 \text{V}/2 (8.6 \text{mA}) = 0.58 \Omega$ 

# JFET voltage-controlled resistor.

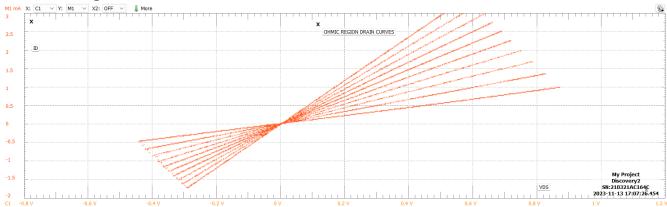
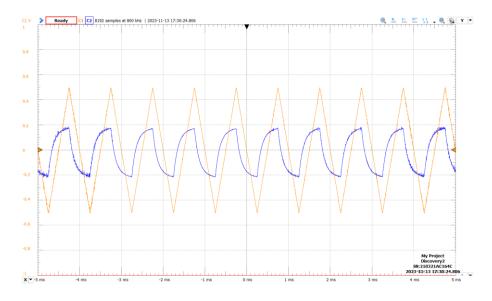


Figure 2 Graph for the JFET voltage controlled resistor

# JFET CONTROLLED ATTENUATOR:



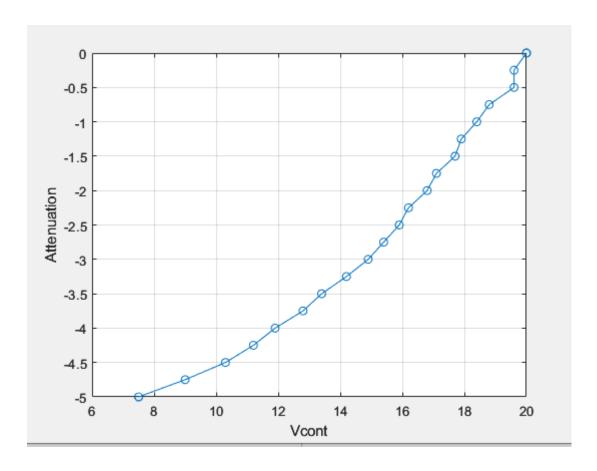
Voltage(V)	Input(V)	Output(V)	Attenuation	Attenuation
			α	(dB)
-5	1	0.42	0.42	7.5
-4.75	1	0.355	0.355	9
-4.5	1	0.305	0.305	10.3
-4.25	1	0.275	0.275	11.2
-4	1	0.255	0.255	11.9
-3.75	1	0.230	0.230	12.8
-3.5	1	0.215	0.215	13.4
-3.25	1	0.195	0.195	14.2
-3	1	0.18	0.18	14.9
-2.75	1	0.17	0.17	15.4
-2.5	1	0.16	0.16	15.9
-2.25	1	0.155	0.155	16.2
-2	1	0.145	0.145	16.8
-1.75	1	0.140	0.140	17.1
-1.5	1	0.130	0.130	17.7
-1.25	1	0.128	0.128	17.9
-1	1	0.120	0.120	18.4
-0.75	1	0.115	0.115	18.8
-0.5	1	0.105	0.105	19.6
-0.25	1	0.105	0.105	19.6
0	1	0.1	0.1	20

Input = 1V

Output = 420mV

Attenuation  $\alpha$  = output/input

In dB =  $-20\log(\alpha)$ 

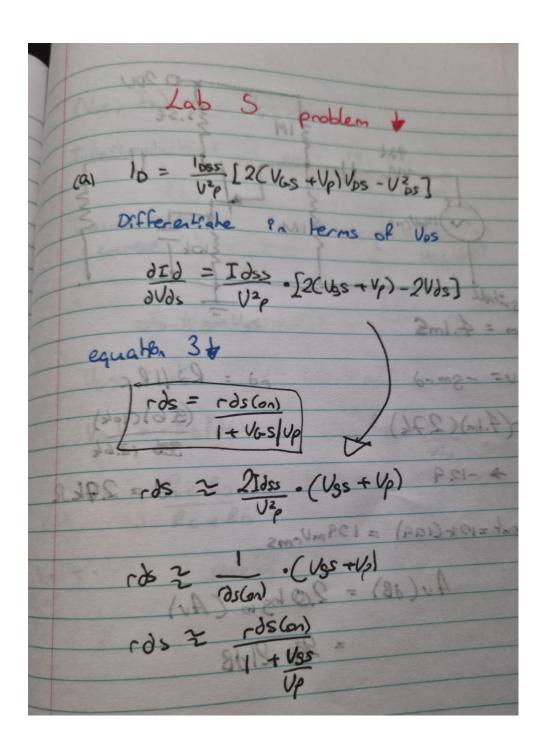


### **Conclusion**

In conclusion I was able to successfully complete the lab and collecting all the results and displaying them in tabular and graphical formats. I was also able to understand the processes of the JFET transistor.

# **Problem**

a)



# b)

