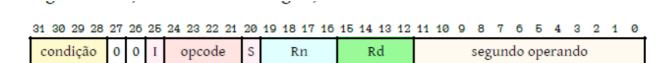
### Tipos de instrução

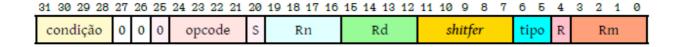
#### Salto

31 30 29 28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
condição	1	0	1	L								of	fset	(cc	om	ple	me	nto	de	doi	is)							

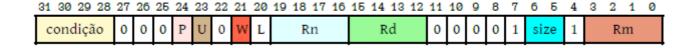
### **ALU**



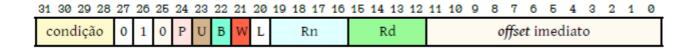
### **Barrel Shifter**



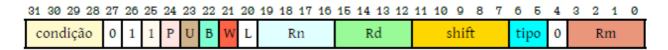
#### **Load/Store Half**



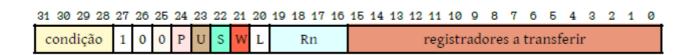
#### Load store com offset imediato



# Load store com registrador de índice



# Load store multiple



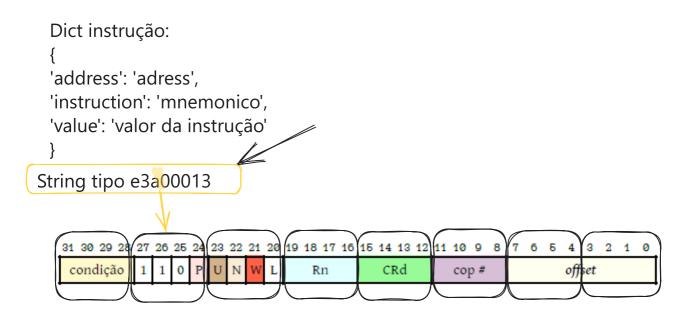
### **Move Coprocessor**

31 30 29 28 2	27	26	25	24	23 23	2 21	20	19 :	18 1	17 1	16	15	14	13	12	11	10	9	8	7	6	Б	4	3	2	1	0
condição	1	1	1	0	op	01	L		CR	n			R	d			coj	<b>9</b> #			op2	2	1		CR	lm	

### **Load store coprocessor**

31 30 29 28	27	26	25	24	23	22	21	20	19 :	18	17	16	15	14	13	12	11	10	9	8	7	6	Б	4	3	2	1	0
condição	1	1	0	P	U	N	W	L		Rı	n			CI	Rd			col	p #					off	set			

# Lógica de tradução



# Tabela de possibilidades

27	26	25	Tipo
0	0	0	Barrel Shift - Load/Store Half
0	0	1	ALU
0	1	0	-
0	1	1	Load store registrador de índice
1	0	0	Load store multiple
1	0	1	Salto
1	1	0	Load store coprocessador
1	1	1	Move coprocessador

# Desambiguição shift - load/store half

11	10	9	8	7	Tipo
0	0	0	0	1	Load/Store
Χ	Χ	Χ	Χ	Χ	Shift

