

POWER

PCIE

GPS

- H1 MountingHole
- H2 MountingHole
- H3 MountingHole
- H4 MountingHole

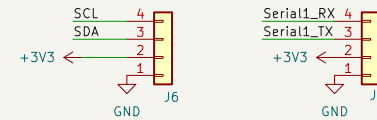
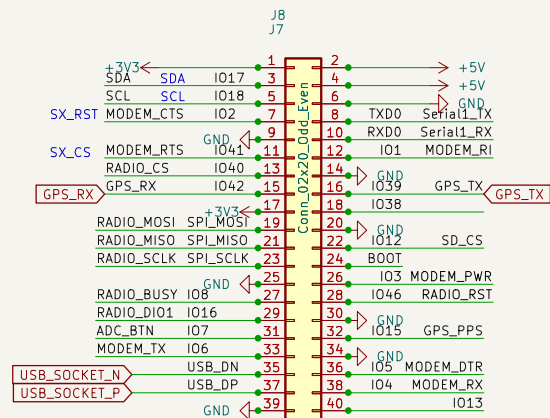
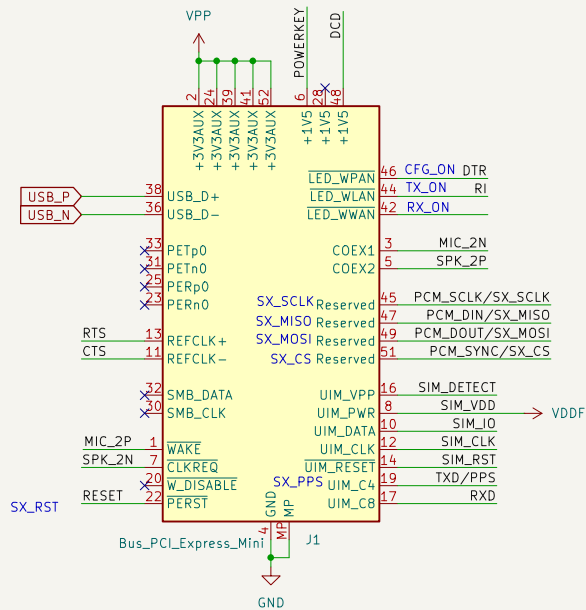




LILYGO[®]

Sheet: /		
File: Kicad_T-ETH-ELite-Gateway-Shield.kicad_sch		
Title: T-ETH-ELite-Gateway-Shield		
Size: A4	Date: 2024-06-27	Rev: 2412
KiCad E.D.A. 8.0.0	RevAuthor: <i>Louis He</i>	Id: 1/4





PCM_DOUT/SX_MOSI RADIO_MOSI
 PCM_DIN/SX_MISO RADIO_MISO
 PCM_SCLK/SX_SCLK RADIO_SCLK

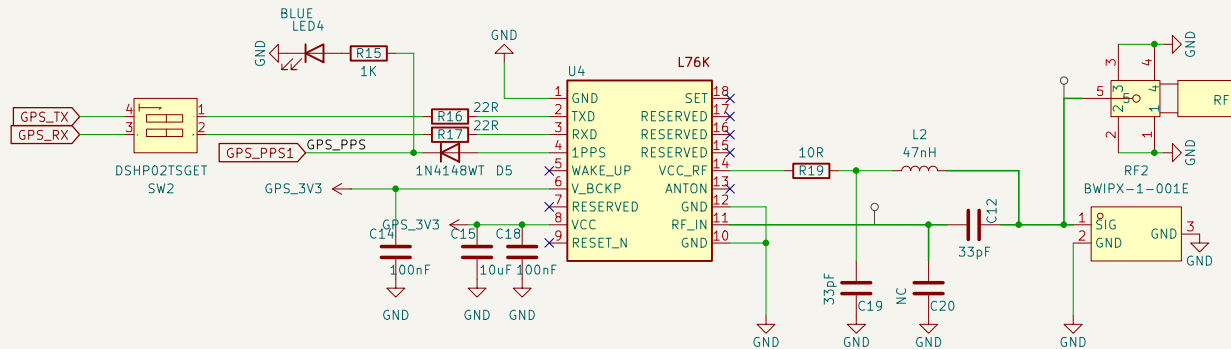
 PCM_SYNC/SX_CS MODEM_RTS
 RESET MODEM_CTS

When SX1302 is connected, CTS is assigned as the reset function of SX1302
 When SX1302 is connected, RTS is assigned as the CS function of SX1302



LILYGO®

Sheet: /PCIE/
 File: PCIE.kicad_sch
Title: T-Eth-Elite-Gateway-Shield
 Size: A4 Date: 2024-06-27
 KiCad E.D.A. 8.0.0 RevAuthor: *Lewis He* Id: 1/4





LILYGO®

Sheet: /GPS/			
File: GPS.kicad_sch			
Title: T-ETH-ELite-Gateway-Shield			
Size: A4	Date: 2024-06-27	RevAuthor: <i>Louis He</i>	Rev: 2412
KiCad E.D.A. 8.0.0			Id: 1/4