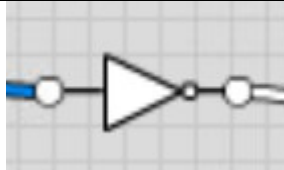
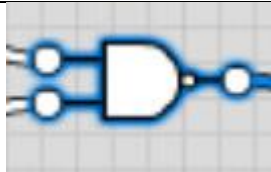
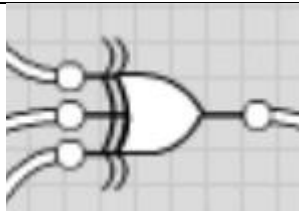


Practical II

Gate	Notation	Truth table																																				
	\bar{A}	<table><tr><th>Input</th><th>Output</th></tr><tr><td>1</td><td>0</td></tr><tr><td>0</td><td>1</td></tr></table>	Input	Output	1	0	0	1																														
Input	Output																																					
1	0																																					
0	1																																					
	\overline{AB}	<table><tr><th>A</th><th>B</th><th>Output</th></tr><tr><td>0</td><td>0</td><td>1</td></tr><tr><td>0</td><td>1</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td></tr><tr><td>1</td><td>1</td><td>0</td></tr></table>	A	B	Output	0	0	1	0	1	1	1	0	1	1	1	0																					
A	B	Output																																				
0	0	1																																				
0	1	1																																				
1	0	1																																				
1	1	0																																				
	\oplus	<table><tr><th>A</th><th>B</th><th>C</th><th>Output</th></tr><tr><td>0</td><td>0</td><td>0</td><td>0</td></tr><tr><td>0</td><td>0</td><td>1</td><td>1</td></tr><tr><td>0</td><td>1</td><td>0</td><td>1</td></tr><tr><td>0</td><td>1</td><td>1</td><td>0</td></tr><tr><td>1</td><td>0</td><td>0</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td><td>0</td></tr><tr><td>1</td><td>1</td><td>0</td><td>0</td></tr><tr><td>1</td><td>1</td><td>1</td><td>1</td></tr></table>	A	B	C	Output	0	0	0	0	0	0	1	1	0	1	0	1	0	1	1	0	1	0	0	1	1	0	1	0	1	1	0	0	1	1	1	1
A	B	C	Output																																			
0	0	0	0																																			
0	0	1	1																																			
0	1	0	1																																			
0	1	1	0																																			
1	0	0	1																																			
1	0	1	0																																			
1	1	0	0																																			
1	1	1	1																																			

C

A	B	Carry	Sum
0	0	0	0
1	0	0	1
0	1	0	1
1	1	1	0

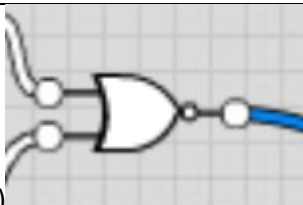
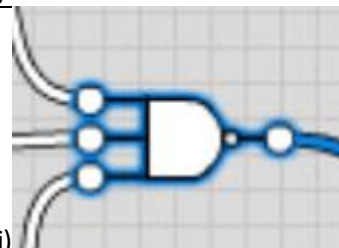
D

B	A	IN	Out0	Out1	Out2	Out3
0	0	0	0	0	0	0
0	0	1	1	0	0	0
0	1	0	0	0	0	0
0	1	0	0	1	0	0
1	0	0	0	0	0	0
1	0	0	0	0	1	0
1	1	0	0	0	0	0
1	1	0	0	0	0	1

2

A)

I) A logic gate in terms of circuitry, is an electronic circuit that use one or more input signals to create an output signal, the output signal is a boolean operation of the input signal.

Gate	Notation	Truth table																																				
<div>i)</div> 	$\overline{A+B}$	<table><tr><th>Input</th><th>Output</th></tr><tr><td>0 0</td><td>1</td></tr><tr><td>0 1</td><td>0</td></tr><tr><td>1 0</td><td>0</td></tr><tr><td>1 1</td><td>0</td></tr></table>	Input	Output	0 0	1	0 1	0	1 0	0	1 1	0																										
Input	Output																																					
0 0	1																																					
0 1	0																																					
1 0	0																																					
1 1	0																																					
<div>ii)</div> 	$A.B.C$	<table><tr><th>A</th><th>B</th><th>C</th><th>Output</th></tr><tr><td>0</td><td>0</td><td>0</td><td>1</td></tr><tr><td>0</td><td>0</td><td>1</td><td>1</td></tr><tr><td>0</td><td>1</td><td>0</td><td>1</td></tr><tr><td>0</td><td>1</td><td>1</td><td>1</td></tr><tr><td>1</td><td>0</td><td>0</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td><td>1</td></tr><tr><td>1</td><td>1</td><td>0</td><td>1</td></tr><tr><td>1</td><td>1</td><td>1</td><td>0</td></tr></table>	A	B	C	Output	0	0	0	1	0	0	1	1	0	1	0	1	0	1	1	1	1	0	0	1	1	0	1	1	1	1	0	1	1	1	1	0
A	B	C	Output																																			
0	0	0	1																																			
0	0	1	1																																			
0	1	0	1																																			
0	1	1	1																																			
1	0	0	1																																			
1	0	1	1																																			
1	1	0	1																																			
1	1	1	0																																			

C)

B	A	x0	x1	x2	x3	X
0	0	0				0
0	0	1				1
0	1		0			0
0	1		1			1

1	0			0		0
1	0			1		1
1	1				0	0
1	1				1	1

3

Ripple adder in logic.ly