

SCAR-DA: A Stable, Congestion-Aware Routing Algorithm with Deadlock-Avoidance

An Algorithm Description

1 Objective

The **SCAR-DA (Stable, Congestion-Aware Routing with Deadlock-Avoidance)** algorithm is a robust pathfinding algorithm designed for networks where path quality depends on dynamic congestion, and where network safety (deadlock-freedom) is a hard constraint. This is particularly relevant for Network-on-Chip (NoC) architectures.

It addresses three primary challenges:

- **Congestion:** Naive shortest-path routing (e.g., standard Dijkstra) creates bottlenecks by overloading the same shortest-hop paths.
- **Instability (Oscillation):** Simple load-aware routing often suffers from "route flapping," where all traffic rushes to a newly free path, congesting it, and then rushing away again.
- **Deadlock:** Adaptive or load-aware routing paths can easily violate the network's deadlock-avoidance (DA) turn model, leading to catastrophic network failure.

SCAR-DA integrates three concepts into a modified Dijkstra's shortest-path search:

1. A multi-factor cost function that considers both link and node congestion.
2. An Exponentially Weighted Moving Average (EWMA) for stable load metrics.
3. A constraint check to prune any path that violates the network's DA ruleset.

2 Algorithm Components

2.1 Input Parameters

$G = (V, E)$ The network graph, where V are routers and E are links.

s, t The source and target nodes for the path.

R_{DA} The Deadlock-Avoidance Ruleset (e.g., an XY, West-First, or Odd-Even turn model).

α, β Weighting coefficients that define the "penalty" for link and node congestion, respectively.

γ_L, γ_N Smoothing factors for the EWMA of link and node loads. A smaller γ results in more smoothing (slower reaction).

2.2 Network State: EWMA Load Smoothing

To prevent route flapping, the algorithm does not use instantaneous load metrics. Instead, it relies on a smoothed load value for each link e and node v , which is updated periodically by a separate network monitoring process (see Algorithm 2).

Let $L_{inst}(t)$ be the instantaneous load (e.g., buffer occupancy, bandwidth utilization) at time t . The EWMA load, $L_{ewma}(t)$, is calculated as:

$$L_{ewma}(t) = (\gamma \times L_{inst}(t)) + ((1 - \gamma) \times L_{ewma}(t - 1))$$

Where γ is the smoothing factor (γ_L for links, γ_N for nodes). The pathfinding algorithm (Algorithm 1) reads these stored L_{ewma} values.

2.3 Multi-Factor Dynamic Cost Function

The core of the algorithm is its cost function. The "cost" of traversing from node u to a neighbor v is a combination of static latency and dynamic, smoothed congestion costs for both the link (u, v) and the destination node v .

2.3.1 Node Cost

The cost associated with the destination router v :

$$C_{node}(v) = Lat_{node}(v) \times (1 + \beta \times L_{node}^{ewma}(v))$$

Where $Lat_{node}(v)$ is the static processing latency of router v and $L_{node}^{ewma}(v)$ is its smoothed buffer load (a value from 0.0 to 1.0).

2.3.2 Link Cost

The cost associated with traversing the link (u, v) :

$$C_{link}(u, v) = Lat_{link}(u, v) \times (1 + \alpha \times L_{link}^{ewma}(u, v))$$

Where $Lat_{link}(u, v)$ is the static latency of the link (e.g., hop count, wire delay) and $L_{link}^{ewma}(u, v)$ is its smoothed bandwidth utilization.

2.3.3 Total Dynamic Cost

The total cost added to the path when moving from u to v is the sum of these two components:

$$\mathbf{C}_{dyn}(\mathbf{u}, \mathbf{v}) = \mathbf{C}_{link}(\mathbf{u}, \mathbf{v}) + \mathbf{C}_{node}(\mathbf{v})$$

2.4 Deadlock-Avoidance Constraint

The algorithm enforces safety by pruning any path segment that violates the network's turn model, R_{DA} . This is managed by a helper function:

```
IsLegalTurn(prev_node, current_node, next_node, RDA)
```

This function returns `false` if the turn from $prev \rightarrow curr \rightarrow next$ is forbidden (e.g., a Y-to-X turn in an XY routing scheme), and `true` otherwise. For the first hop from the source, $prev_node$ is null, and the function always returns `true`.

3 SCAR-DA Pathfinding Algorithm

The core algorithm is a modified Dijkstra's, shown in Algorithm 1. It finds the lowest-cost path that is also deadlock-free.

Algorithm 1 SCAR-DA Pathfinding Algorithm

Require: Graph $G = (V, E)$, source s , target t , ruleset R_{DA} , α , β

```
1: Let  $dist[v] \leftarrow \infty$  for all  $v \in V$ 
2: Let  $prev[v] \leftarrow \text{null}$  for all  $v \in V$ 
3:  $dist[s] \leftarrow 0$ 
4:  $pq \leftarrow \text{PriorityQueue}(V)$                                  $\triangleright$  Min-priority queue, stores  $(v, dist[v])$ 
5:  $pq.add(s, 0)$ 

6: while  $pq$  is not empty do
7:    $u \leftarrow pq.pop\_min()$ 
8:   if  $u = t$  then
9:     return  $\text{BUILDPATH}(prev, t)$                                  $\triangleright$  Found the best path
10:    end if
11:     $prev\_node \leftarrow prev[u]$ 
12:    for each neighbor  $v$  of  $u$  do           $\triangleright$  — 1. Deadlock-Avoidance Constraint Check —
13:      if  $\text{ISLEGALTURN}(prev\_node, u, v, R_{DA}) = \text{false}$  then
14:        continue                                 $\triangleright$  Prune this path; it's an illegal turn
15:      end if                                 $\triangleright$  — 2. Multi-Factor Cost Calculation —
16:       $L_{link} \leftarrow G.\text{get\_link\_load}(u, v)$                  $\triangleright$  Returns  $L_{link}^{ewma}$ 
17:       $L_{node} \leftarrow G.\text{get\_node\_load}(v)$                  $\triangleright$  Returns  $L_{node}^{ewma}$ 
18:       $Lat_{link} \leftarrow G.\text{get\_link\_latency}(u, v)$ 
19:       $Lat_{node} \leftarrow G.\text{get\_node\_latency}(v)$ 
20:       $C_{link} \leftarrow Lat_{link} \times (1 + \alpha \times L_{link})$ 
21:       $C_{node} \leftarrow Lat_{node} \times (1 + \beta \times L_{node})$ 
22:       $dynamic\_cost \leftarrow C_{link} + C_{node}$                                  $\triangleright$  — 3. Standard Dijkstra Relaxation Step —
23:       $new\_dist \leftarrow dist[u] + dynamic\_cost$ 
24:      if  $new\_dist < dist[v]$  then
25:         $dist[v] \leftarrow new\_dist$ 
26:         $prev[v] \leftarrow u$ 
27:         $pq.add\_or\_update(v, new\_dist)$ 
28:      end if
29:    end for
30:  end while

31: return null                                 $\triangleright$  No deadlock-free path found
```

4 Network State Update Process

This process (Algorithm 2) is not part of the pathfinding call. It is a separate, concurrent process that runs periodically (e.g., every N clock cycles) to update the smoothed load values that Algorithm 1 relies on.

Algorithm 2 Periodic EWMA State Update Process

Require: Graph G , γ_L , γ_N

```

1: procedure UPDATERETEWNMA                                ▷ Run this procedure at a fixed interval
2:   for each link  $e = (u, v)$  in  $G.E$  do
3:      $L_{inst} \leftarrow G.\text{get\_instantaneous\_link\_load}(e)$ 
4:      $L_{old} \leftarrow e.L_{ewma}$ 
5:      $L_{new} \leftarrow (\gamma_L \times L_{inst}) + (1 - \gamma_L) \times L_{old}$ 
6:      $e.L_{ewma} \leftarrow L_{new}$                                 ▷ Store the new smoothed value
7:   end for

8:   for each node  $v$  in  $G.V$  do
9:      $L_{inst} \leftarrow G.\text{get\_instantaneous\_node\_load}(v)$ 
10:     $L_{old} \leftarrow v.L_{ewma}$ 
11:     $L_{new} \leftarrow (\gamma_N \times L_{inst}) + (1 - \gamma_N) \times L_{old}$ 
12:     $v.L_{ewma} \leftarrow L_{new}$                                 ▷ Store the new smoothed value
13:   end for
14: end procedure

```
