

目次

1	Library settings	1
1.1	Units	1
1.2	Voltage terminals	1
1.3	Operating conditions	1
1.4	Logic threshold	1
2	Cell settings	2
2.1	Cell : sky130_fd_sc_hd__inv_1	2
2.1.1	Basics	2
2.1.2	Input pin : A	2
2.1.3	Output pin : Y	2

1 Library settings

lib. name	delay model
SKY130_1P5V_25C	table_lookup

1.1 Units

cap	volt	cur	leak	time	res
pF	V	mA	pW	ns	Ohm

1.2 Voltage terminals

vdd	vss	gnd	pwell	nwell
VPWR	VGND	gnd	VNB	VPB

1.3 Operating conditions

operationg cond.	temperature	voltage
TCCOM	25.0	1.5

1.4 Logic threshold

input rise	input fall	output rise	output fall
50.0	50.0	50.0	50.0

2 Cell settings

2.1 Cell : sky130_fd_sc_hd__inv_1

2.1.1 Basics

name	type	code	area	leak
sky130_fd_sc_hd__inv_1	Combinational	INV	1.0	6.312599

2.1.2 Input pin : A

direction	related pwr pin	related gnd pin	max trans	cap.
input	VPWR	VGND	4.9	0.001967

2.1.3 Output pin : Y

direction	func	max cap	leak
output	!A	0.49	6.312599

2.1.3.1 related pin : A

related pin	func	max cap
A	!A	0.49

direction	prop min.	prop center	prop max
cell_fall	0.086980	0.574510	3.986189
cell_rise	0.172281	1.786246	7.780295

direction	tran min.	tran center	tran max
fall_transition	0.055171	0.669480	2.652201
rise_transition	0.151742	1.013050	7.042749

direction	eintl min.	eintl center	eintl max
fall_power	0.000899	0.001066	0.001410
rise_power	0.020950	0.154805	1.090676