

# Three Phase Open Loop Configuration PWM Generator for Use in Low Speed BLDC Control

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PROJECT PROPOSAL

Department of Computer and Electrical Engineering

Attention to Dr. Mitra MIRHASSANI

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# Three Phase Open Loop **Configuration PWM Generator for Use in Low Speed BLDC Control**

Santos, Matthew Schulz, Kurtis

103103503 104077177

### University Of Windsor

Department of Computer and Electrical Engineering

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#### 1 Introduction

Brushless DC motors commonly abbreviated as BLDC have become widely used throughout the world. They are considered to be one of the most desired types of motors due to their increased efficiency, longer lifetime, and precision control. In order to actuate a BLDC, an advanced pulse width modulation based control circuit is needed to correctly toggle the associated supply currents in the proper sequence. The vast majority of these control circuits operate using positioning feedback in order to properly synchronize commutation events. In order to account for this feedback requirement some motors are pre-built with position sensors (hall sensors) and the others are sensorless and typically controlled through the measure of back-EMF current. While proven to be adequate at high speeds, the sensorless technique fails at low RPM due to negligible back-emf and an open loop configuration with no feedback is required to control the motor.

## 2 Proposal

#### 2.1 Objective

A customer has requested an open loop BLDC speed controller capable of operating a three phase "EMAX Model 2213-935kV" at 12V in the low to 500RPM region for use evaluating its slipping potential. Thus the objective of this project is to design and build a circuit capable of producing the pulse width modulation signals needed to operate the three phase BLDC motor as indicated. In addition the overall design of the controller is to requested to be modular in nature to allow for easy reconfiguration into a synchronous AC motor PWM controller.

### 2.2 Implementation Strategy

Following a *considerable* amount of research and through examination of the BLDC in question a hybrid approach was taken whereby pulse signals are produced in a manner similar to sine wave pulse width modulation (SPWM) using comparators to reference sine waves produced in a Wien Bridge Oscillator to a known potential. This provided the added benefit of automatically allowing for a switching delay and easily allowing for the transition to AC motor control by simply changing the reference voltage to that of a triangular waveform (sine-triangular PWM generation). Furthermore to allow for strength control of the motor an

additional layer of pulse width modulation was superimposed using a 555 timer circuit specifically setup to produce triangular waveforms such that it could also be used in AC motor control if necessary. The output signals are then connected to a standard H-bridge driver specifically designed to withstand the 0.8A current draw required for motor operation. See appendix A for the appropriate circuit schematics. Note many components were selected with respect to current inventory rather then ideal values.

#### 2.2.1 Constraints, Variables, and Simulation Results

Three overall control variables are needed to allow for adequate control of the BLDC: speed, strength, and switching delay. Each of these parameters have been setup to be varied through the use of variable resistors labeled Rs, Ra, and Rd respectively. Additional information on specifications, tolerances, and associated outputs of these and other parameters can be found throughout the schematic diagrams and output plots located in the appendices.

#### 2.2.2 Required Components

For the purposes of this project all passive components were deemed exempt from the overall budget along with a number of components included in the provided analogue parts kit. In addition the customer is supplying the motor and two dual variable power supplies for use in operating both the motor and control circuit. The remaining parts are estimated to cost approximately \$9.30 and are for the most part readily available. For a full breakdown of the required components and associated costs see Appendix C: Materials List.

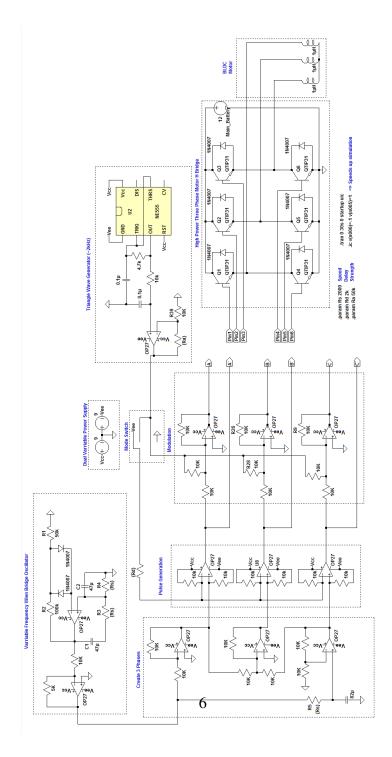
#### 3 Conclusions

- 1. The proposed circuit has been thoroughly simulated and is expected to perform adequately in the specified range.
- 2. Through the modification of the Sine wave reference signal and PWM connections to the H-bridge both AC and DC PWM configurations are possible.

# **Appendices**

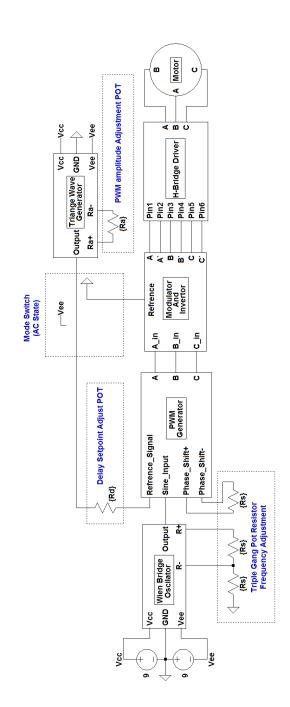
# **A** Circuit Schematics

# **A.1** Overall Circuit Schematic Diagram



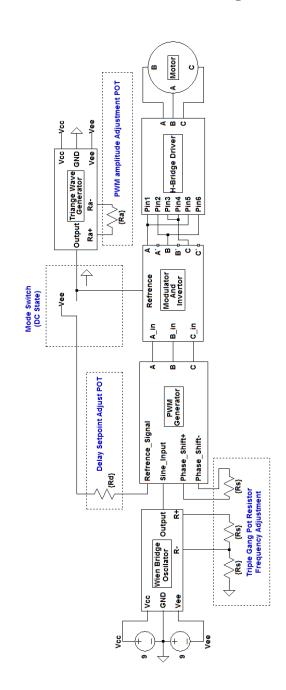
# A.2 AC Motor Controller Circuit Block Diagram

AC Motor SPWM Configuration

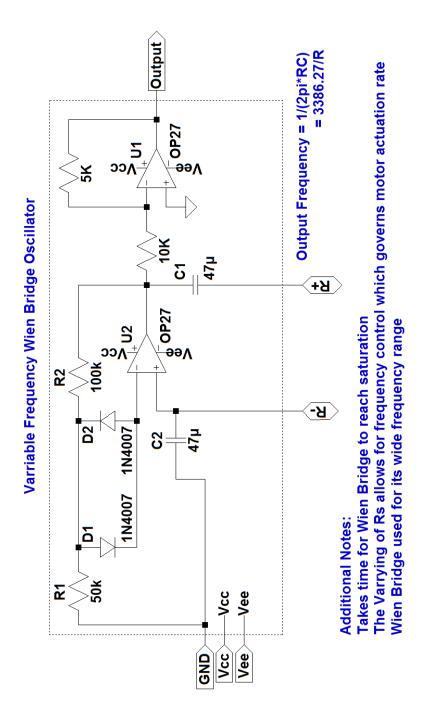


# **A.3** DC Motor Controller Circuit Block Diagram

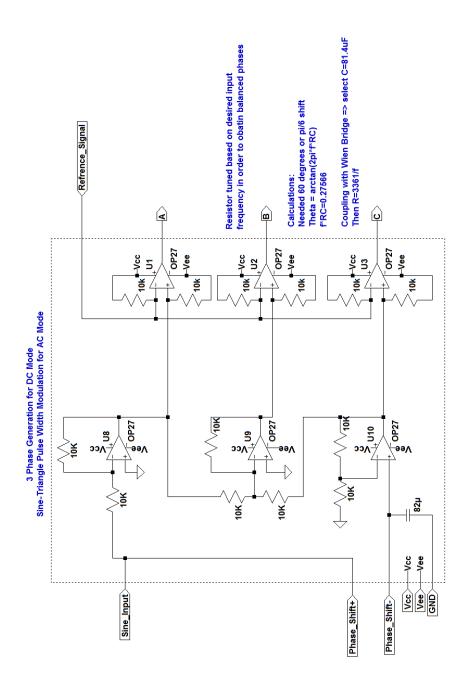
**BLDC PWM Configuration** 



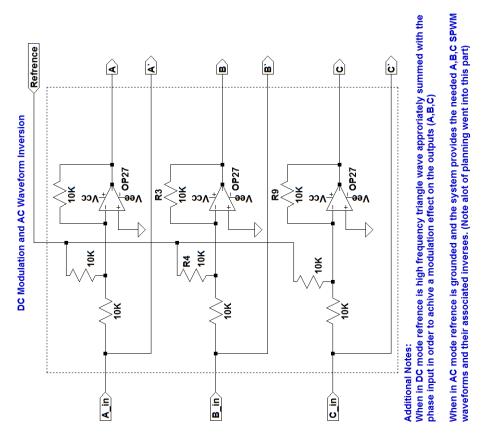
# A.4 Wien Bridge Oscillator Circuit



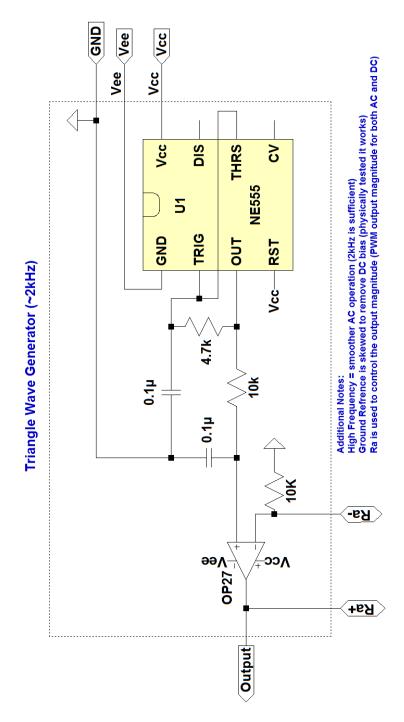
## A.5 Phase Generation Circuit



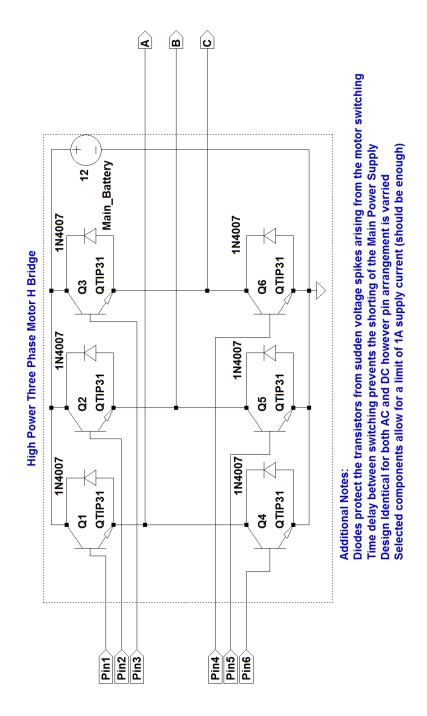
# A.6 Modulation and Inversion Circuit



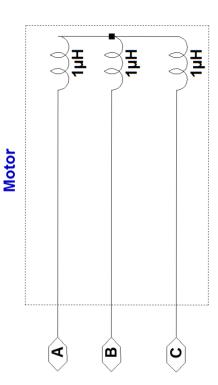
# **A.7** Triangle Wave Generator Circuit



## A.8 H-Bridge Three Phase Driver Circuit



# A.9 Simple Motor Model

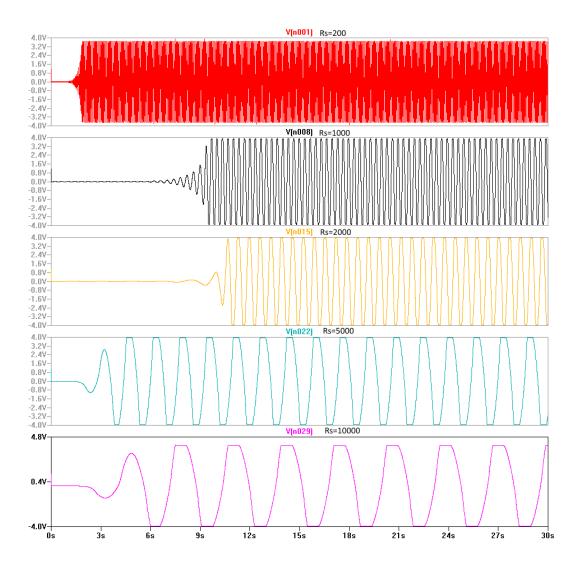


the generation of back emf associated with the rotation of a real motor Simplistic Model does not account for varriations in resistance and

Exists mainly for simulation test purposes

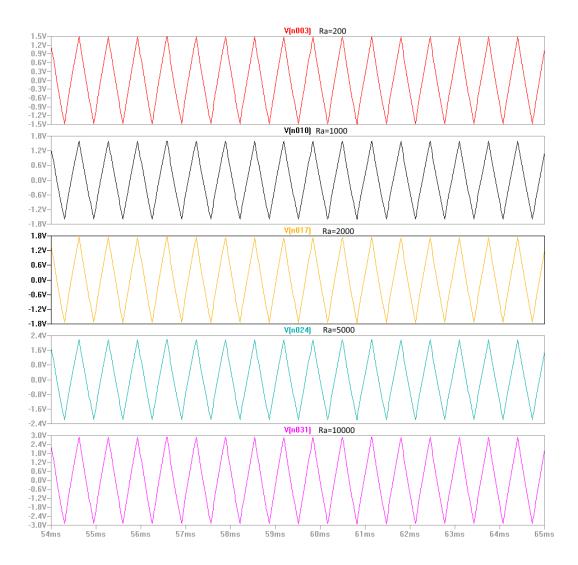
#### **B** Simulation Results

### **B.1** Wien Bridge Oscillator Simulated Output



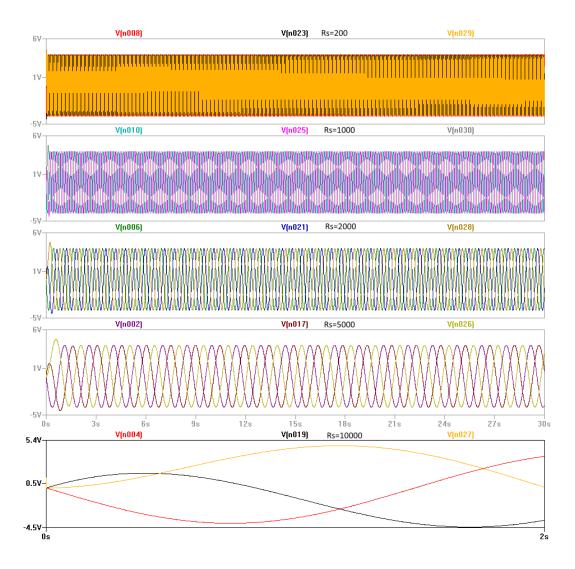
Thus it is shown that the adjustment of Rs correctly adjusts frequency output while maintaining overall magnitude. It is noted that the oscillations take time in order to reach saturation. Through the manipulation of Rs frequencies between 100Hz and 0.22Hz are incurred.

#### **B.2** Triangle Wave Generator Simulated Output



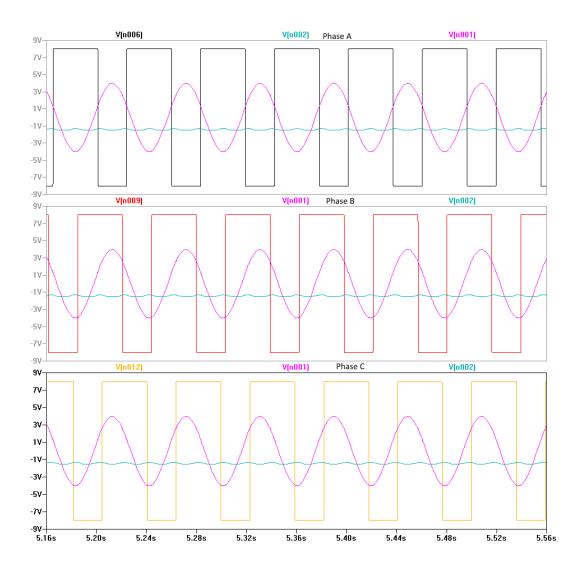
As expected the frequency output of the generator is fixed at approximately 1500Hz which should be sufficient for both Modulation of the DC control signal and sine wave resolution during the SPWM configuration. The varying of the Ra tuning resistor correctly adjusts the maximum output amplitude of the triangle wave from 1.5V to 3.0V as intended.

## **B.3** Three Phase Sine Wave Generation

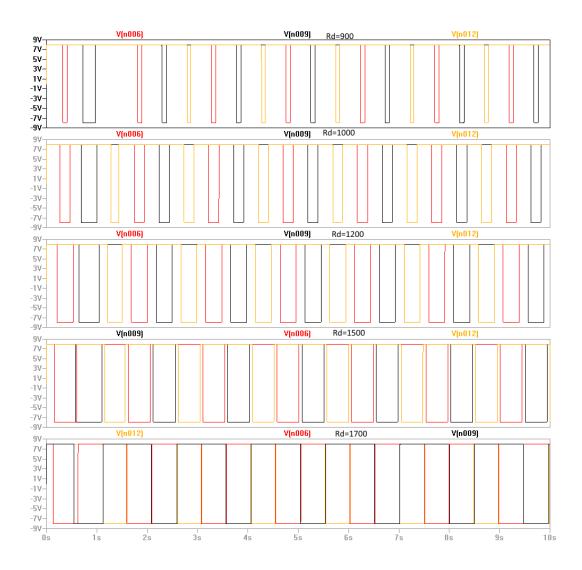


Through the solving of the associated system of equations the phase shifting circuit was tuned in conjunction with the Wien Bridge oscillator frequency setpoint so that it will correctly produce the required 120 degree shifts at any frequency as in the above simulation.

## **B.4** Phase Generator Output (DC Configuration)

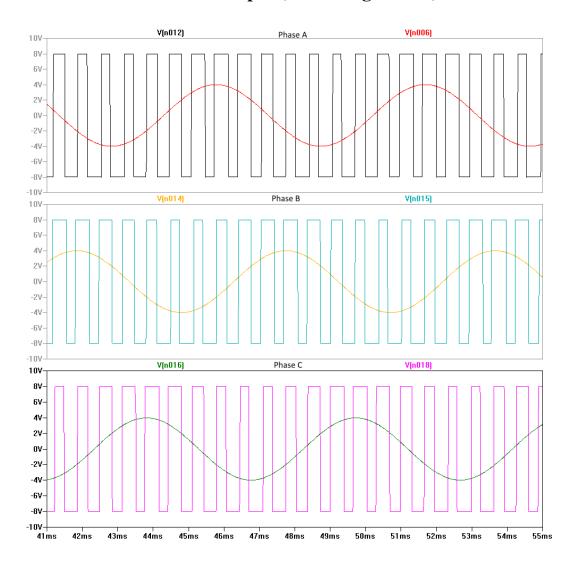


The above simulation illustrates the production of the *inverse* ON/OFF switching signals needed for commutation switching (about to be fed into an inverting summer). As illustrated these square wave signals are produced by comparing the 3 phase sine waves with a DC setpoint established through the tuning of Rd. Through alteration of this Rd resistance the spacing between switching events is varied as evident in the next figure.



From the figure it is clear to see that an increase in Rd produces shorter delay between actuation events. Care must be taken to always ensure a delay so as to prevent direct shorting of the H-Bridge driver. While not a variable of primary importance control of this delay allows for a smoothing like effect on motor control and also doubles as another form of modulation adjustment.

#### **B.5** Phase Generator Output (AC Configuration)

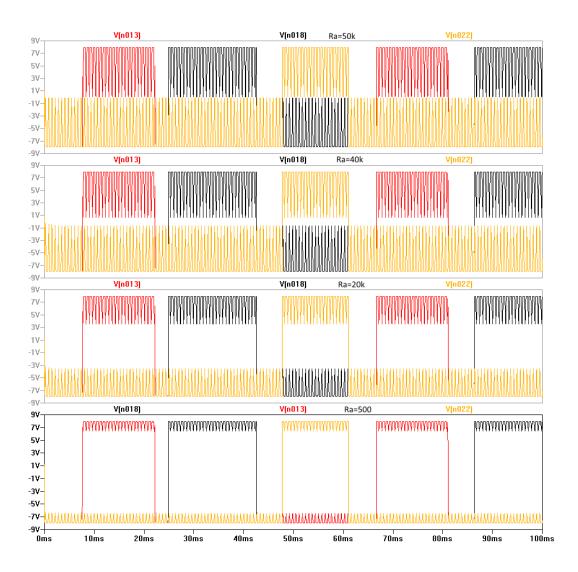


Through the toggling of a switch the triangular waveform is set to connect to the reference terminal of the Phase Generator Circuit and its previous connection is then grounded. The 3 phase sine wave is directly compared with a high frequency triangle wave which is used to directly approximate the sine wave in the form of Sinewave Pulse Width Modulation (SPWM). The results are illustrated above. These three signals can be used to directly drive an AC motor however their inverse is also required and procured in the next stage

#### **B.6** Modulation and Inversion Output (DC Configuration)

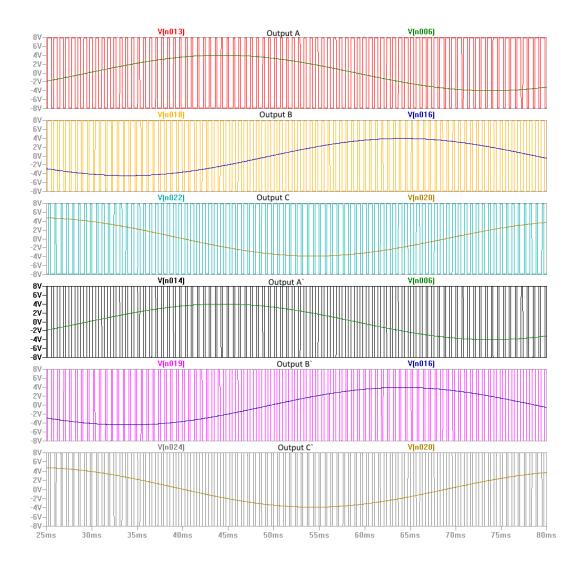


In DC configuration this circuit acts as an inverting summer which superimposes the high frequency triangular waveform on top of the previously generated square wave pulse signals. This has a modulation effect and through the varying of Ra can be used to adjust the average strength of the signal entering the H-Bridge Driver as illustrated in the next figure.



As illustrated above the altering of Ra acts as a throthe on the output effectively adjusting its average amplitude through time based switching as per the definition of pulse width modulation.

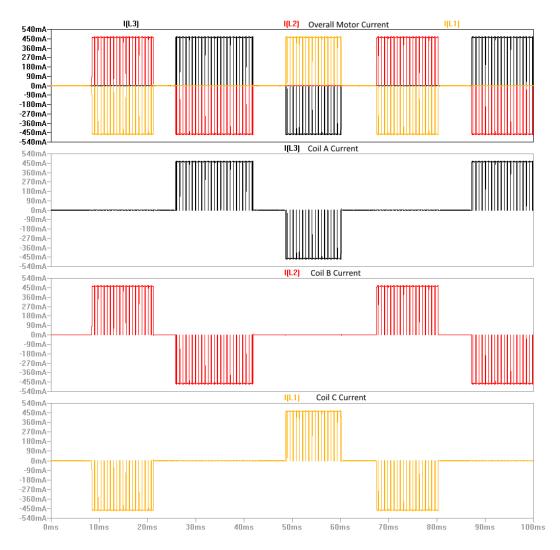
#### **B.7** Modulation and Inversion Output (AC Configuration)



In this configuration the circuit is simply an inverter and is used to extract the required converse waveforms of the SPWM signals coming from the phase generator output. After inversion all the required signals for AC motor commutation have been produced and can be directly inserted into the H-bridge.

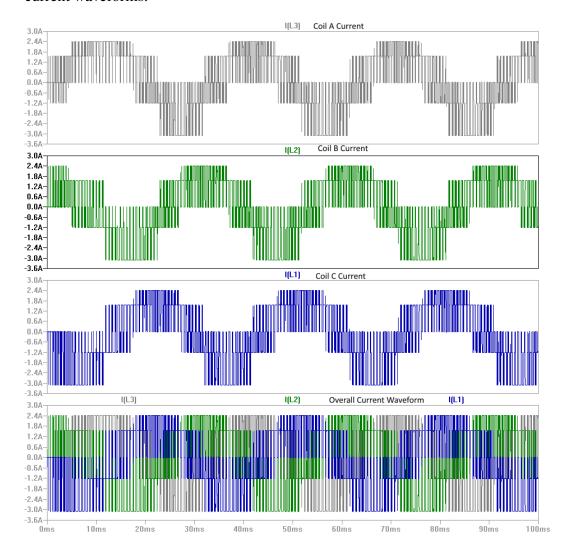
#### **B.8** Simulated Motor Current Waveforms

In order to verify that the controller operates correctly the current waveforms in each of the windings were directly measured for DC configuration and the results are shown below.



From the figure it is clear that the motor is provided adequate current to actuate with a proper switching cycle including the needed delay to prevent the H-bridge from shorting.

When examined in the AC configuration the motor exhibited the following current waveforms.



From the figure it is clear that the motor is achieving commutation through the required waveform cycling which approximates a sinusoidal waveform (hence sinewave pulse width modulation). Although not immediately evident in the figure it is assured that shorting of the H-bridge is impossible since the relevant control signals on each transistor pair are inverses as generated in the previous stage.

# **C** Materials List

4	Required	Number Provided in	Numberin	Number in Estimated Unit	Total Fyngaga	Additional Mates
	Quantity	Analogue Kit	Inventory	Cost	lotal Expense	Additional Notes
capacitor, 47μF	2	0	3	exlusive	\$0.00	
capacitor, 100nF	2	0	10	exlusive	\$0.00	
capacitor, 82μF	2	0	0	exlusive	\$0.00	Need to Order
diode, 1N4007	8	2	100+	\$0.10	\$0.60	
opamps, LM324N (quad OpAmp)	3	0	unknown	\$0.50	\$1.50	alternatives exist
BLDC Motor	1	0	1	exlusive	\$0.00	Customer Supplied
bipolar transistor, QTIP31	9	0	40+	\$0.50	\$3.00	
varriable resistor, POT (0-10k)	1	0	0	\$0.75	\$0.75	Need to Order
varriable resistor, POT (0-10k)	1	0	0	\$0.75	\$0.75	Need to Order
varriable resistor, 3 Gang POT (0-10k)	1	0	0	\$2.50	\$2.50	Need to Order
resistor, 10K	22	2	30+	exlusive	\$0.00	
resistor, 4.7K	2	2	30+	exlusive	\$0.00	
resistor, 50K	1	0	30+	exlusive	\$0.00	
resistor, 100K	1	2	30+	exlusive	\$0.00	
4 Pole Switch	1	0	2	\$0.20	\$0.20	
Breadboard	1	0	2	exlusive	\$0.00	
Varriable Power Supplies	2	0	2	exlusive	\$0.00	Customer Supplied

Total Estimated Expense

#### References

- [1] A. Sedra, K. Smith. *Microelectronic Circuits*. 7th ed. New York: Oxford University Press, Nov. 2014.
- [2] Freescale Semiconductor Ltd, Sensorless BLDC Motor Control Using MC9S08AW60. DRM086, Rev. 0.1, January, 2007.
- [3] Wilson, Dave. *So, Which PWM Technique is Best.* Texas Instruments Motor Drive & Control Blog, April, 2012.
- [4] Yaskawa Electric America Inc, *Variable Frequency Drives 3-Phase Motor Control*. AR.AFD.01, October, 2006.