**Implementation of applications on zc702 without including interrupt mechanism**

To run every application, two “makes” should be done; one to create the software library and bitstream of the accelerator and the other to call the accelerator and build the executable on the host of the zc702 board. Create the accelerator of an application in a Linux server by sourcing the Xilinx SDSoC version 2016.2, and making its software library (.a file) and FPGA bitstream (BOOT.BIN file). Place the generated library in the Lib directory provided inside the host application directory, and the generated bit stream file inside /mnt. After restarting the board OS, make the executable on host and run it by executing the .sh files provided. The experiment results are generated as .txt files, one file for every combination of FPGA and CPU cores. To successfully implement the applications, power/energy measurement and Dynamic Scheduler files should be addressed correctly in the applications.