

CMSC 133

Introduction to Computer Organization, Architecture and Assembly Language

Laboratory Report

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Summary.

The Laboratory activity is all about putting into action what we learned in our previous lessons and video lectures. It aims to help us learn the basics of MIPS assembly language from basic programs while task by task progressing into more complex problems.

The First task which will be featured in this laboratory report is on how to find the summation of a specified array. The task is actually quite simple as there are comments within the file that will help us to discover on what procedures we have to use. Of course, it is not expected that there will also be prepared comments in the next task. Through this task, I learned through practice on how to use branching instructions such as beq where we only have to branch if for example i would be equal to N . The task also tackled on how we can loop a predefined array in MIPS assembly.

In approaching my method to solving this task, I first created a C program that would solve the laboratory problem. Things would then be much simpler after that as I only had to translate my C code into MIPS assembly code. Paired with my C code and the pre-placed comments in the activity, I was able to successfully create a MIPS code that will solve the problem which I solved through looping all the elements in the array.

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1 The Lab Problem

The problem to be solved in this first task of the laboratory activity is to create a subroutine wherein it has to iterate through a set of numbers, specifically the Fibonacci array and to finally add them up. This means that through the iteration or loop, we have to create an instance where it adds the previous iteration number and the current iteration number until the loop is satisfied. The sum would then be returned to the caller to be printed in the console.

2 The Assembly Source Code

```
integer_array_sum:

DBG:      ##### DEBUGG BREAKPOINT #####

        addi    $v0, $zero, 0           # Initialize Sum to zero.
        add $t0, $zero, $zero          # Initialize array index i to zero.

for_all_in_array:

        ##### Append a MIPS-instruktion before each of these comments
        # $t1 = offset, $t2 = n, $t3 = address

        beq $t0, $a1, end_for_all      # Done if i == N
        sll $t1, $t0, 2                 # 4*i
        add $t3, $a0, $t1               # address = ARRAY + 4*i
        lw  $t2, 0($t3)                 # n = A[i]
        add $v0, $v0, $t2               # Sum = Sum + n
        addi $t0, $t0, 1                # i++
        j   for_all_in_array            # proceeds to the next element

end_for_all:

        jr  $ra                         # Return to caller.
```

3 Screenshots

- A. **Start of Execution** – Show the current state of the registers, code/program/text segment and data and stack segments in the memory after the program is loaded. Show the initial values of the registers. Show that your program and data has been loaded in the code and the data segments respectively.

Int Regs [16]	PC	Text
PC = 0	[00400000] 8fa40000 lw \$4, 0(\$29)	; 183: lw \$a0 0(\$sp) # argc
EPC = 0	[00400004] 27a50004 addiu \$5, \$29, 4	; 184: addiu \$a1 \$sp 4 # argv
Cause = 0	[00400008] 24a60004 addiu \$6, \$5, 4	; 185: addiu \$a2 \$a1 4 # envp
BadVAddr = 0	[0040000c] 00041080 sll \$2, \$4, 2	; 186: sll \$v0 \$a0 2
Status = 3000ff10	[00400010] 00c23021 addu \$6, \$6, \$2	; 187: addu \$a2 \$a2 \$v0
HI = 0	[00400014] 0c10001a jal 0x00400068 [main]	; 188: jal main
LO = 0	[00400018] 00000000 nop	; 189: nop
R0 [r0] = 0	[0040001c] 3402000a ori \$2, \$0, 10	; 191: li \$v0 10
R1 [at] = 0	[00400020] 0000000c syscall	; 192: syscall # syscall 10 (exit)
R2 [v0] = 0	[00400024] 20020000 addi \$2, \$0, 0	; 40: addi \$v0, \$zero, 0 # Initialize Sum to zero.
R3 [v1] = 0	[00400028] 00004020 add \$8, \$0, \$0	; 41: add \$t0, \$zero, \$zero # Initialize array index i to zero.
R4 [a0] = 5	[0040002c] 11050007 beq \$8, \$5, 28 [end_for_all-0x0040002c]	
R5 [a1] = 7ffff788	[00400030] 00084880 sll \$9, \$8, 2	; 48: sll \$t1, \$t0, 2 # 4*i
R6 [a2] = 7ffff7a0	[00400034] 00895820 add \$11, \$4, \$9	; 49: add \$t3, \$a0, \$t1 # address = ARRAY + 4*i
R7 [a3] = 0	[00400038] 8d6a0000 lw \$10, 0(\$11)	; 50: lw \$t2, 0(\$t3) # n = A[i]
R8 [t0] = 0	[0040003c] 004a1020 add \$2, \$2, \$10	; 51: add \$v0, \$v0, \$t2 # Sum = Sum + n
R9 [t1] = 0	[00400040] 21080001 addi \$8, \$8, 1	; 52: addi \$t0, \$t0, 1 # i++
R10 [t2] = 0	[00400044] 0810000b j 0x0040002c [for_all_in_array]; 53: j for_all_in_array # next element	
R11 [t3] = 0	[00400048] 03e00008 jr \$31	; 57: jr \$ra # Return to caller.
R12 [t4] = 0	[0040004c] 03e00008 jr \$31	; 74: jr \$ra
R13 [t5] = 0	[00400050] 23bdfcfc addi \$29, \$29, -4	; 91: addi \$sp, \$sp, -4 # PUSH return address to caller
R14 [t6] = 0	[00400054] afbf0000 sw \$31, 0(\$29)	; 92: sw \$ra, 0(\$sp)
R15 [t7] = 0	[00400058] 8fbf0000 lw \$31, 0(\$29)	; 96: lw \$ra, 0(\$sp) # Pop return address to caller
R16 [s0] = 0	[0040005c] 23bd0004 addi \$29, \$29, 4	; 97: addi \$sp, \$sp, 4
R17 [s1] = 0	[00400060] 03e00008 jr \$31	; 99: jr \$ra
R18 [s2] = 0	[00400064] 03e00008 jr \$31	; 112: jr \$ra
R19 [s3] = 0	[00400068] 23bdfcfc addi \$29, \$29, -4	; 157: addi \$sp, \$sp, -4 # PUSH return address
R20 [s4] = 0	[0040006c] afbf0000 sw \$31, 0(\$29)	; 158: sw \$ra, 0(\$sp)
R21 [s5] = 0	[00400070] 3402000a ori \$2, \$0, 4	; 164: li \$v0, 4
R22 [s6] = 0	[00400074] 3c011001 lui \$1, 4097 [STR_sum_of_fibonacci_a]	
R23 [s7] = 0	[00400078] 34240047 ori \$4, \$1, 71 [STR_sum_of_fibonacci_a]	
R24 [t8] = 0	[0040007c] 0000000c syscall	; 166: syscall
R25 [t9] = 0	[00400080] 3c011001 lui \$1, 4097	; 168: lw \$a0, ARRAY_SIZE
R26 [k0] = 0	[00400084] 8c240000 lw \$4, 0(\$1)	
R27 [k1] = 0	[00400088] 34020001 ori \$2, \$0, 1	; 169: li \$v0, 1
R28 [gp] = 10008000	[0040008c] 0000000c syscall	; 170: syscall
R29 [sp] = 7ffff784	[00400090] 34020004 ori \$2, \$0, 4	; 172: li \$v0, 4
R30 [s8] = 0	[00400094] 3c011001 lui \$1, 4097 [STR_sum_of_fibonacci_b]	
R31 [ra] = 0	[00400098] 34240057 ori \$4, \$1, 87 [STR_sum_of_fibonacci_b]	
	[0040009c] 0000000c syscall	; 174: syscall
	[004000a0] 3c011001 lui \$1, 4097 [FIBONACCI_ARRAY]; 176: la \$a0, FIBONACCI_ARRAY	
	[004000a4] 34240004 ori \$4, \$1, 4 [FIBONACCI_ARRAY]	
	[004000a8] 3c011001 lui \$1, 4097	; 177: lw \$a1, ARRAY_SIZE
	[004000ac] 8c250000 lw \$5, 0(\$1)	
	[004000b0] 0c100009 jal 0x00400024 [integer_array_sum]	
	[004000b4] 00402020 add \$4, \$2, \$0	; 181: add \$a0, \$v0, \$zero
	[004000b8] 34020001 ori \$2, \$0, 1	; 182: li \$v0, 1
	[004000bc] 0000000c syscall	; 183: syscall

- B. **Middle of Execution** – Show the current state of the registers and data and stack segments mid-execution. Also show the currently pointed instruction.

Int Regs [16]	PC	Text
PC = 400044	[00400000] 8fa40000 lw \$4, 0(\$29)	; 183: lw \$a0 0(\$sp) # argc
EPC = 400014	[00400004] 27a50004 addiu \$5, \$29, 4	; 184: addiu \$a1 \$sp 4 # argv
Cause = 24	[00400008] 24a60004 addiu \$6, \$5, 4	; 185: addiu \$a2 \$a1 4 # envp
BadVAddr = 0	[0040000c] 00041080 sll \$2, \$4, 2	; 186: sll \$v0 \$a0 2
Status = 3000ff10	[00400010] 00c23021 addu \$6, \$6, \$2	; 187: addu \$a2 \$a2 \$v0
HI = 0	[00400014] 0c10001a jal 0x00400068 [main]	; 188: jal main
LO = 0	[00400018] 00000000 nop	; 189: nop
R0 [r0] = 0	[0040001c] 3402000a ori \$2, \$0, 10	; 191: li \$v0 10
R1 [at] = 10010000	[00400020] 0000000c syscall	; 192: syscall # syscall 10 (exit)
R2 [v0] = 58	[00400024] 20020000 addi \$2, \$0, 0	; 40: addi \$v0, \$zero, 0 # Initialize Sum to zero.
R3 [v1] = 0	[00400028] 00004020 add \$8, \$0, \$0	; 41: add \$t0, \$zero, \$zero # Initialize array index i to zero.
R4 [a0] = 10010004	[0040002c] 11050007 beq \$8, \$5, 28 [end_for_all-0x0040002c]	
R5 [a1] = a	[00400030] 00084880 sll \$9, \$8, 2	; 48: sll \$t1, \$t0, 2 # 4*i
R6 [a2] = 7ffff7a0	[00400034] 00895820 add \$11, \$4, \$9	; 49: add \$t3, \$a0, \$t1 # address = ARRAY + 4*i
R7 [a3] = 0	[00400038] 8d6a0000 lw \$10, 0(\$11)	; 50: lw \$t2, 0(\$t3) # n = A[i]
R8 [t0] = 9	[0040003c] 004a1020 add \$2, \$2, \$10	; 51: add \$v0, \$v0, \$t2 # Sum = Sum + n
R9 [t1] = 20	[00400040] 21080001 addi \$8, \$8, 1	; 52: addi \$t0, \$t0, 1 # i++
R10 [t2] = 22	[00400044] 0810000b j 0x0040002c [for_all_in_array]; 53: j for_all_in_array # next element	
R11 [t3] = 10010024	[00400048] 03e00008 jr \$31	; 57: jr \$ra # Return to caller.
R12 [t4] = 0	[0040004c] 03e00008 jr \$31	; 74: jr \$ra
R13 [t5] = 0	[00400050] 23bdfcfc addi \$29, \$29, -4	; 91: addi \$sp, \$sp, -4 # PUSH return address to caller
R14 [t6] = 0	[00400054] afbf0000 sw \$31, 0(\$29)	; 92: sw \$ra, 0(\$sp)
R15 [t7] = 0	[00400058] 8fbf0000 lw \$31, 0(\$29)	; 96: lw \$ra, 0(\$sp) # Pop return address to caller
R16 [s0] = 0	[0040005c] 23bd0004 addi \$29, \$29, 4	; 97: addi \$sp, \$sp, 4
R17 [s1] = 0	[00400060] 03e00008 jr \$31	; 99: jr \$ra
R18 [s2] = 0	[00400064] 03e00008 jr \$31	; 112: jr \$ra
R19 [s3] = 0	[00400068] 23bdfcfc addi \$29, \$29, -4	; 157: addi \$sp, \$sp, -4 # PUSH return address
R20 [s4] = 0	[0040006c] afbf0000 sw \$31, 0(\$29)	; 158: sw \$ra, 0(\$sp)
R21 [s5] = 0	[00400070] 3402000a ori \$2, \$0, 4	; 164: li \$v0, 4
R22 [s6] = 0	[00400074] 3c011001 lui \$1, 4097 [STR_sum_of_fibonacci_a]	
R23 [s7] = 0	[00400078] 34240047 ori \$4, \$1, 71 [STR_sum_of_fibonacci_a]	
R24 [t8] = 0	[0040007c] 0000000c syscall	; 166: syscall
R25 [t9] = 0	[00400080] 3c011001 lui \$1, 4097	; 168: lw \$a0, ARRAY_SIZE
R26 [k0] = 0	[00400084] 8c240000 lw \$4, 0(\$1)	
R27 [k1] = 0	[00400088] 34020001 ori \$2, \$0, 1	; 169: li \$v0, 1
R28 [gp] = 10008000	[0040008c] 0000000c syscall	; 170: syscall
R29 [sp] = 7ffff780	[00400090] 34020004 ori \$2, \$0, 4	; 172: li \$v0, 4
R30 [s8] = 0	[00400094] 3c011001 lui \$1, 4097 [STR_sum_of_fibonacci_b]	
R31 [ra] = 4000b4	[00400098] 34240057 ori \$4, \$1, 87 [STR_sum_of_fibonacci_b]	
	[0040009c] 0000000c syscall	; 174: syscall
	[004000a0] 3c011001 lui \$1, 4097 [FIBONACCI_ARRAY]; 176: la \$a0, FIBONACCI_ARRAY	
	[004000a4] 34240004 ori \$4, \$1, 4 [FIBONACCI_ARRAY]	
	[004000a8] 3c011001 lui \$1, 4097	; 177: lw \$a1, ARRAY_SIZE
	[004000ac] 8c250000 lw \$5, 0(\$1)	
	[004000b0] 0c100009 jal 0x00400024 [integer_array_sum]	
	[004000b4] 00402020 add \$4, \$2, \$0	; 181: add \$a0, \$v0, \$zero
	[004000b8] 34020001 ori \$2, \$0, 1	; 182: li \$v0, 1
	[004000bc] 0000000c syscall	; 183: syscall

- C. **End of Execution** – Show the current state of registers and data and stack segments after the execution of the last instruction of your program. Also show the currently pointed instruction after the execution of the last instruction.

Int Regs [16]	Text
PC = 400020	[00400000] 8fa40000 lw \$4, 0(\$29) ; 183: lw \$a0 0(\$sp) # argc
EPC = 0	[00400004] 27a50004 addiu \$5, \$29, 4 ; 184: addiu \$a1 \$sp 4 # argv
Cause = 0	[00400008] 24a60004 addiu \$6, \$5, 4 ; 185: addiu \$a2 \$a1 4 # envp
BadVAddr = 0	[0040000c] 00041080 sll \$2, \$4, 2 ; 186: sll \$v0 \$a0 2
Status = 3000ff10	[00400010] 00c23021 addu \$6, \$6, \$2 ; 187: addu \$a2 \$a2 \$v0
HI = 0	[00400014] 0c10001a jal 0x00400068 [main] ; 188: jal main
LO = 0	[00400018] 00000000 nop ; 189: nop
	[0040001c] 3402000a ori \$2, \$0, 10 ; 191: li \$v0 10
R0 [r0] = 0	[00400020] 0000000c syscall ; 192: syscall # syscall 10 (exit)
R1 [at] = 10010000	[00400024] 20020000 addi \$2, \$0, 0 ; 40: addi \$v0, \$zero, 0 # Initialize Sum to zero.
R2 [v0] = a	[00400028] 00004020 add \$8, \$0, \$0 ; 41: add \$t0, \$zero, \$zero # Initialize array index i to zero.
R3 [v1] = 0	[0040002c] 11050007 beq \$8, \$5, 28 [end_for_all-0x0040002c]
R4 [a0] = 100100d6	[00400030] 00094880 sll \$9, \$8, 2 ; 48: sll \$t1, \$t0, 2 # 4*i
R5 [a1] = 400064	[00400034] 00895820 add \$11, \$4, \$9 ; 49: add \$t3, \$a0, \$t1 # address = ARRAY + 4*i
R6 [a2] = 7ffff7a0	[00400038] 8d6a0000 lw \$10, 0(\$11) ; 50: lw \$t2, 0(\$t3) # n = A[i]
R7 [a3] = 0	[0040003c] 004a1020 add \$2, \$2, \$10 ; 51: add \$v0, \$v0, \$t2 # Sum = Sum + n
R8 [t0] = 1001002c	[00400040] 21080001 addi \$8, \$8, 1 ; 52: addi \$t0, \$t0, 1 # i++
R9 [t1] = 24	[00400044] 0810000b j 0x0040002c [for_all_in_array]; 53: j for_all_in_array # next element
R10 [t2] = 37	[00400048] 03e00008 jr \$31 ; 57: jr \$ra # Return to caller.
R11 [t3] = 10010028	[0040004c] 03e00008 jr \$31 ; 74: jr \$ra
R12 [t4] = 0	[00400050] 23bdfffc addi \$29, \$29, -4 ; 91: addi \$sp, \$sp, -4 # PUSH return address to caller
R13 [t5] = 0	[00400054] afbf0000 sw \$31, 0(\$29) ; 92: sw \$ra, 0(\$sp)
R14 [t6] = 0	[00400058] 8fbf0000 lw \$31, 0(\$29) ; 96: lw \$ra, 0(\$sp) # Pop return address to caller
R15 [t7] = 0	[0040005c] 23bd0004 addi \$29, \$29, 4 ; 97: addi \$sp, \$sp, 4
R16 [s0] = 0	[00400060] 03e00008 jr \$31 ; 99: jr \$ra
R17 [s1] = 0	[00400064] 03e00008 jr \$31 ; 112: jr \$ra
R18 [s2] = 0	[00400068] 23bdfffc addi \$29, \$29, -4 ; 157: addi \$sp, \$sp, -4 # PUSH return address
R19 [s3] = 0	[0040006c] afbf0000 sw \$31, 0(\$29) ; 158: sw \$ra, 0(\$sp)
R20 [s4] = 0	[00400070] 34020004 ori \$2, \$0, 4 ; 164: li \$v0, 4
R21 [s5] = 0	[00400074] 3c011001 lui \$1, 4097 [STR_sum_of_fibonacci_a]
R22 [s6] = 0	[00400078] 34240047 ori \$4, \$1, 71 [STR_sum_of_fibonacci_a]
R23 [s7] = 0	[0040007c] 0000000c syscall ; 166: syscall
R24 [t8] = 0	[00400080] 3c011001 lui \$1, 4097 ; 168: lw \$a0, ARRAY_SIZE
R25 [t9] = 0	[00400084] 8c240000 lw \$4, 0(\$1)
R26 [k0] = 0	[00400088] 34020001 ori \$2, \$0, 1 ; 169: li \$v0, 1
R27 [k1] = 0	[0040008c] 0000000c syscall ; 170: syscall
R28 [gp] = 10008000	[00400090] 34020004 ori \$2, \$0, 4 ; 172: li \$v0, 4
R29 [sp] = 7ffff784	[00400094] 3c011001 lui \$1, 4097 [STR_sum_of_fibonacci_b]
R30 [s8] = 0	[00400098] 34240057 ori \$4, \$1, 87 [STR_sum_of_fibonacci_b]
R31 [ra] = 400018	[0040009c] 0000000c syscall ; 174: syscall
	[004000a0] 3c011001 lui \$1, 4097 [FIBONACCI_ARRAY]; 176: la \$a0, FIBONACCI_ARRAY
	[004000a4] 34240004 ori \$4, \$1, 4 [FIBONACCI_ARRAY]
	[004000a8] 3c011001 lui \$1, 4097 ; 177: lw \$a1, ARRAY_SIZE
	[004000ac] 8c250000 lw \$5, 0(\$1)
	[004000b0] 0c100009 jal 0x00400024 [integer_array_sum]
	[004000b4] 00402020 add \$4, \$2, \$0 ; 181: add \$a0, \$v0, \$zero
	[004000b8] 34020001 ori \$2, \$0, 1 ; 182: li \$v0, 1
	[004000bc] 0000000c syscall ; 183: syscall

4 Learning & Insights

Through this laboratory activity, I learned how to check what values goes into the registers, how to load values from the registers, and also in how to manipulate the values within it. I also learned how to translate higher-level programming languages like C code into MIPS assembly as I created a program in C before solving it using MIPS. Both are quite similar in solving the task but MIPS is much more integral in trying to see how the values change step-by-step through the registers.

Through checking out the main function, I also learned that to exit the program we had to store 10 into \$v0 which probably means that it is a code for exiting the program when the syscal detects it. This is useful to know in the future as my programs probably won't work if I don't do so.

In looping the array, since \$a0 points to the address of the first integer in the array, we had to shift left by two bits to access the next element in the array as each integer in the array is composed of 4 bytes or 1 word. This is important to know since it actually composes of two lines to access the next array which is shifting it by 4 bytes through sll or 4*i where i is the current index and then adding it to \$a0. This means that progressively, to access the second element of the array the offset would be 4 then the third would be 8, the fourth element would be 12, etc. Overall, this means that to access the next element of the array, we need to always shift left by 4 bytes since an integer in MIPS is 1 word or 4 bytes.

5 Comparison to a High-Level Implementation

Here is an implementation of the lab problem in the language C.

```
int sum(int arr[], int n)
{
    int sum = 0; // initialize sum

    for (int i = 0; i < n; i++)
        sum += arr[i];

    return sum;
}

int main()
{
    int arr[] = {1,1,2,3,5,8,13,21,34,55};
    int n = sizeof(arr) / sizeof(arr[0]);
    printf("The sum of the first %d Fibonnaci numbers is %d", n, sum(arr, n));
    return 0;
}
```

Similar to our assembly source code, the main acts as the caller and the function sum acts as the callee. We can also see that the iteration is much more understandable and much shorter as we don't have to add instructions such as sll or shift-left-logical and add to access the current and next element of the array. The loops are also slightly different as in C, all the necessary procedures to loop is within a single line while in MIPS we had to use several lines of code to initialize the value, increment the *i*, and whether to branch when a condition is met or not .

The way of printing the output is quite different, while in C we can simply write printf and the output we want to print, the way we had to print in MIPS is to use instructions such as load immediate, load address, and syscall to pass control to the system and do the printing for you.

6 Conclusion

Overall, this laboratory activity helped polish what I learned from the lecture videos provided, it enabled me to understand MIPS assembly by hand. Similar to other programming languages, I realized that it is important to understand MIPS Assembly as it will assist me in future projects especially those that includes the specific use of addresses, memories and registers. This particular task helped in discovering how the program moves data around, how it transforms that data, and in how we can make programming decisions based on what we saw. I am looking forward to what I will learn and would then be capable to do after solving the other future tasks in this laboratory activity.