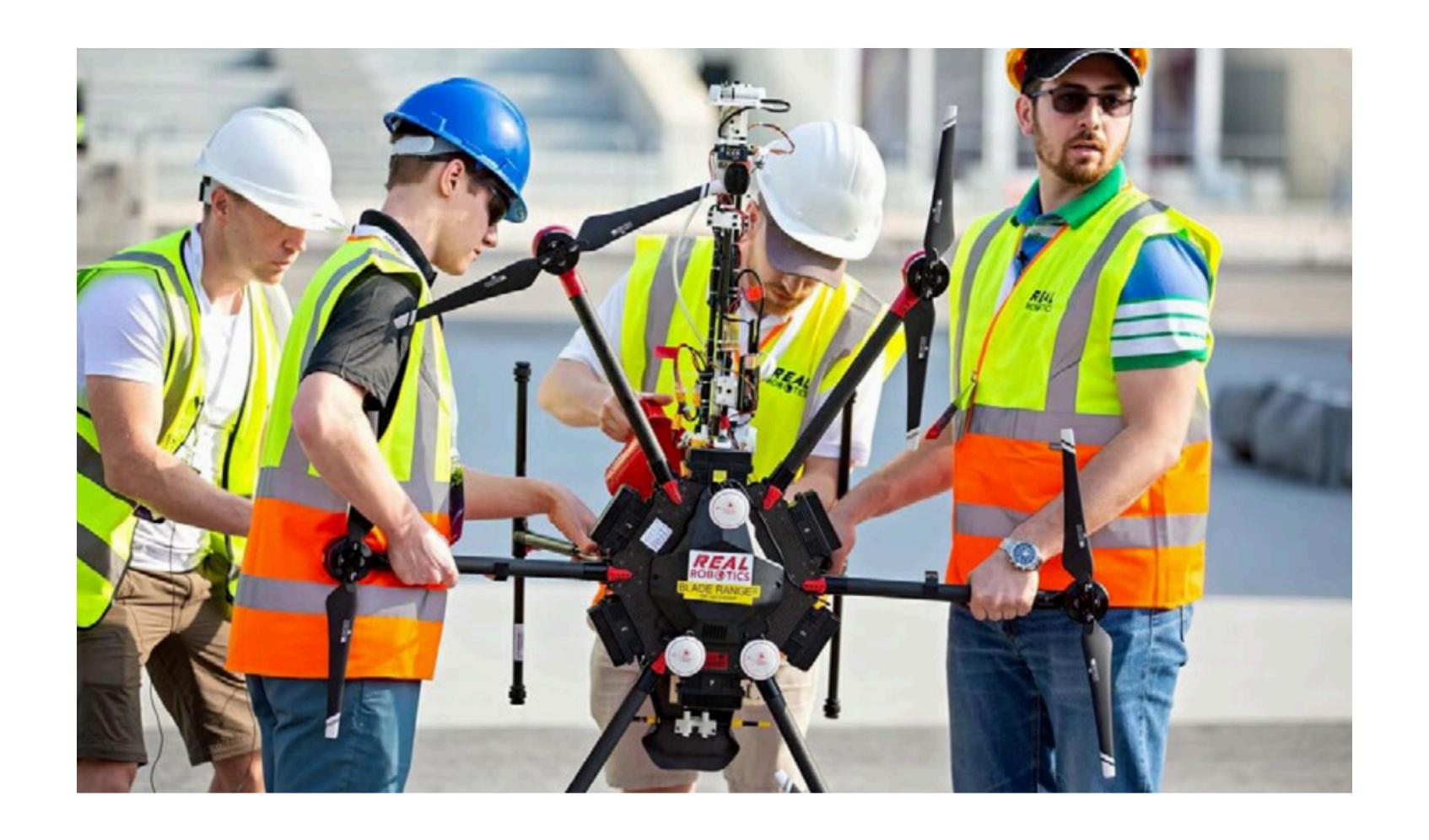
O S Practice

Timed Verification of Robot Software

ROBOSTAR YORK

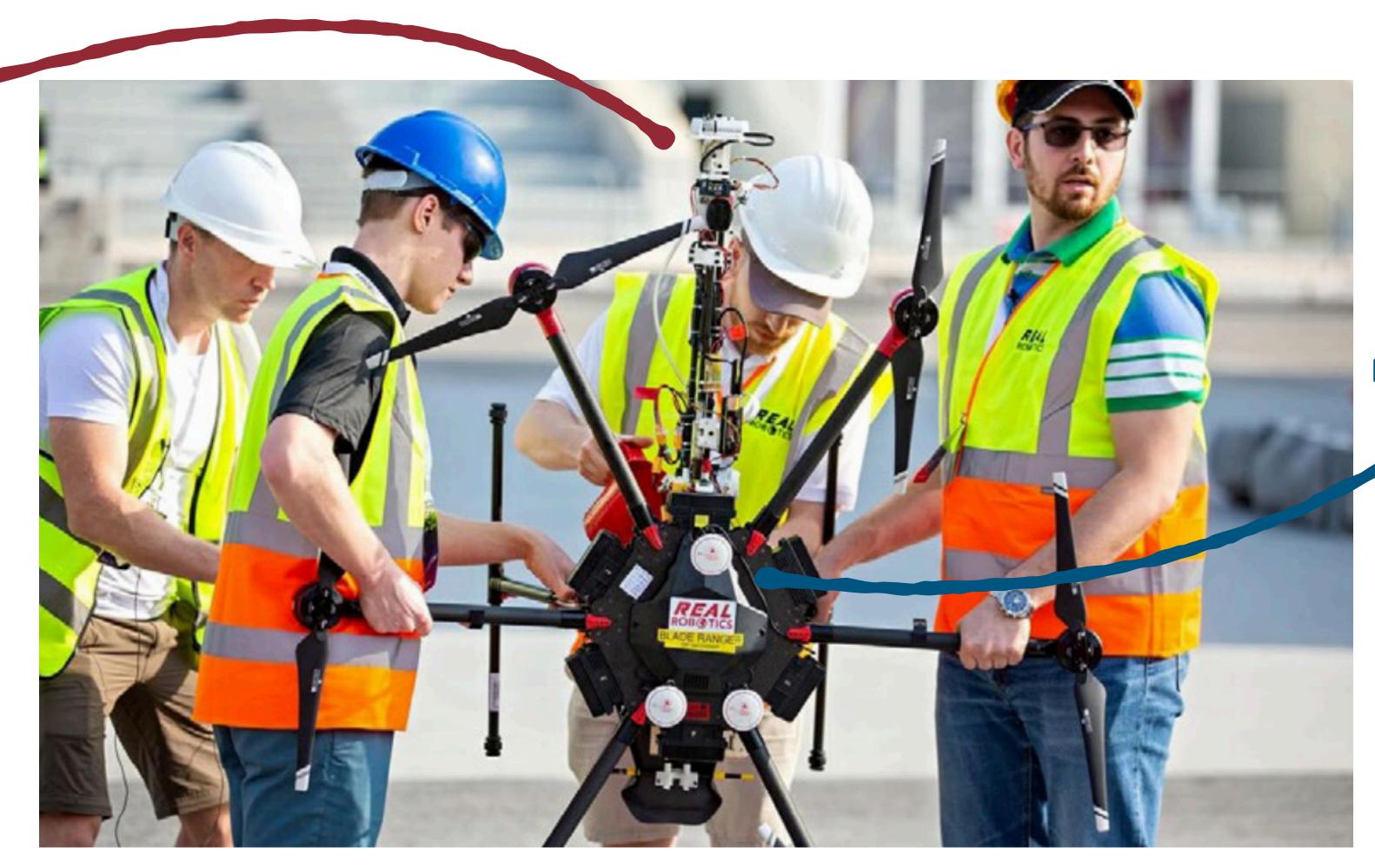
Matt Windsor (they/he), Concurrency Workshop 2022





Concurrency Workshop 2022

nozzle controller



main controller

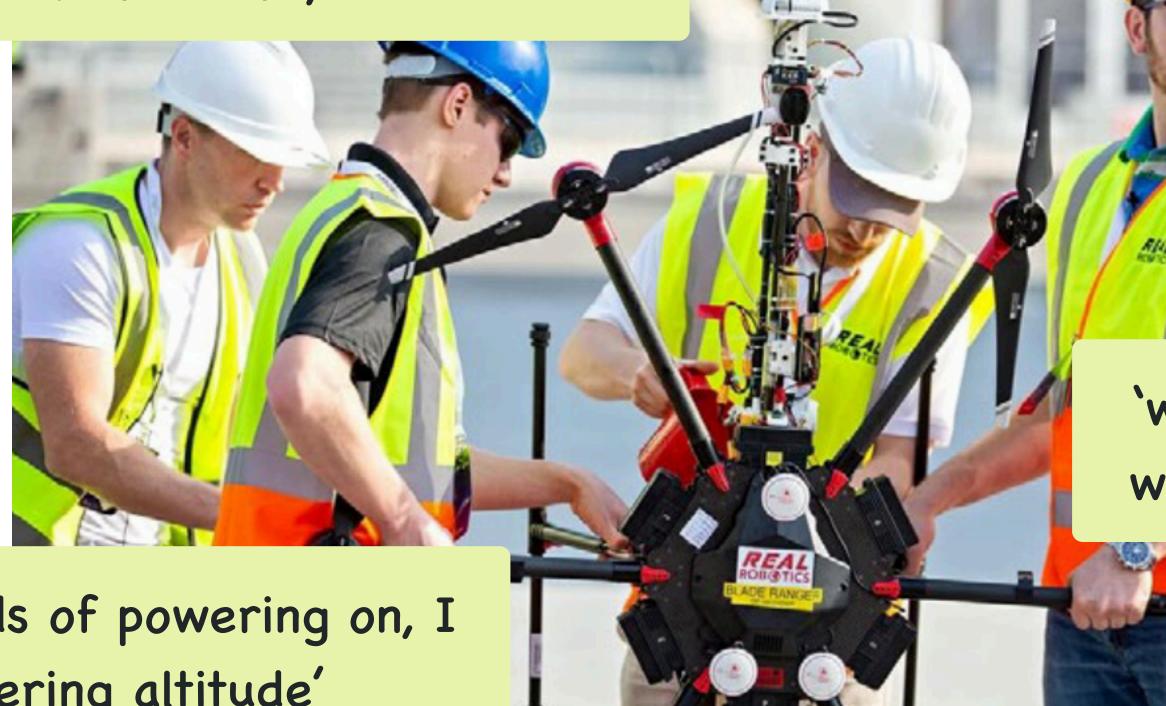


main controller nozzle controller robot software verification concurrency verification ROBOSTAR

Matt Windsor robostar.cs.york.ac.uk

YORK

'within X seconds of a fire appearing in my field of vision, I detect it'



'within Z seconds of a battery low warning, I return to home position'

'within Y seconds of powering on, I am at hovering altitude'





robot software verification

timed concurrency verification

am at hovering altitude'





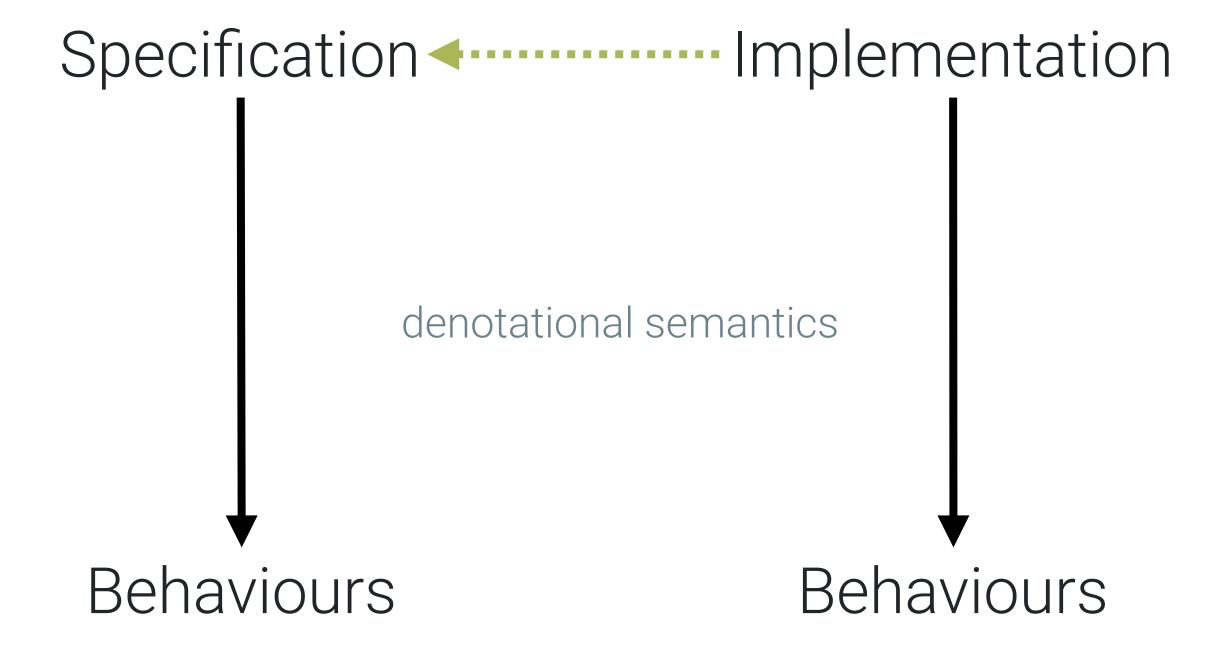
Verification approach Refinement

Specification ----- Implementation



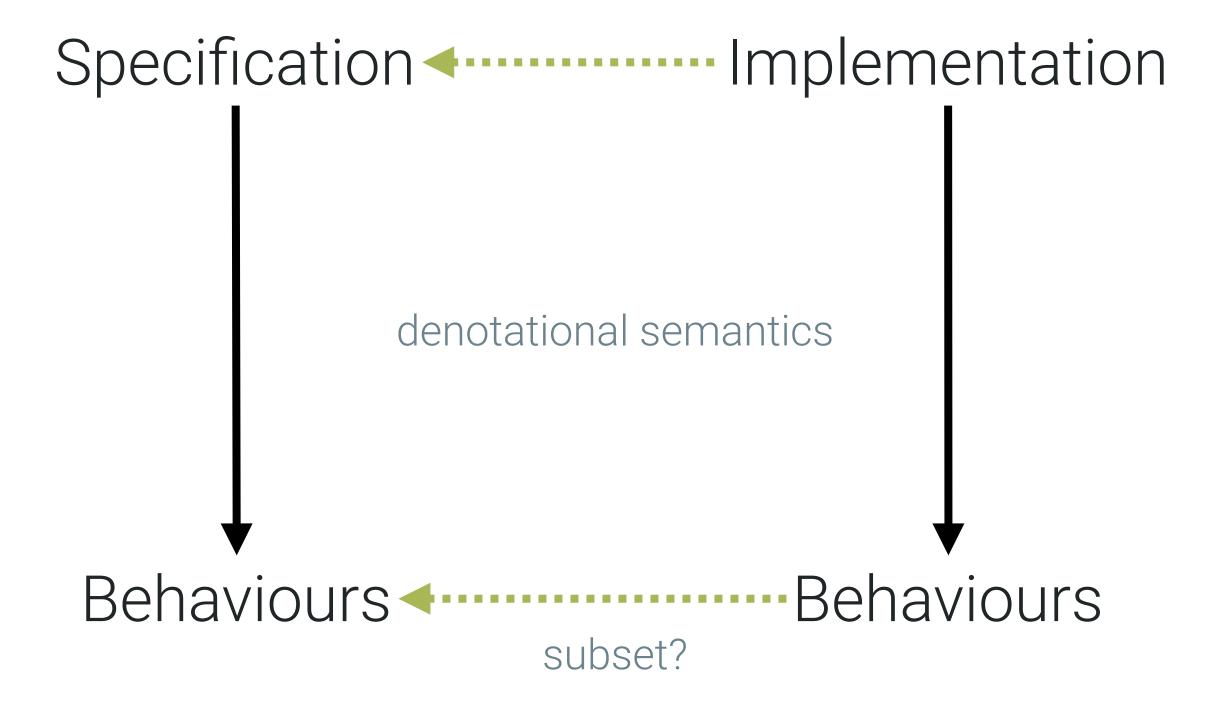
robostar.cs.york.ac.uk Matt Windsor

Refinement



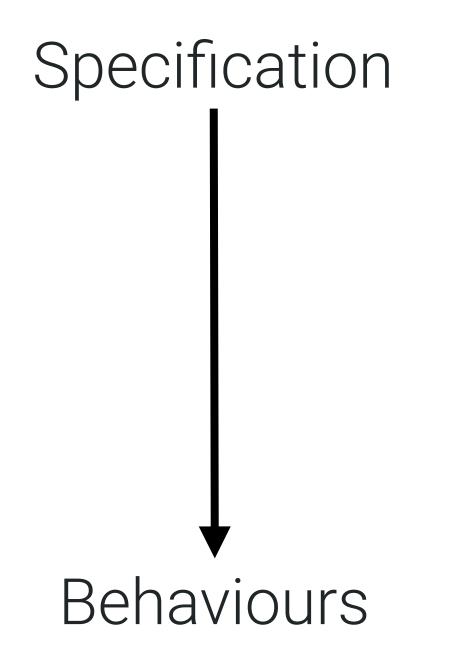


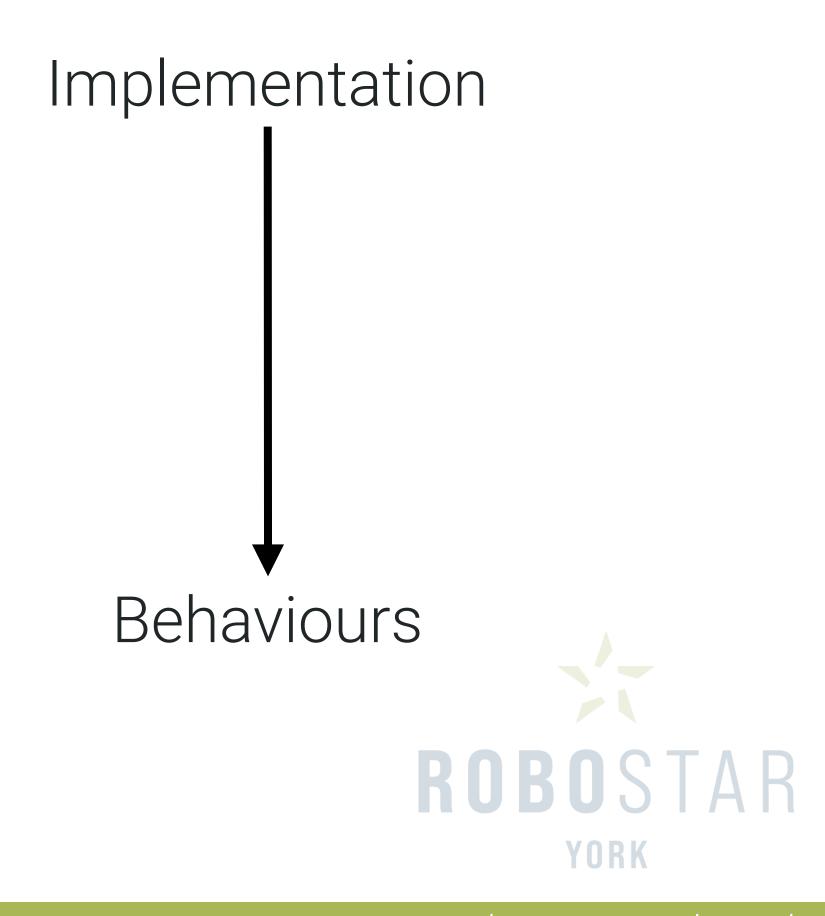
Refinement



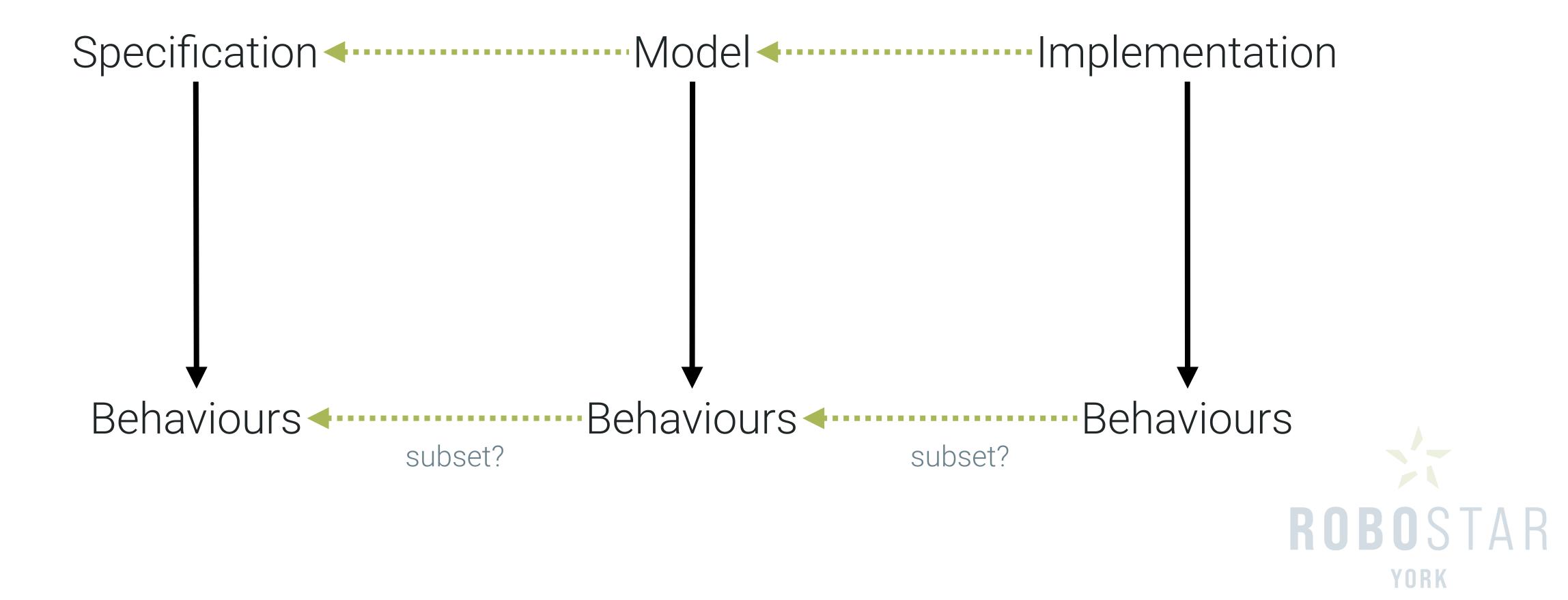


Verification approach Refinement: transitivity





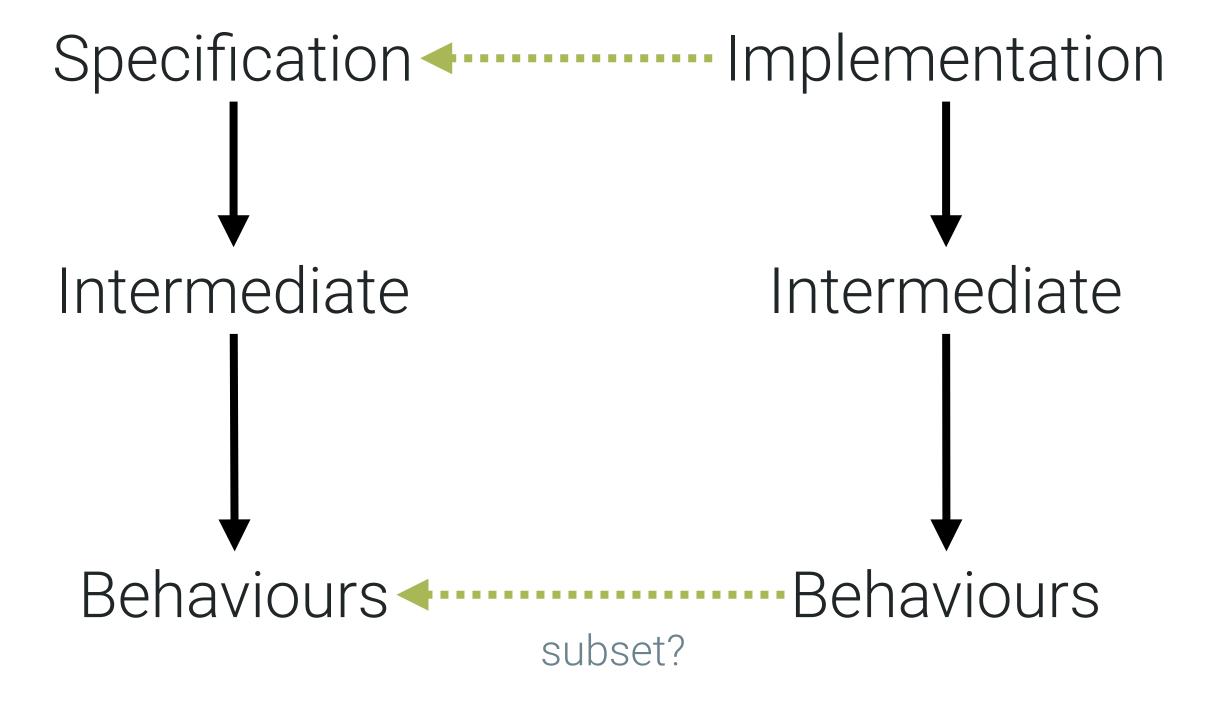
Refinement: transitivity



Refinement: transitivity, again



Refinement: transitivity, again





CSP

Process algebra for refinement

STOP deadlock

P[]Q external choice

P;Q sequential composition

SKIP termination

P|~|Q internal choice

μ x. P(x) recursion

a → P event

P | Q interleaving

P/\Q interrupt

P[A]Q

alphabetised parallel

...and many others STAR

CSPRefinement

Spec [M=Impl

all behaviours of process **Impl**according to *semantic model* **M**are behaviours of process **Spec**according to **M**



'within X seconds of a fire appearing in my field of vision, I detect it'



'within Z seconds of a battery low warning, I return to home position'

'within Y seconds of powering on, I am at hovering altitude'



robostar.cs.york.ac.uk Matt Windsor

tock-CSP Discrete-time process algebra



event representing passage of one time unit



tock-CSP

Developments: FDR timed sections

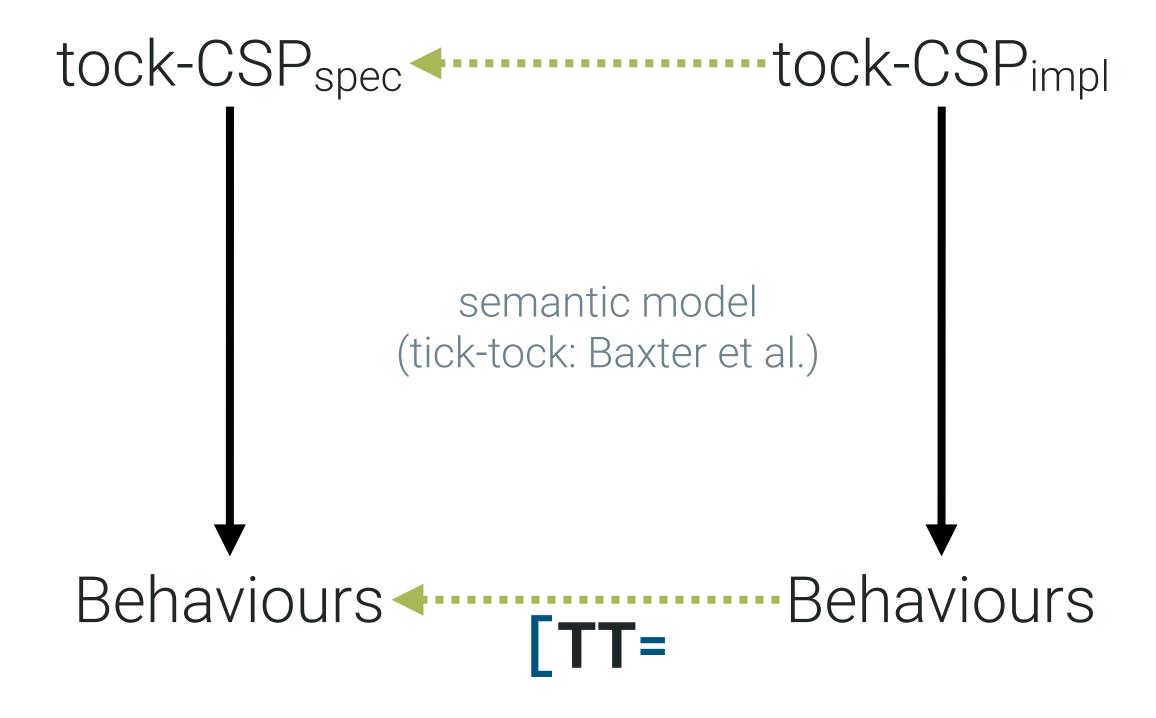
[], \(\) etc. **lifted** to accommodate **tock**

distinction between STOP (time may progress) and USTOP (timelock)



tock-CSP

Developments: tick-tock semantic model

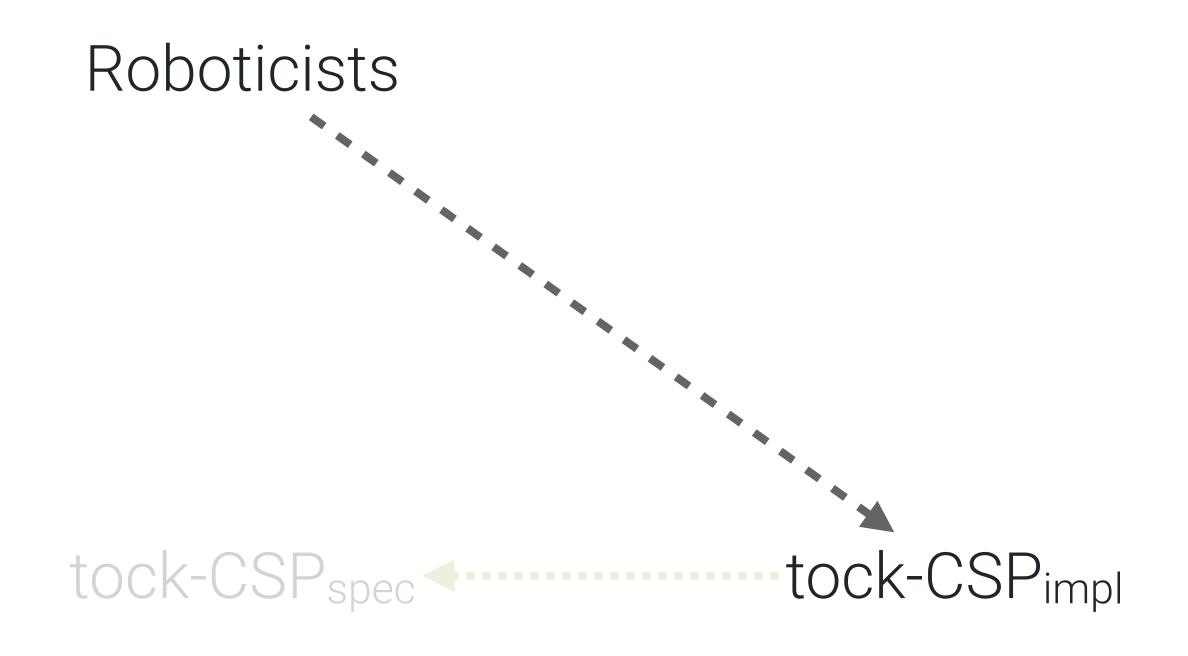


model-shifting into traces model for FDR etc.

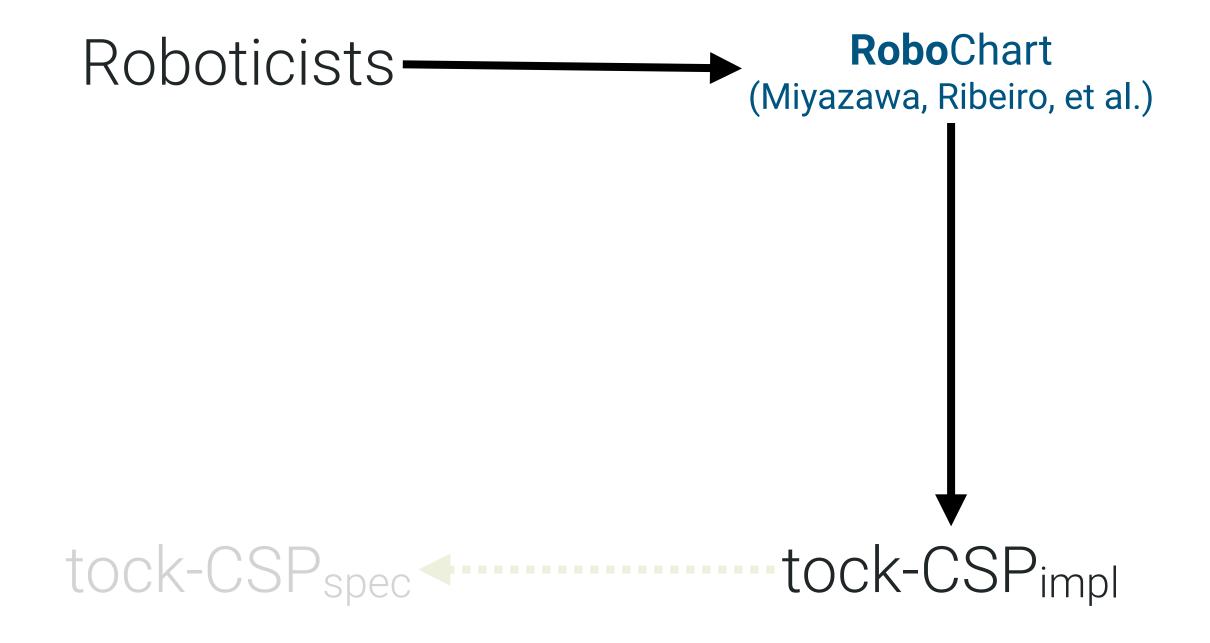


tock-CSP_{spec} tock-CSP_{impl}





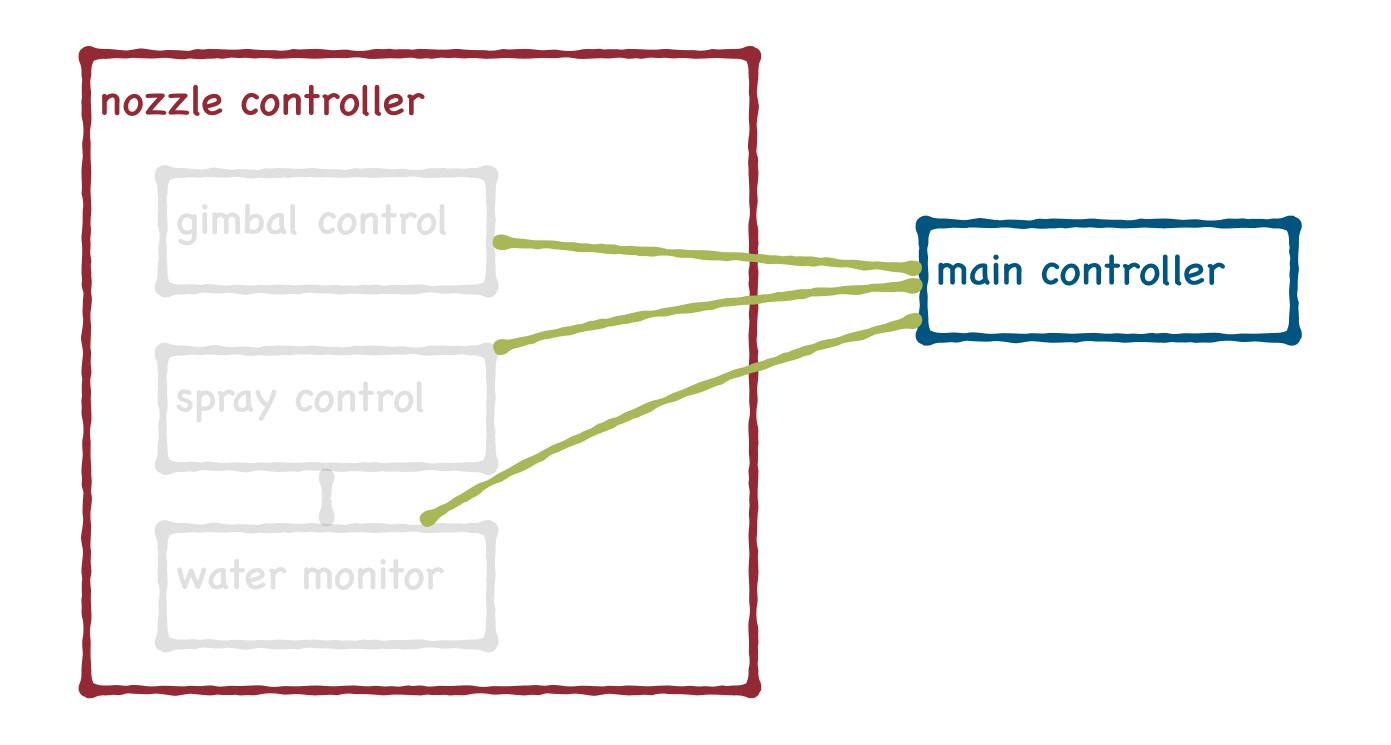


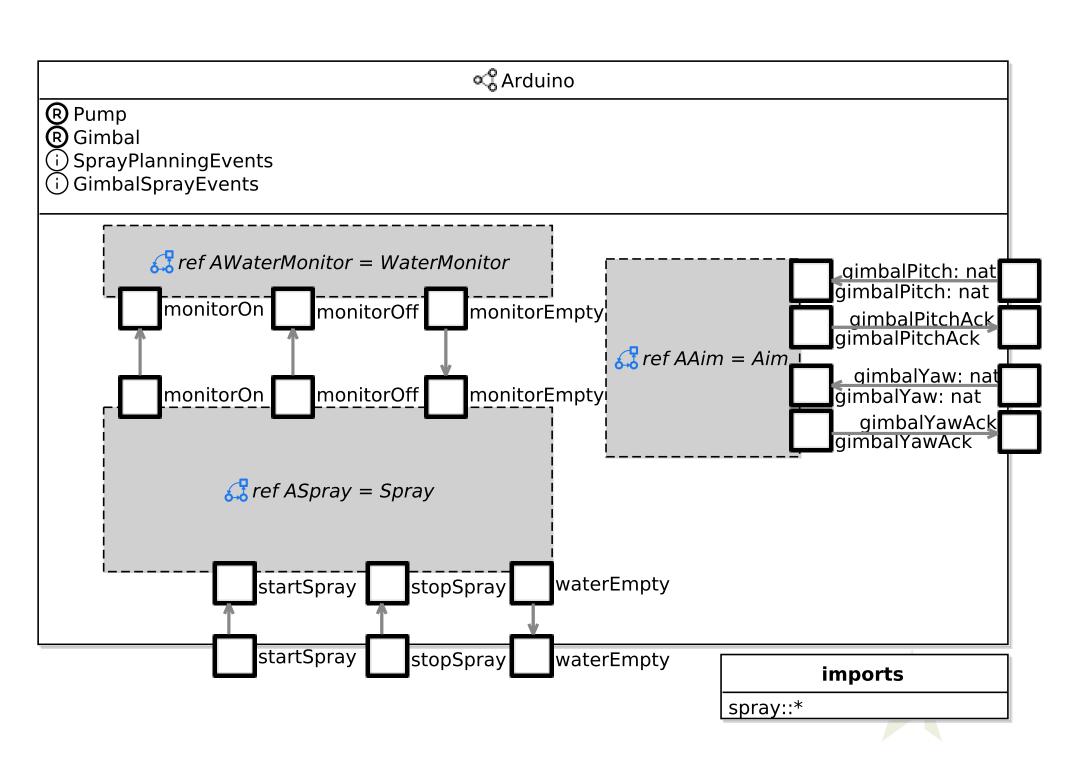




RoboChart

Graphical notation for software design models

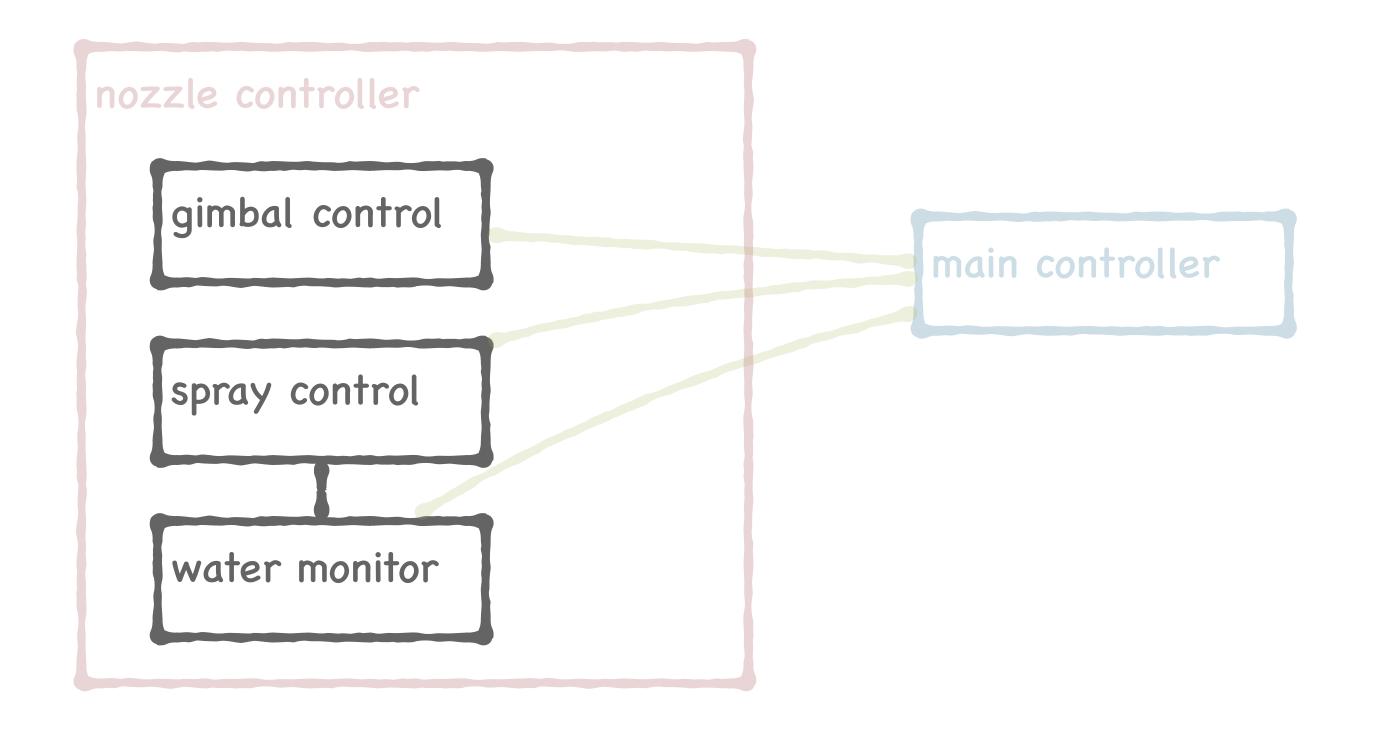


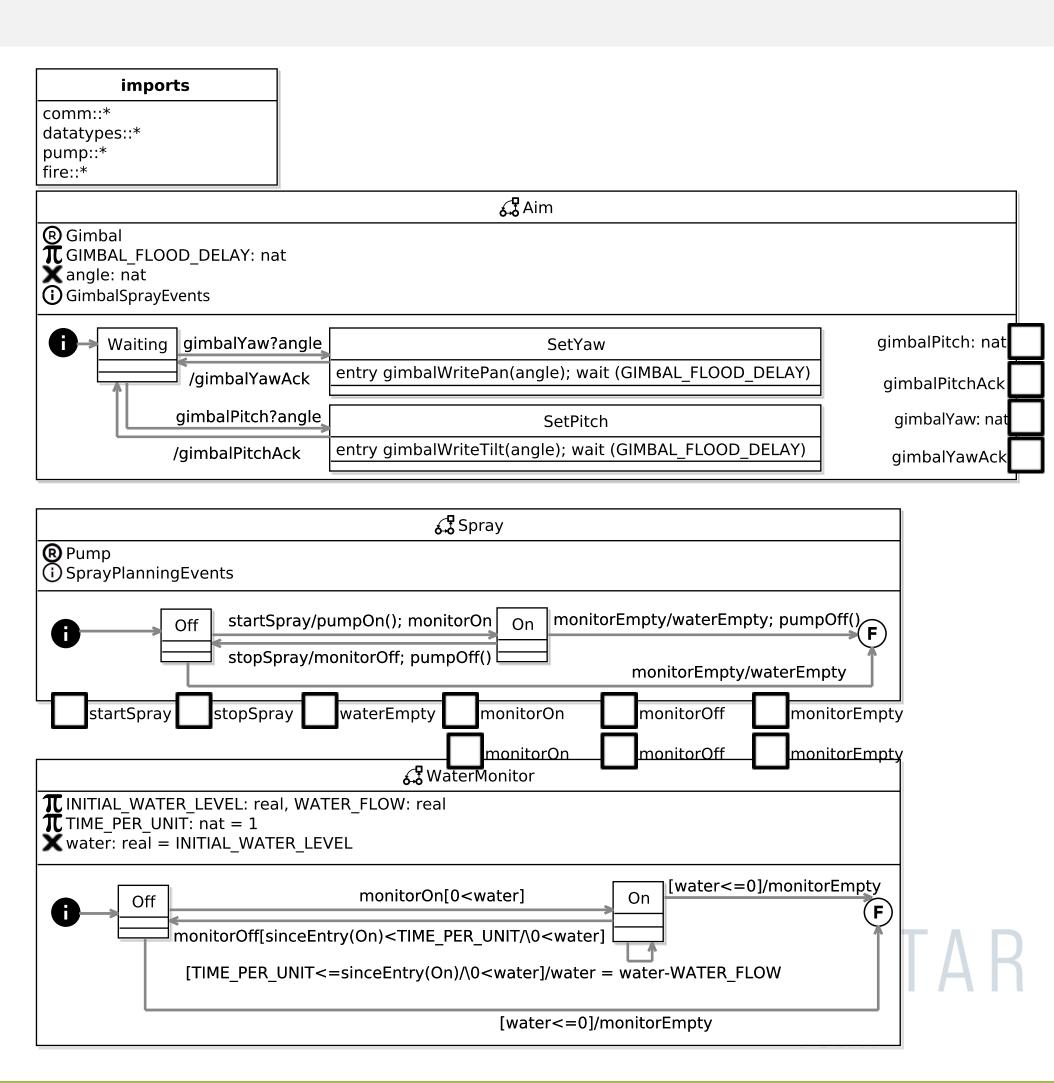


ROBOSTAR YORK

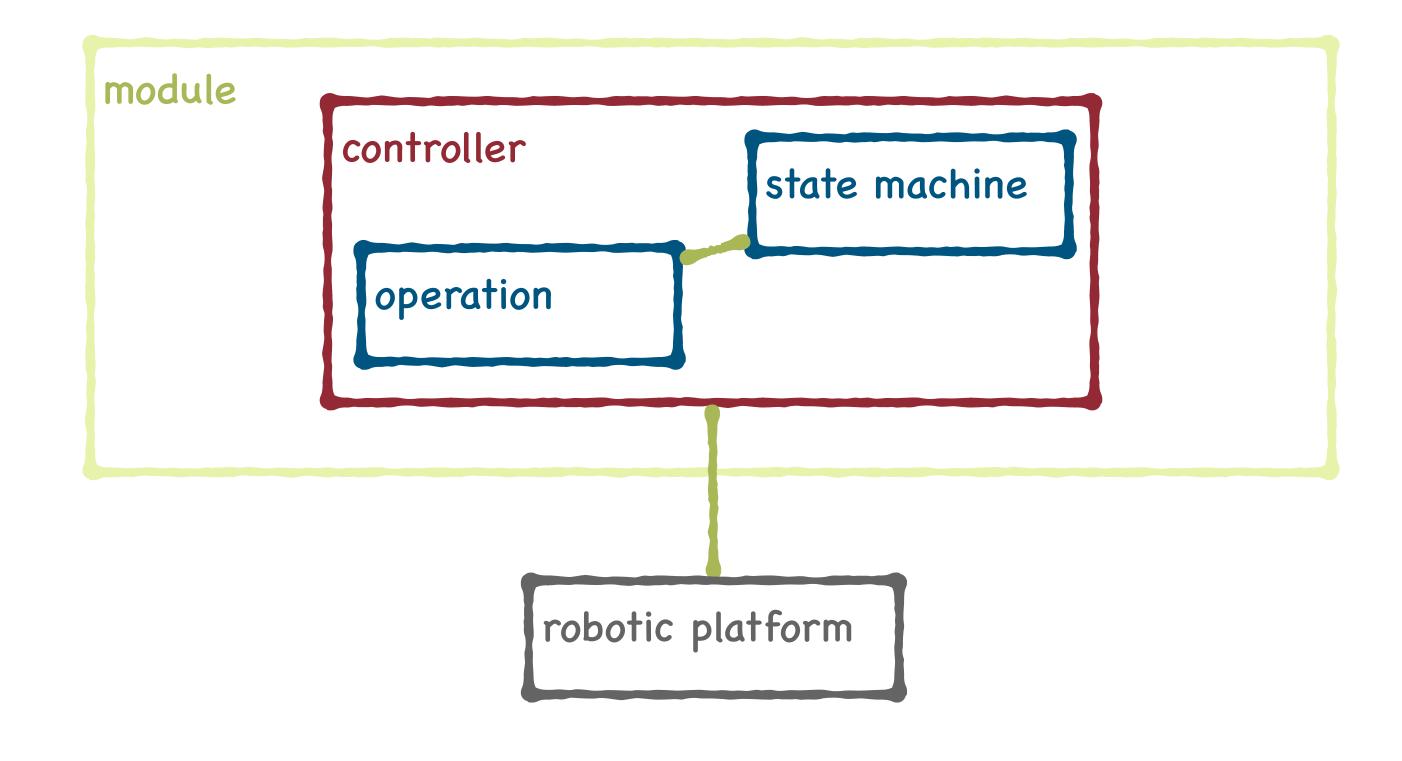
RoboChart

Graphical notation for software design models



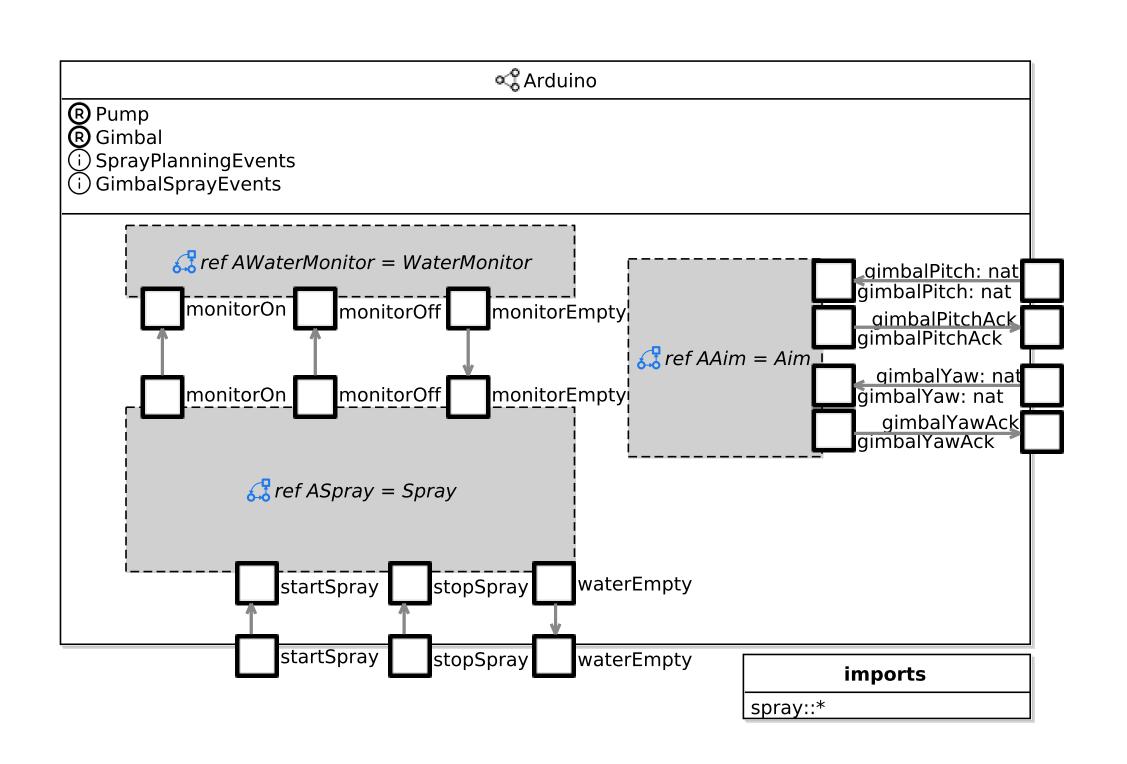


RoboChart Component model





RoboChartTranslation of controller to CSP



State machines become **processes** parallelised by shared events

eg

```
AWaterMonitor

[ { monitorOn, monitorOff, monitorEmpty } ]

( ASpray || AAim )
```

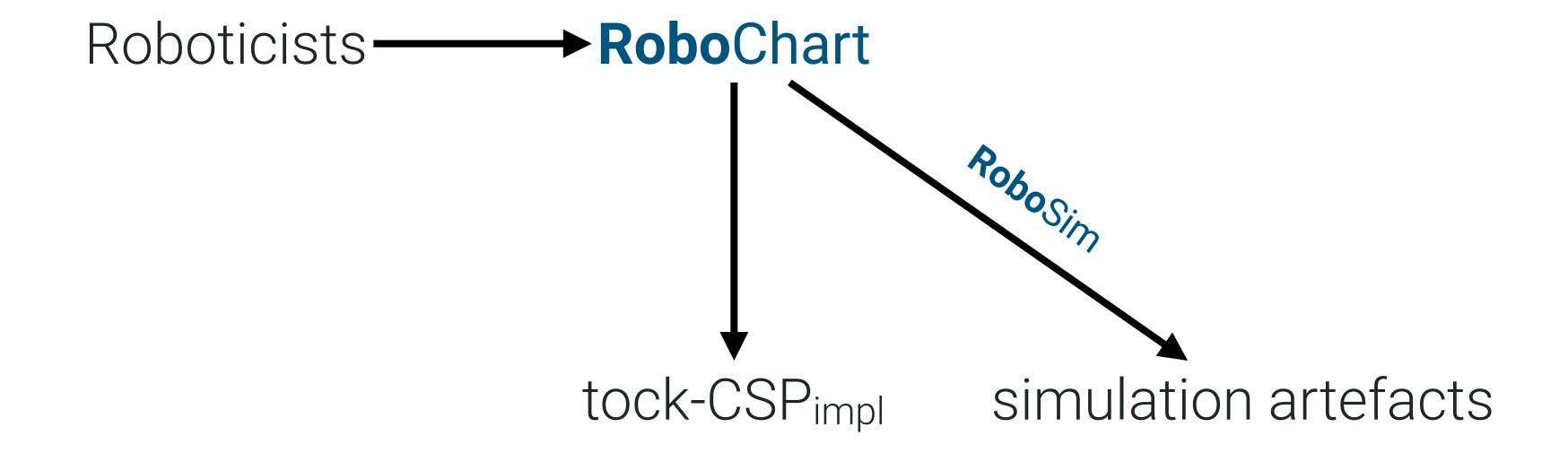
Matt Windsor robostar.cs.york.ac.uk

ROBOSTAR

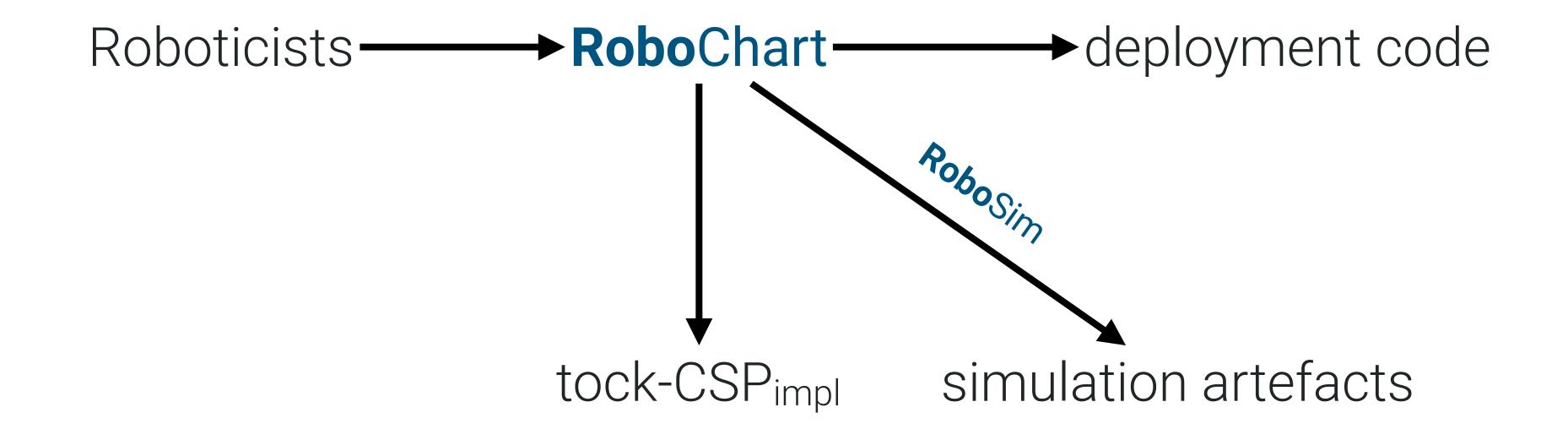
YORK



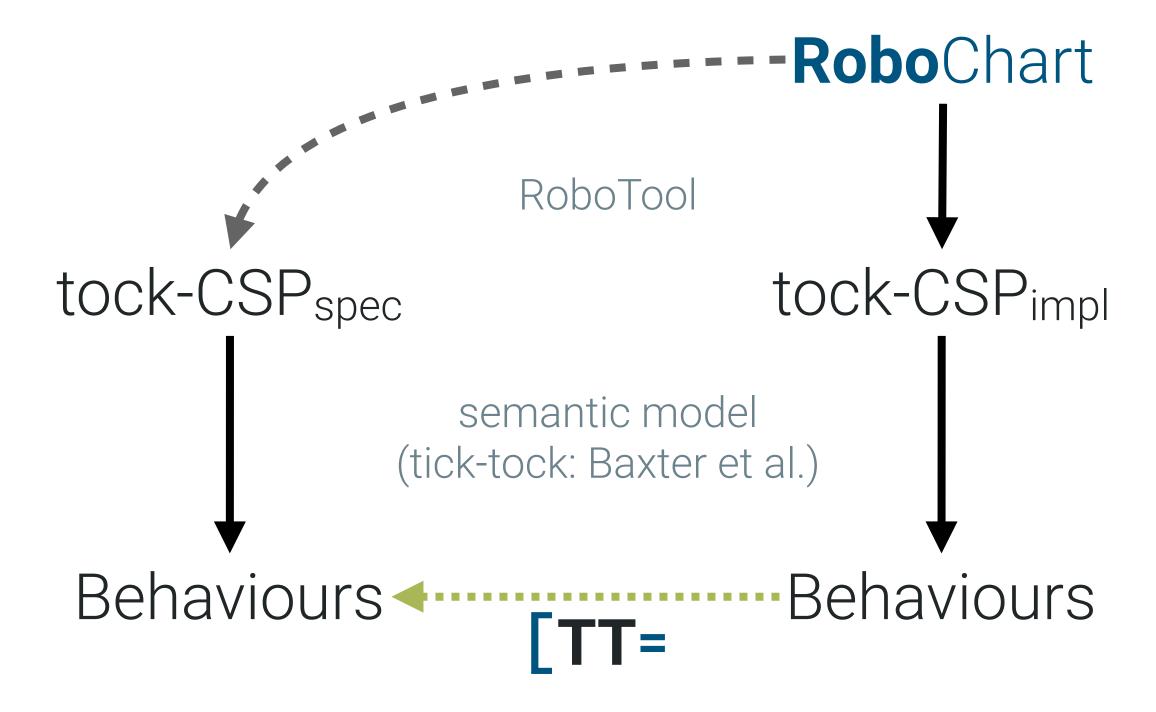




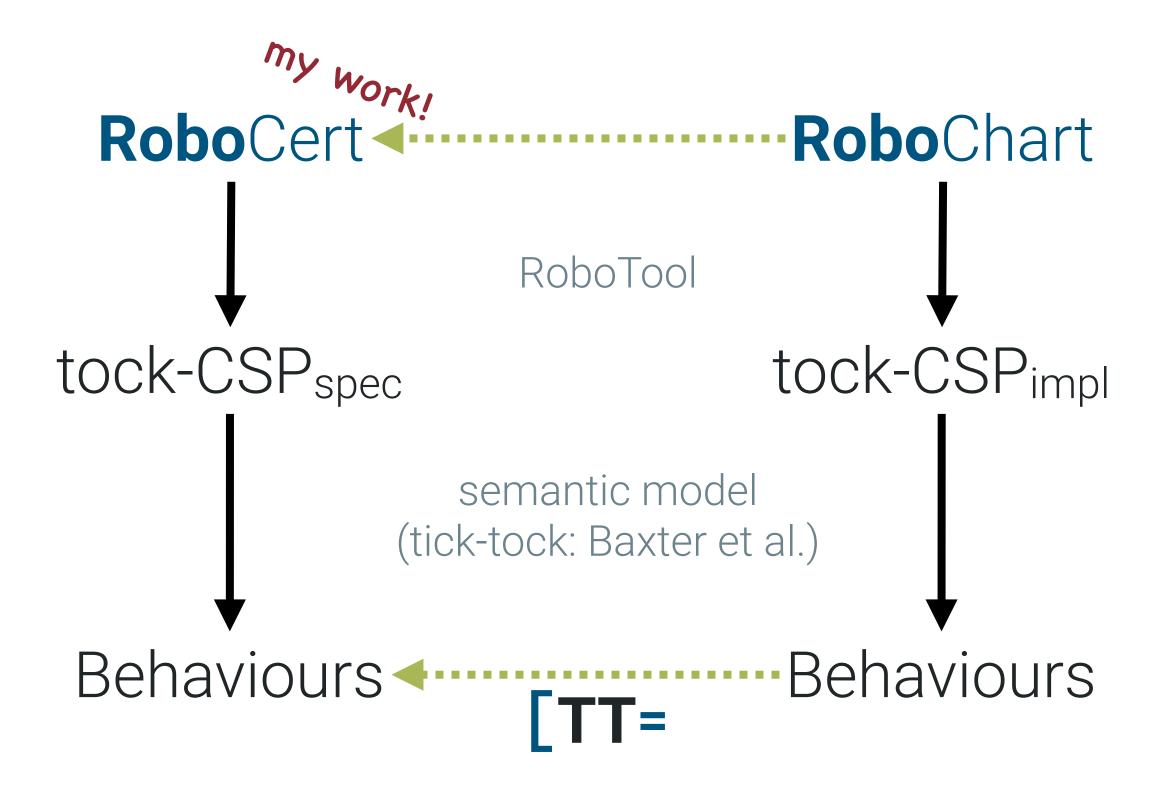














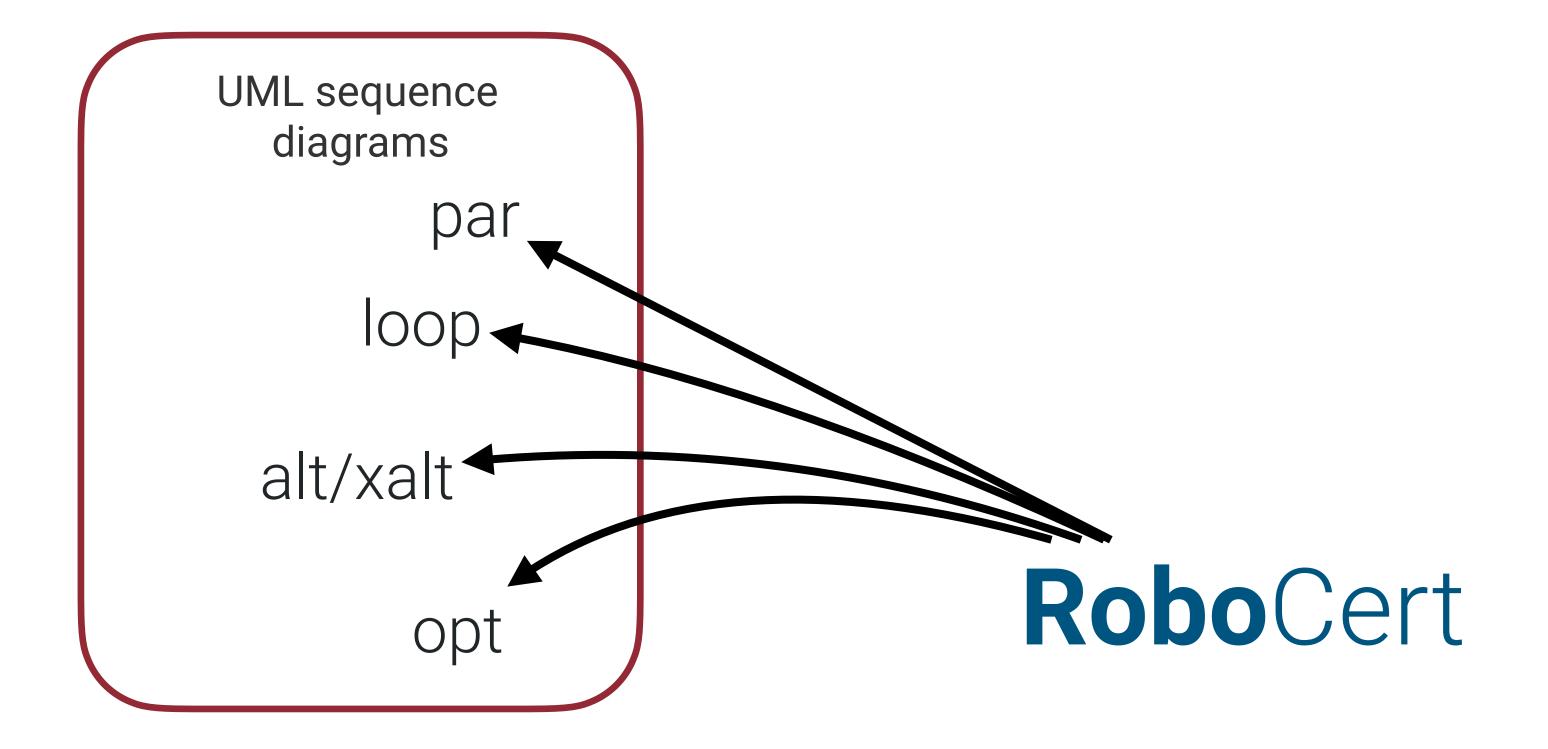




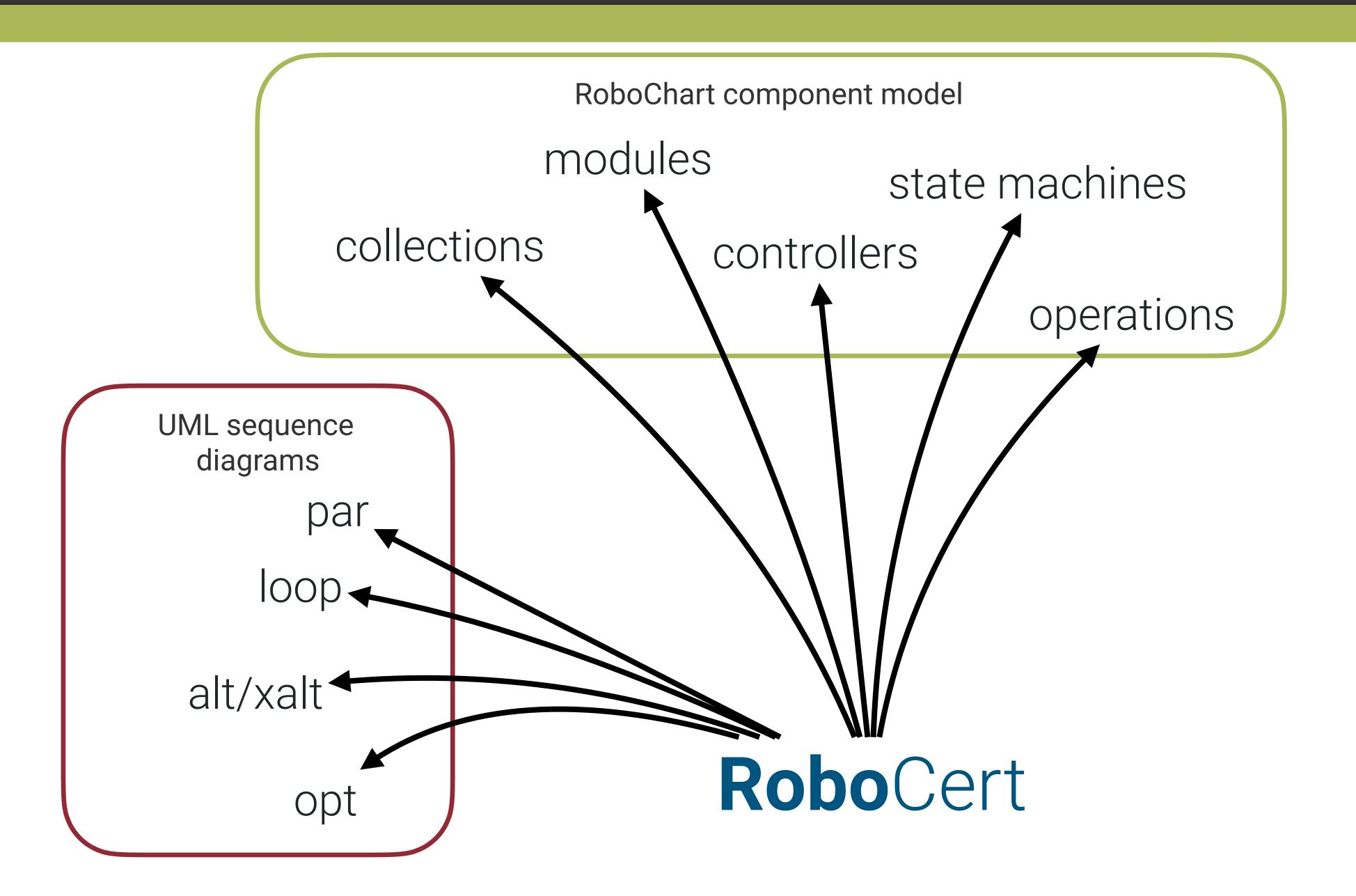
20

Matt Windsor robostar.cs.york.ac.uk

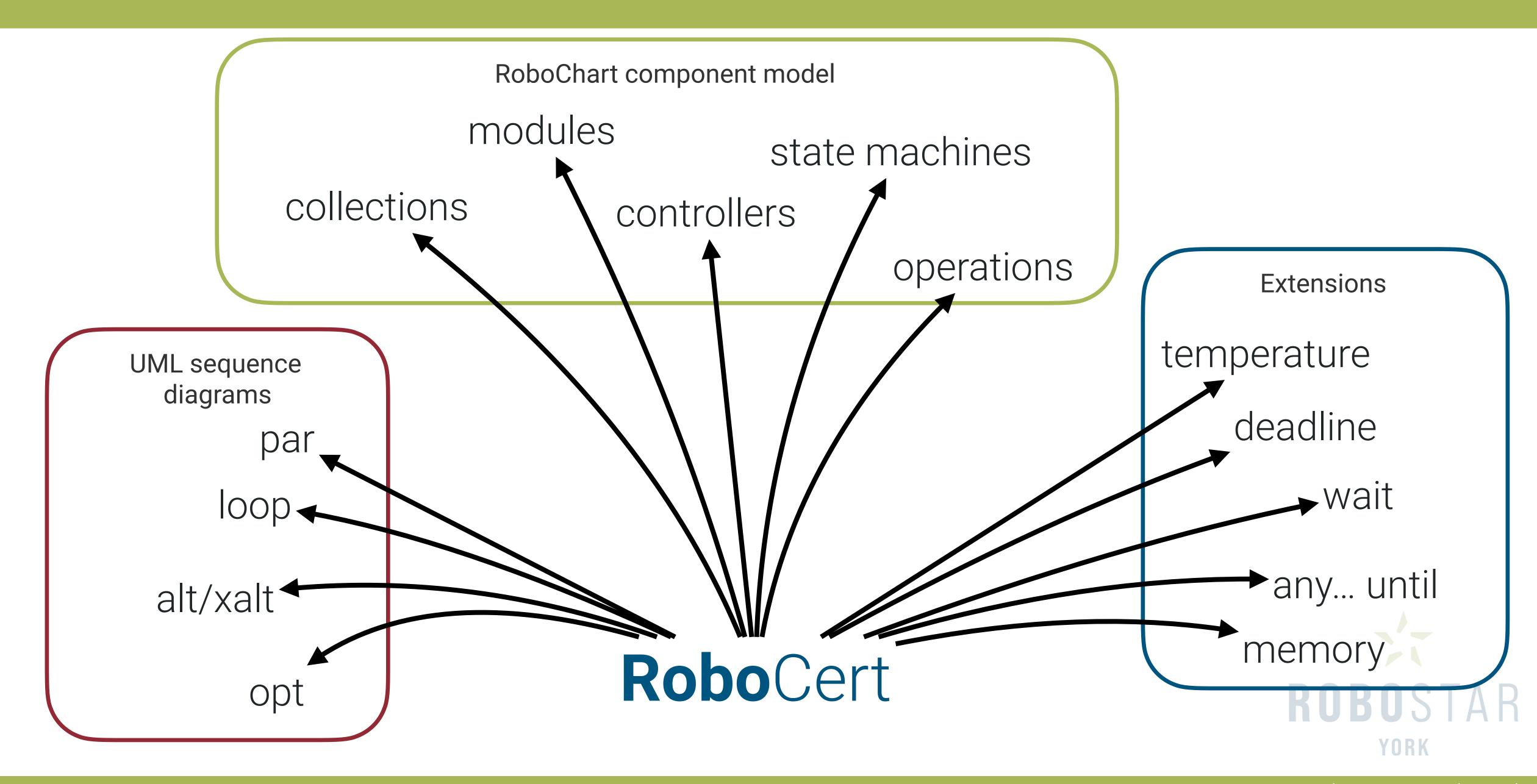
Concurrency Workshop 2022



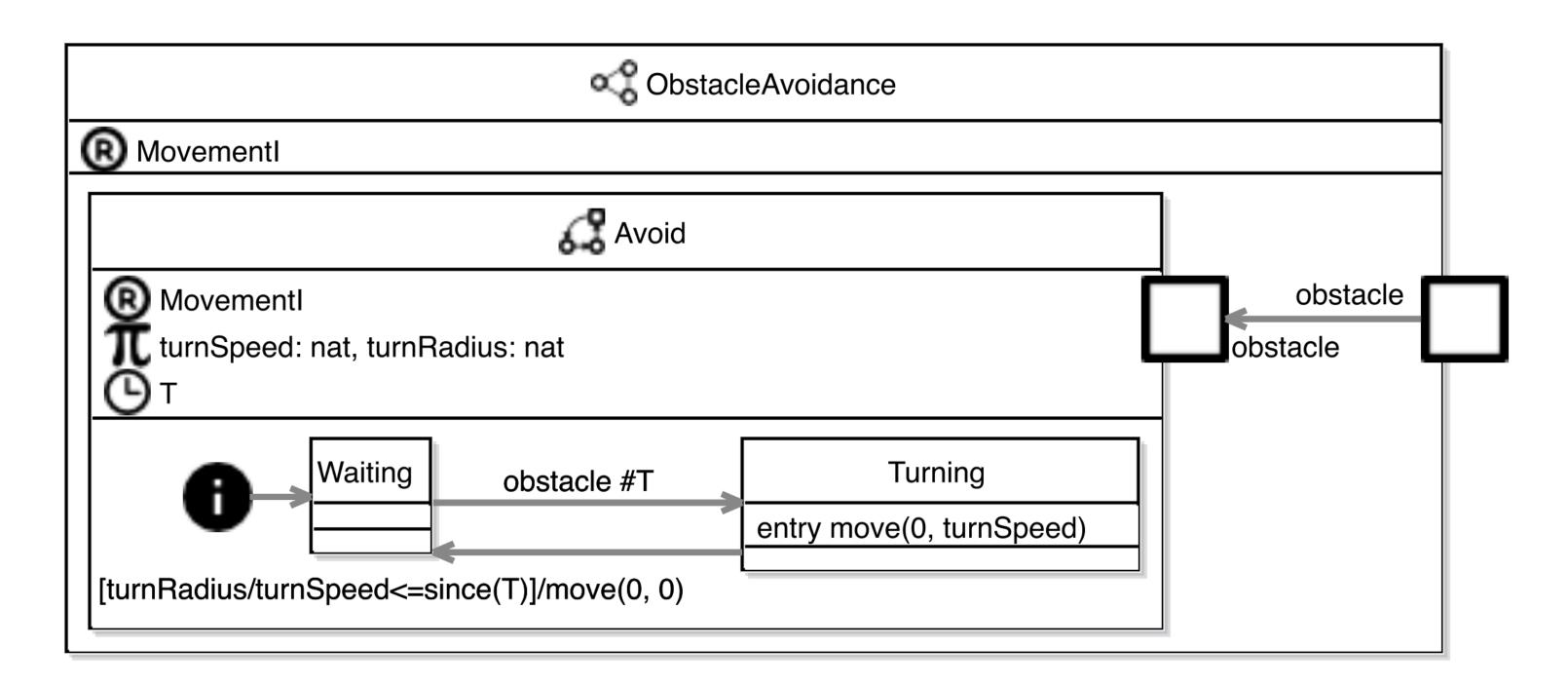








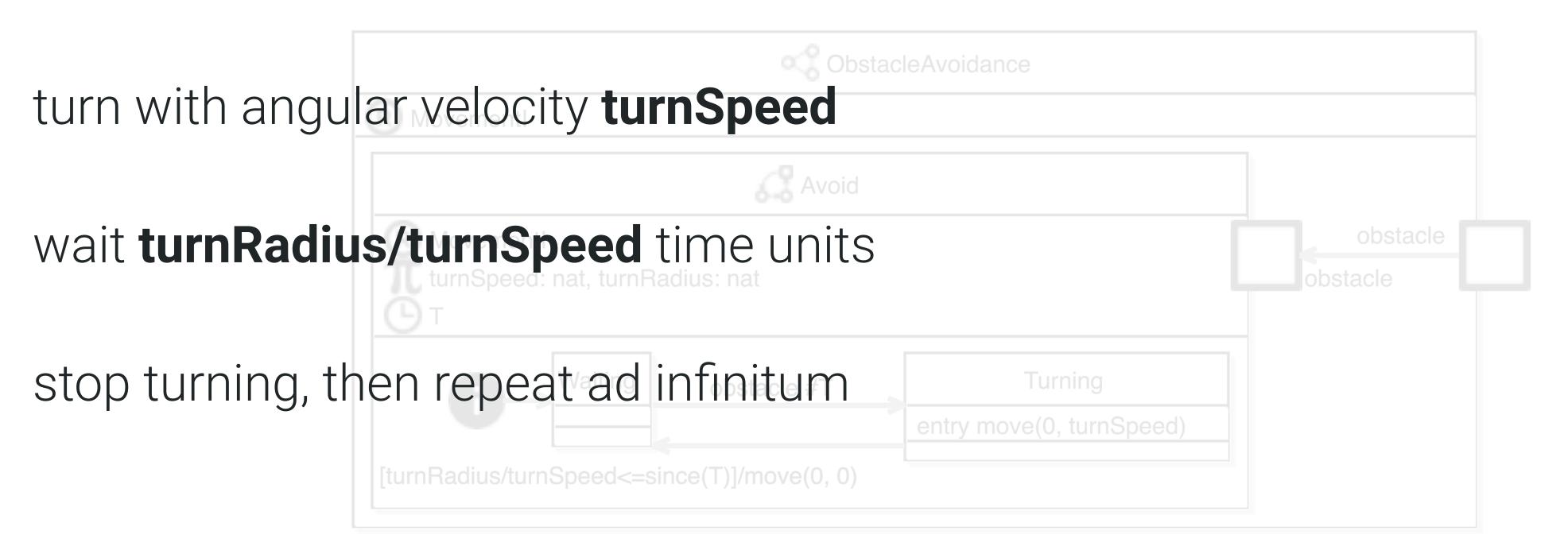
RoboCert Another motivating example





RoboCert Another motivating example

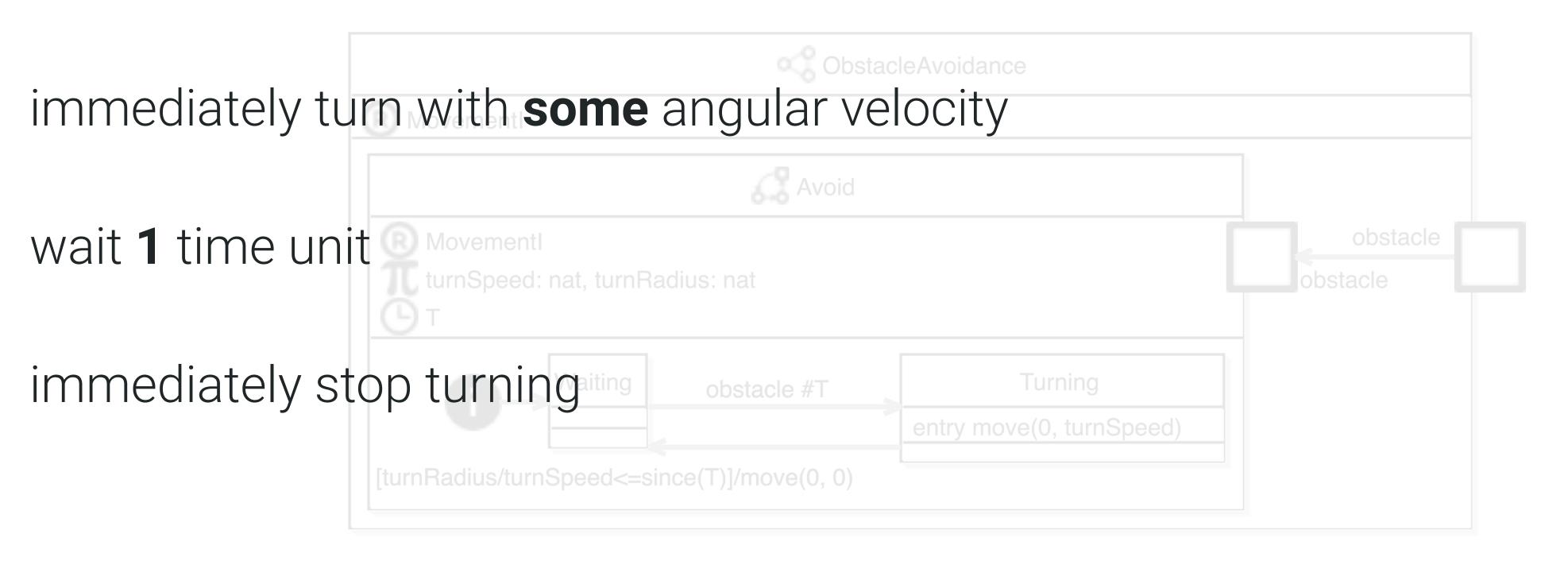
wait for warning of obstacle





RoboCert Behaviour set, in English

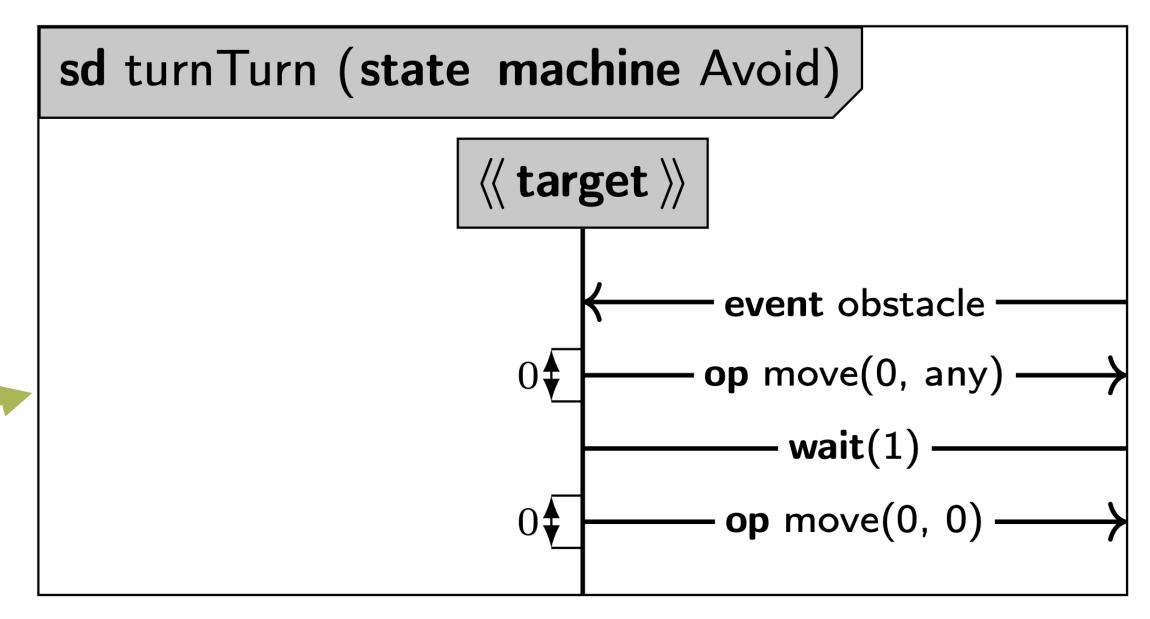
receive warning of obstacle





RoboCert Behaviour set, as a sequence diagram

receive warning of obstacle immediately turn with **some** angular velocity wait 1 time unit immediately stop turning ObstacleAvoidance R Movement **Avoid** R Movement obstacle TturnSpeed: nat, turnRadius: nat obstacle Waiting Turning obstacle #T entry move(0, turnSpeed) [turnRadius/turnSpeed<=since(T)]/move(0, 0)





RoboCert

Interface with RoboChart component model

```
sd turnTurn (state machine Avoid)

(\langle target \rangle)
```

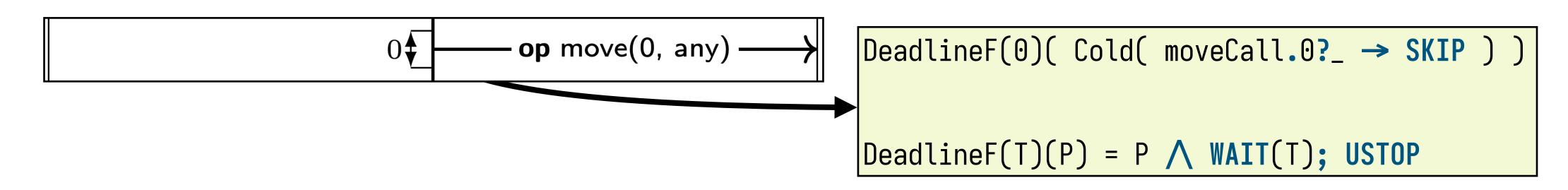
targets: RoboChart components or 'subcomponents of' said components

lifelines: targets or subcomponents of targets

diagram edge corresponds to 'world': component(s) outside the target ROBOSTAR

ROBOSTAR YORK

RoboCertDeadlines



Hybrid of UML notation and RoboChart discrete time semantics

Use timestop to encode deadlines



RoboCert

Deadlines as a use of timestop



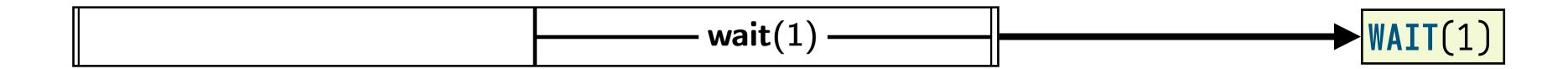


both sides of interrupt must synchronise on tocks

after T units, rhs no longer permits time, so no more time can pass in PROBOSTAR

OBOSTAR YORK

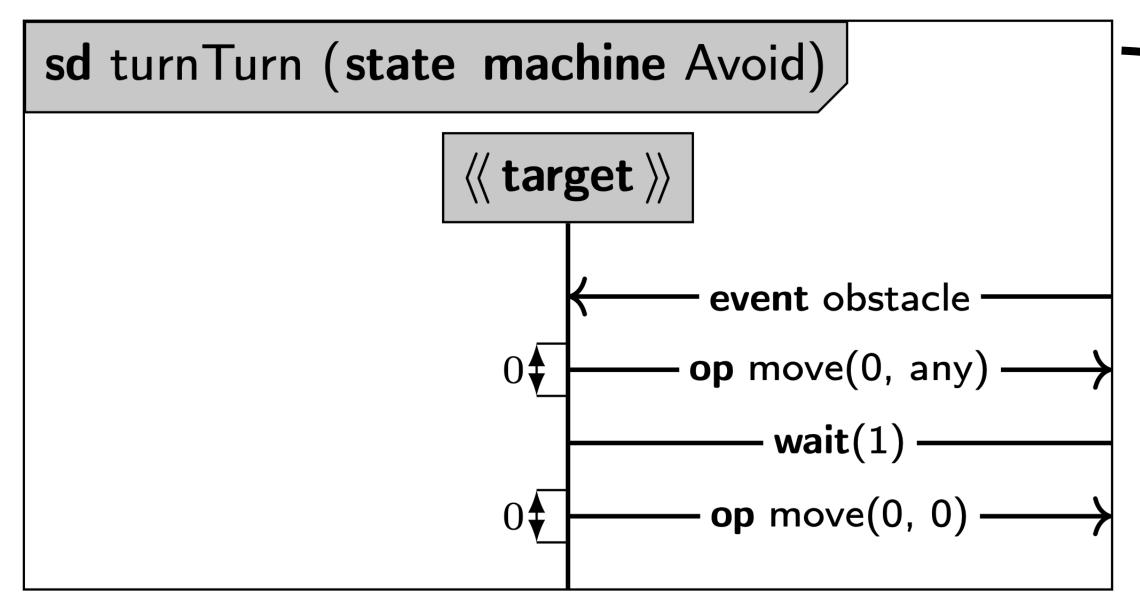
RoboCert Waits



Waiting for *n* units = sequence of *n* tocks



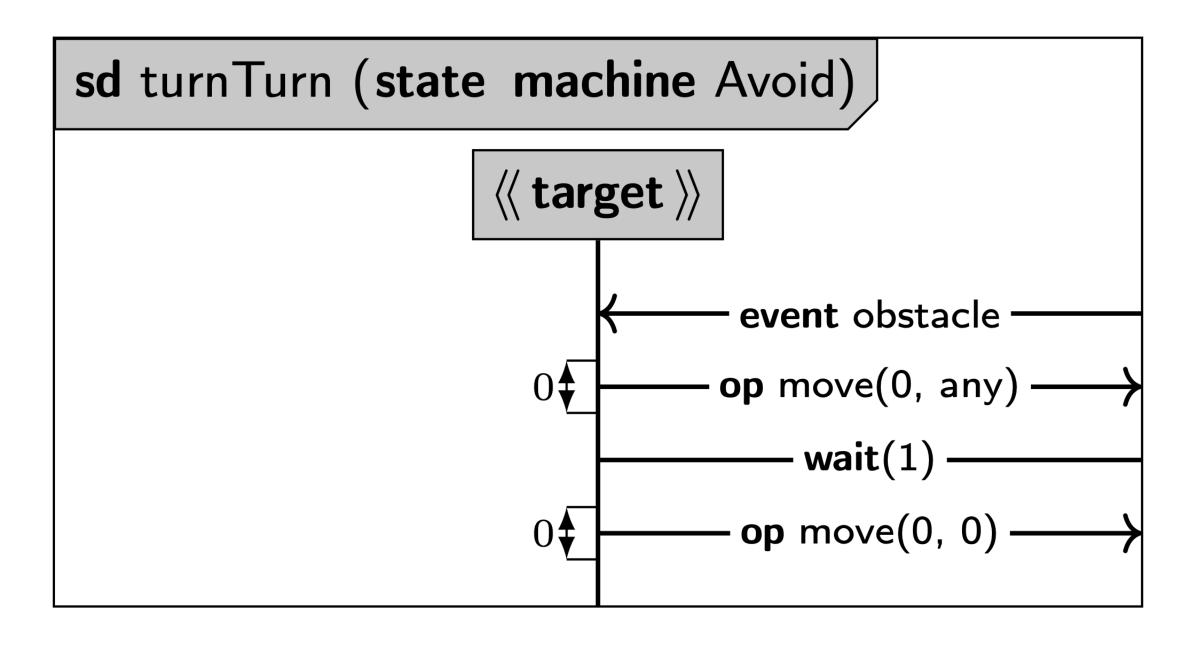
RoboCert CSP of diagram



```
turnTurn = (
  Cold( obstacle.in → SKIP );
  DeadlineF(0)( Cold( moveCall.0?_ → SKIP ) );
  WAIT(1);
  DeadlineF(0)( Cold( moveCall.0.0 \rightarrow SKIP ) )
Cold(P) = P \sim |tock \rightarrow P|
DeadlineF(T)(P) = P \wedge WAIT(T); USTOP
```

YORK

RoboCert Assertions



assertion A: turnTurn is observed in the traces model
... is not observed timed

... holds

... does not hold



RoboCert CSP of assertions

```
assertion A: turnTurn is observed in the traces model

... is not observed timed

... holds

... does not hold

( Avoid [T= turnTurn; STOP )

not ( Avoid [T= turnTurn; STOP )

( turnTurn [T= Avoid )
```

in **timed** model, we use semantic model **TT**



Concluding...

Building blocks towards reasoning

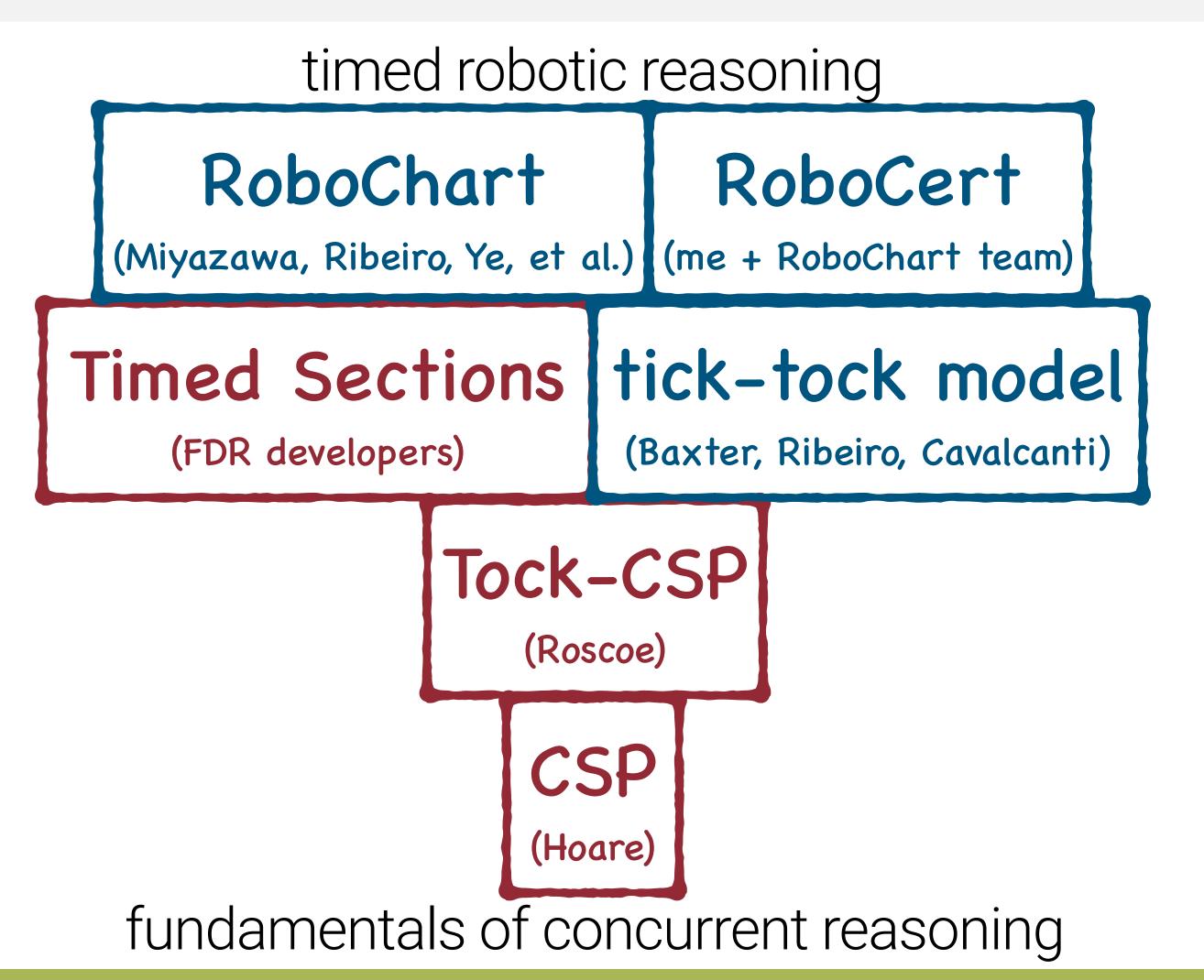
timed robotic reasoning



fundamentals of concurrent reasoning

Concluding...

Building blocks towards reasoning





Concluding...Further info

RoboChart manual

https://robostar.cs.york.ac.uk/publications/techreports/reports/robochart-reference.pdf

RoboCert manual

https://robostar.cs.york.ac.uk/publications/reports/robocert.pdf

Tool

https://robostar.cs.york.ac.uk/robotool/

Eclipse update site

https://robostar.cs.york.ac.uk/robotool/update/

RoboCert extras

https://github.com/UoY-RoboStar/robocert-evaluation

