Master's Thesis Exam Verification of the Blocking and Non-Blocking Michael-Scott Queue Algorithms

Mathias Pedersen, 201808137 Advisor: Amin Timany

Aarhus University

June 2024



- Initial goal was to prove safety of the two M&S Queues
- The project later generalised the results to apply to queues in general

- Initial goal was to prove safety of the two M&S Queues
- The project later generalised the results to apply to queues in general
- In particular, three different specifications for queues were given
 - Sequential specification
 - Useful for sequential clients
 - Concurrent specification
 - Proves safety of concurrent queues
 - Useful for some concurrent clients
 - Doesn't track queue contents
 - HOCAP-style specification
 - Stronger specification, useful for more complex clients
 - Tracks queue contents
 - Demonstrated with a PoC queue client, QueueAdd

- Initial goal was to prove safety of the two M&S Queues
- The project later generalised the results to apply to queues in general
- In particular, three different specifications for queues were given
 - Sequential specification
 - Useful for sequential clients
 - Concurrent specification
 - Proves safety of concurrent gueues
 - Useful for some concurrent clients
 - Doesn't track queue contents
 - HOCAP-style specification
 - Stronger specification, useful for more complex clients
 - Tracks queue contents
 - Demonstrated with a PoC queue client, QueueAdd
- It was demonstrated that the HOCAP-style specification derives the sequential and concurrent specifications

- Initial goal was to prove safety of the two M&S Queues
- The project later generalised the results to apply to queues in general
- In particular, three different specifications for queues were given
 - Sequential specification
 - Useful for sequential clients
 - Concurrent specification
 - Proves safety of concurrent queues
 - Useful for some concurrent clients
 - Doesn't track queue contents
 - HOCAP-style specification
 - Stronger specification, useful for more complex clients
 - Tracks queue contents
 - Demonstrated with a PoC queue client, QueueAdd
- It was demonstrated that the HOCAP-style specification derives the sequential and concurrent specifications
- Implementations of the M&S Queues in HeapLang were proven to meet the three specifications
 - In particular, both version are safe

- Initial goal was to prove safety of the two M&S Queues
- The project later generalised the results to apply to queues in general
- In particular, three different specifications for queues were given
 - Sequential specification
 - Useful for sequential clients
 - Concurrent specification
 - Proves safety of concurrent queues
 - Useful for some concurrent clients
 - Doesn't track queue contents
 - HOCAP-style specification
 - Stronger specification, useful for more complex clients
 - Tracks queue contents
 - Demonstrated with a PoC queue client, QueueAdd
- It was demonstrated that the HOCAP-style specification derives the sequential and concurrent specifications
- Implementations of the M&S Queues in HeapLang were proven to meet the three specifications
 - In particular, both version are safe
- All proofs have been mechanised in the Coq proof assistant

Outline

- Queue Specifications
- 2 The Two-Lock Michael-Scott Queue
- Proving that the Two-Lock Michael-Scott Queue Satisfies the HOCAP-style Specification
- 4 The Lock-Free Michael-Scott Queue
- 5 Proving that the Lock-and-CC-Free Michael-Scott Queue Satisfies the HOCAP-style Specification

Queue Specifications

Specifications for Queues

Assumptions on Queues

- Queues consists of initialize, enqueue, and dequeue
- initialize creates an empty queue: []
- **enqueue** adds a value, v, to the beginning of the queue xs_v : $v :: xs_v$
- dequeue depends on whether queue is empty:
 - If non-empty, xs_v ++ v, remove value v at end of queue and return Some v
 - If empty, [], return None

Specifications for Queues

Assumptions on Queues

- Queues consists of initialize, enqueue, and dequeue
- initialize creates an empty queue: []
- **enqueue** adds a value, v, to the beginning of the queue xs_v : $v :: xs_v$
- dequeue depends on whether queue is empty:
 - If non-empty, xs_v ++ v, remove value v at end of queue and return Some v
 - If empty, [], return None

Nature of Specifications

- Specifications written in Iris, a higher order CSL
- **E**xpressed in terms of *Hoare triples*: $\{P\}$ e $\{v. \Phi v\}$
- Hoare triples prove partial correctness of programs, e
- In particular: safety
- Idea: clients can use Hoare triples to prove results about their own code

HOCAP-style Specification - Abstract State RA

■ We will need a construction to allow clients to track contents of queue

HOCAP-style Specification - Abstract State RA

- We will need a construction to allow clients to track contents of queue
- Idea: have two "views" of the abstract state of the queue

Authoritative view	Fragmental view
$\gamma \mapsto_{ullet} xs_v$	$\gamma \mapsto_{\circ} x s_{v}$
Owned by queue	Owned by client

HOCAP-style Specification - Abstract State RA

- We will need a construction to allow clients to track contents of queue
- Idea: have two "views" of the abstract state of the queue

Authoritative view	Fragmental view
$\gamma \mapsto_{ullet} xs_{v}$	$\gamma \mapsto_{\circ} xs_{v}$
Owned by queue	Owned by client

- Construction ensures:
 - authoritative and fragmental views always agree on abstract state of queue
 - views can only be updated in unison
- Implemented using the resource algebra: $Auth((FRAC \times Ag(List \ Val))^?)$
- The desirables are captured by the following lemmas

Lemmas on the Abstract State RA

$$\vdash \boxminus \exists \gamma. \ \gamma \bowtie_{\bullet} xs_{v} * \gamma \bowtie_{\circ} xs_{v}$$

$$(Abstract State Alloc)$$

$$\gamma \bowtie_{\bullet} xs'_{v} * \gamma \bowtie_{\circ} xs_{v} \vdash xs_{v} = xs'_{v}$$

$$(Abstract State Agree)$$

$$\gamma \bowtie_{\bullet} xs'_{v} * \gamma \bowtie_{\circ} xs_{v} \Rightarrow \gamma \bowtie_{\bullet} xs''_{v} * \gamma \bowtie_{\circ} xs''_{v}$$

$$(Abstract State Update)$$

- Post-condition of initialize specification gives fragmental view to clients
- Hoare triples for enqueue and dequeue are conditioned on view-shifts
- Clients must show that they can supply the fragmental view, so that the abstract (and concrete) state can be updated
- View-shifts and Hoare-triples parametrised by predicates *P* and *Q*
 - Client might have resources that need to be updated as a result of enqueue/dequeue
 - \blacksquare P is the clients resources before enqueue/dequeue and Q the resources after

Definition (HOCAP Specification)

```
\exists isQueue : Val \rightarrow Qgnames \rightarrow Prop.
\forall v_a, G. isQueue(v_a, G) \implies \Box isQueue(v_a, G)
```

- Post-condition of initialize specification gives fragmental view to clients
- Hoare triples for enqueue and dequeue are conditioned on view-shifts
- Clients must show that they can supply the fragmental view, so that the abstract (and concrete) state can be updated
- View-shifts and Hoare-triples parametrised by predicates P and Q
 - Client might have resources that need to be updated as a result of enqueue/dequeue
 - \blacksquare P is the clients resources before enqueue/dequeue and Q the resources after

Definition (HOCAP Specification)

```
\exists isQueue : Val 	o Qgnames 	o Prop.

\forall v_q, G. \text{ isQueue}(v_q, G) \implies \Box \text{ isQueue}(v_q, G)

\land \quad \{\text{True}\} \text{ initialize } () \{v_q. \exists G. \text{ isQueue}(v_q, G) * G. \gamma_{Abst} \Rightarrow_{\circ} []\}
```

- Post-condition of initialize specification gives fragmental view to clients
- Hoare triples for enqueue and dequeue are conditioned on view-shifts
- Clients must show that they can supply the fragmental view, so that the abstract (and concrete) state can be updated
- View-shifts and Hoare-triples parametrised by predicates P and Q
 - Client might have resources that need to be updated as a result of enqueue/dequeue
 - P is the clients resources before enqueue/dequeue and Q the resources after

Definition (HOCAP Specification)

 \exists isQueue : $Val \rightarrow Qgnames \rightarrow Prop.$

```
\begin{array}{ll} \forall v_q, G. \ \mathsf{isQueue}(v_q, G) \implies \Box \ \mathsf{isQueue}(v_q, G) \\ \wedge & \{\mathsf{True}\} \ \mathsf{initialize} \ () \ \{v_q. \exists G. \ \mathsf{isQueue}(v_q, G) * G.\gamma_{\mathsf{Abst}} \Rightarrow_{\mathsf{o}} \ [] \} \\ \wedge & \forall v_q, v, G, P, Q. \ \ \left( \forall x s_v. \ G.\gamma_{\mathsf{Abst}} \Rightarrow_{\bullet} x s_v * P \Rightarrow_{\mathcal{E} \backslash \mathcal{N}, i \uparrow} \triangleright G.\gamma_{\mathsf{Abst}} \Rightarrow_{\bullet} (v :: x s_v) * Q \right) - \\ & \{ \mathsf{isQueue}(v_q, G) * P \} \ \mathsf{enqueue} \ \ v_q \ v \ \{w. Q \} \end{array}
```

- Post-condition of initialize specification gives fragmental view to clients
- Hoare triples for enqueue and dequeue are conditioned on view-shifts
- Clients must show that they can supply the fragmental view, so that the abstract (and concrete) state can be updated
- View-shifts and Hoare-triples parametrised by predicates P and Q
 - Client might have resources that need to be updated as a result of enqueue/dequeue
 - P is the clients resources before enqueue/dequeue and Q the resources after

Definition (HOCAP Specification)

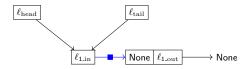
```
\exists \text{ isQueue}: Val \rightarrow Qgnames \rightarrow \text{Prop.} \\ \forall v_q, G. \text{ isQueue}(v_q, G) \implies \Box \text{ isQueue}(v_q, G) \\ \land \quad \{\text{True}\} \text{ initialize} \left(\right) \{v_q, \exists G. \text{ isQueue}(v_q, G) * G.\gamma_{Abst} \mapsto_{\bullet} \left[\right] \} \\ \land \quad \forall v_q, v, G, P, Q. \quad \left(\forall xs_v. G.\gamma_{Abst} \mapsto_{\bullet} xs_v * P \Rightarrow_{\mathcal{E} \backslash \mathcal{N}.i\uparrow} \triangleright G.\gamma_{Abst} \mapsto_{\bullet} \left(v :: xs_v\right) * Q\right) \rightarrow \\ \quad \{\text{isQueue}(v_q, G) * P\} \text{ enqueue} \ v_q \ v \ \{w.Q\} \} \\ \land \quad \forall v_q, G, P, Q. \quad \left(\forall xs_v. G.\gamma_{Abst} \mapsto_{\bullet} xs_v * P \Rightarrow_{\mathcal{E} \backslash \mathcal{N}.i\uparrow} \triangleright \left(\begin{array}{c} (xs_v = [] * G.\gamma_{Abst} \mapsto_{\bullet} xs_v * Q(\text{None})) \\ \lor \left(\begin{array}{c} \exists v, xs_v'. xs_v = xs_v' + [v] * \\ G.\gamma_{Abst} \mapsto_{\bullet} xs_v' * Q(\text{Some} v) \end{array}\right) \right) \rightarrow \\ \downarrow \text{isQueue}(v, G) * P \text{ dequeue} \ v \ \{w.Q(w)\} \}
```

The Two-Lock Michael-Scott Queue

Implementation: initialize

■ The queue data structure is a linked list

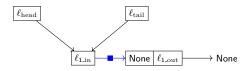
```
initialize \triangleq
let node = ref(None, ref(None)) in
let H\_lock = newLock() in
let T\_lock = newLock() in
ref((ref(node), ref(node)), (H\_lock, T\_lock))
```



Implementation: initialize

- The queue data structure is a linked list
- A node x in the linked list is a triple, $x = (\ell_{\rm in}, w, \ell_{\rm out})$, with $\ell_{\rm in} \mapsto (w, \ell_{\rm out})$
- We use the following notation for nodes

$$\mathsf{in}(x) = \ell_{\mathrm{in}}$$
 $\mathsf{val}(x) = w$ $\mathsf{out}(x) = \ell_{\mathrm{out}}$



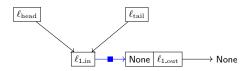
Implementation: initialize

- The queue data structure is a linked list
- A node x in the linked list is a triple, $x = (\ell_{\rm in}, w, \ell_{\rm out})$, with $\ell_{\rm in} \mapsto (w, \ell_{\rm out})$
- We use the following notation for nodes

$$\mathsf{in}(x) = \ell_{\mathrm{in}}$$
 $\mathsf{val}(x) = w$ $\mathsf{out}(x) = \ell_{\mathrm{out}}$

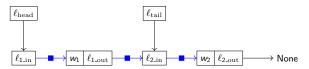
- The initialize function first creates an initial head node, x_{head}
- Then, a lock protecting the head pointer, and a lock protecting the tail pointer
- Finally, it creates the head and tail pointers, $\ell_{\rm head}$ and $\ell_{\rm tail}$, both pointing to $x_{\rm head}$

```
initialize \triangleq
let node = ref(None, ref(None)) in
let H\_lock = newLock() in
let T\_lock = newLock() in
ref((ref(node), ref(node)), (H\_lock, T\_lock))
```



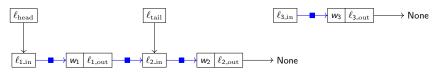
- The enqueue function consists of the following steps
 - I Create a new node, x_{new} , containing value to be enqueued
 - 2 Acquire the tail lock
 - 3 Add x_{new} to linked list
 - 4 Swing tail pointer to x_{new}
 - 5 Release the tail lock

```
enqueue Q value \triangleq
let node = ref (Some value, ref (None)) in
acquire(snd(snd(! Q)));
snd(!(!(snd(fst(! Q))))) \leftarrow node;
snd(fst(! Q)) \leftarrow node;
release(snd(snd(! Q)))
```



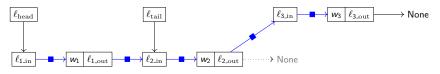
- The enqueue function consists of the following steps
 - I Create a new node, x_{new} , containing value to be enqueued
 - 2 Acquire the tail lock
 - 3 Add x_{new} to linked list
 - 4 Swing tail pointer to x_{new}
 - 5 Release the tail lock

```
enqueue Q value \triangleq
let node = ref (Some value, ref (None)) in
acquire(snd(snd(! Q)));
snd(!(!(snd(fst(! Q)))) \leftarrow node;
snd(fst(! Q)) \leftarrow node;
release(snd(snd(! Q)))
```



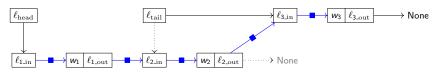
- The enqueue function consists of the following steps
 - I Create a new node, x_{new} , containing value to be enqueued
 - 2 Acquire the tail lock
 - 3 Add x_{new} to linked list
 - 4 Swing tail pointer to x_{new}
 - 5 Release the tail lock

```
enqueue Q value \triangleq
let node = ref (Some value, ref (None)) in
acquire(snd(snd(! Q)));
snd(!(!(snd(fst(! Q)))) \leftarrow node;
snd(fst(! Q)) \leftarrow node;
release(snd(snd(! Q)))
```



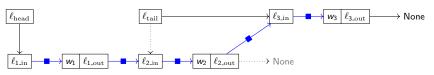
- The enqueue function consists of the following steps
 - 1 Create a new node, x_{new} , containing value to be enqueued
 - 2 Acquire the tail lock
 - 3 Add x_{new} to linked list
 - 4 Swing tail pointer to x_{new}
 - Release the tail lock

```
enqueue Q value \triangleq  let node = ref (Some value, ref (None)) in acquire(snd(snd(!Q))); snd(!(!(snd(fst(!Q)))) \leftarrow node; snd(fst(!Q)) \leftarrow node; release(snd(snd(!Q)))
```



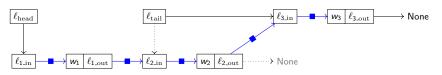
- The enqueue function consists of the following steps
 - 1 Create a new node, x_{new} , containing value to be enqueued
 - 2 Acquire the tail lock
 - 3 Add x_{new} to linked list
 - 4 Swing tail pointer to x_{new}
 - 5 Release the tail lock
- Once a node is enqueued, its position in the linked list is fixed

```
enqueue Q value \triangleq let node = ref (Some value, ref (None)) in acquire(snd(snd(! Q))); snd(!(!(snd(fst(! Q)))) \leftarrow node; snd(fst(! Q)) \leftarrow node; release(snd(snd(! Q)))
```



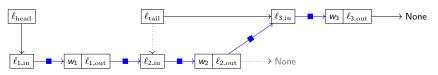
- The enqueue function consists of the following steps
 - 1 Create a new node, x_{new} , containing value to be enqueued
 - 2 Acquire the tail lock
 - 3 Add x_{new} to linked list
 - 4 Swing tail pointer to x_{new}
 - 5 Release the tail lock
- Once a node is enqueued, its position in the linked list is fixed
- Adding and swinging not atomic → Tail node is either last or second last

```
enqueue Q value \triangleq let node = ref (Some value, ref (None)) in acquire(snd(snd(!Q))); snd(!(!(snd(fst(!Q))))) \leftarrow node; snd(fst(!Q)) \leftarrow node; release(snd(snd(!Q)))
```



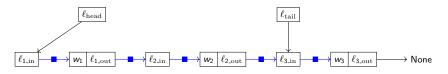
- The enqueue function consists of the following steps
 - \blacksquare Create a new node, x_{new} , containing value to be enqueued
 - 2 Acquire the tail lock
 - 3 Add x_{new} to linked list
 - 4 Swing tail pointer to x_{new}
 - 5 Release the tail lock
- Once a node is enqueued, its position in the linked list is fixed
- Adding and swinging not atomic → Tail node is either last or second last
- dequeue ignores tail pointer → Tail node can lag behind head node

```
enqueue Q value \triangleq
let node = ref (Some value, ref (None)) in
acquire(snd(snd(! Q)));
snd(!(!(snd(fst(! Q)))) \leftarrow node;
snd(fst(! Q)) \leftarrow node;
release(snd(snd(! Q)))
```



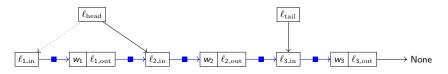
- The dequeue function checks if queue is empty
 - If empty, return None
 - Else, swing head pointer to new head node, and return its value

```
dequeue Q \triangleq
acquire(fst(snd(! Q)));
let node = !(fst(fst(! Q))) in
let new\_head = !(snd(! node)) in
if new\_head = None then
release(fst(snd(! Q)));
None
else
let value = fst(! new\_head) in
fst(fst(! Q)) \leftarrow new\_head;
release(fst(snd(! Q)));
value
```



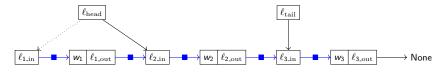
- The dequeue function checks if queue is empty
 - If empty, return None
 - Else, swing head pointer to new head node, and return its value

```
dequeue Q \triangleq
acquire(fst(snd(! Q)));
let node = !(fst(fst(! Q))) in
let new_head = !(snd(! node)) in
if new_head = None then
release(fst(snd(! Q)));
None
else
let value = fst(! new_head) in
fst(fst(! Q)) \leftarrow new_head;
release(fst(snd(! Q)));
value
```



- The dequeue function checks if queue is empty
 - If empty, return None
 - Else, swing head pointer to new head node, and return its value
- Dequeued node not freed → Linked list only grows

```
dequeue Q \triangleq
acquire(fst(snd(! Q)));
let node = !(fst(fst(! Q))) in
let new_head = !(snd(! node)) in
if new_head = None then
release(fst(snd(! Q)));
None
else
let value = fst(! new_head) in
fst(fst(! Q)) \leftarrow new_head;
release(fst(snd(! Q)));
value
```



Proving that the Two-Lock Michael-Scott Queue Satisfies the HOCAP-style Specification

The isLL Predicate

- Idea: express the structure of the linked list in terms of points-to predicates
- Also captures persistent and non-persistent parts of the linked list

Definition (Linked List Predicate)

$$\begin{split} \text{isLL_chain}([]) \triangleq & \text{True} \\ \text{isLL_chain}([x]) \triangleq & \text{in}(x) \mapsto^{\square} (\text{val}(x), \text{out}(x)) \\ \text{isLL_chain}(x :: x' :: xs) \triangleq & \text{in}(x) \mapsto^{\square} (\text{val}(x), \text{out}(x)) * \text{out}(x') \mapsto^{\square} & \text{in}(x) * & \text{isLL_chain}(x' :: xs) \\ \end{split}$$

$$isLL(x :: xs) \triangleq out(x) \mapsto None * isLL_chain(x :: xs)$$

Example

Consider the list:
$$xs = [(\ell_{3.\text{in}}, w_3, \ell_{3.\text{out}}); (\ell_{2.\text{in}}, w_2, \ell_{2.\text{out}}); (\ell_{1.\text{in}}, w_1, \ell_{1.\text{out}})].$$

$$isLL(xs) = \ell_{3.\text{out}} \mapsto \text{None} * \ell_{3.\text{in}} \mapsto^{\square} (w_3, \ell_{3.\text{out}}) * \ell_{2.\text{out}} \mapsto^{\square} \ell_{3.\text{in}} * \ell_{2.\text{in}} \mapsto^{\square} (w_2, \ell_{2.\text{out}}) * \ell_{1.\text{out}} \mapsto^{\square} \ell_{2.\text{in}} * \ell_{1.\text{in}} \mapsto^{\square} (w_1, \ell_{1.\text{out}})$$

$$\ell_{1.\text{in}} \mapsto^{\square} (w_1, \ell_{1.\text{out}}) \mapsto^{\square} \ell_{2.\text{in}} \mapsto^{\square} \ell_{2.\text{out}} \mapsto^{\square} \ell_{3.\text{in}} \mapsto^{\square} \text{None}$$

Invariant

- Queue predicate must be persistent (according to specification)
- Problem: the queue relies on non-persistent resources (e.g. $\ell_{\text{head}} \mapsto \text{in}(x_{\text{head}})$)
- Solution: identify an *invariant* (persistent), describing the resources

Invariant

- Queue predicate must be persistent (according to specification)
- Problem: the queue relies on non-persistent resources (e.g. $\ell_{\text{head}} \mapsto \text{in}(x_{\text{head}})$)
- Solution: identify an *invariant* (persistent), describing the resources
- Contains abstract state of queue existentially quantified as it can change

Definition (Two-Lock M&S Queue HOCAP Invariant)

$$I_{\text{TLH}}(\ell_{\text{head}}, \ell_{\text{tail}}, G) \triangleq \exists x s_v. G. \gamma_{A \text{hst.}} \Rightarrow_{\bullet} x s_v *$$
 (abstract state)

Invariant

- Queue predicate must be persistent (according to specification)
- Problem: the queue relies on non-persistent resources (e.g. $\ell_{\text{head}} \mapsto \text{in}(x_{\text{head}})$)
- Solution: identify an *invariant* (persistent), describing the resources
- Contains abstract state of queue existentially quantified as it can change
- Defines structure of the concrete linked list, xsc

Definition (Two-Lock M&S Queue HOCAP Invariant)

```
\begin{split} I_{\mathsf{TLH}}(\ell_{\mathrm{head}},\ell_{\mathrm{tail}},\mathsf{G}) &\triangleq \exists x s_{\mathsf{v}}.\ \mathsf{G}.\gamma_{\mathrm{Abst}} \mapsto_{\bullet} x s_{\mathsf{v}} * \\ &\exists x s, x s_{\mathrm{queue}}, x s_{\mathrm{old}}, x_{\mathrm{head}}, x_{\mathrm{tail}}. \\ &x s = x s_{\mathrm{queue}} + + [x_{\mathrm{head}}] + + x s_{\mathrm{old}} * \\ &\text{isLL}(x s) * \end{split} \tag{concrete state}
```

Invariant

- Queue predicate must be persistent (according to specification)
- Problem: the queue relies on non-persistent resources (e.g. $\ell_{\text{head}} \mapsto \text{in}(x_{\text{head}})$)
- Solution: identify an invariant (persistent), describing the resources
- Contains abstract state of queue existentially quantified as it can change
- Defines structure of the concrete linked list, xsc
- Asserts relation between abstract state and concrete state

Definition (Two-Lock M&S Queue HOCAP Invariant)

```
\begin{split} \mathsf{I}_{\mathsf{TLH}}(\ell_{\mathrm{head}},\ell_{\mathrm{tail}},\mathcal{G}) &\triangleq \exists x s_v. \ \mathcal{G}.\gamma_{\mathrm{Abst}} \mapsto_{\bullet} x s_v * \\ &\exists x s, x s_{\mathrm{queue}}, x s_{\mathrm{old}}, x_{\mathrm{head}}, x_{\mathrm{tail}}. \end{aligned} \qquad \text{(concrete state)} \\ &x s = x s_{\mathrm{queue}} + + [x_{\mathrm{head}}] + + x s_{\mathrm{old}} * \\ &\mathrm{isLL}(x s) * \\ &\mathrm{projVal}(x s_{\mathrm{queue}}) = \mathrm{wrapSome}(x s_v) * \end{split}
```

Invariant

- Queue predicate must be persistent (according to specification)
- Problem: the queue relies on non-persistent resources (e.g. $\ell_{\text{head}} \mapsto \text{in}(x_{\text{head}})$)
- Solution: identify an *invariant* (persistent), describing the resources
- Contains abstract state of queue existentially quantified as it can change
- Defines structure of the concrete linked list, xsc
- Asserts relation between abstract state and concrete state
- Identifies possible queue states: Static, Enqueue, Dequeue, and Both
 - Two locks → Four queue states
 - Invariant describes the queue resources in each state
 - See next slide

Definition (Two-Lock M&S Queue HOCAP Invariant)

```
\begin{split} I_{\mathsf{TLH}}(\ell_{\mathrm{head}},\ell_{\mathrm{tail}},G) &\triangleq \exists x s_{\mathsf{v}}.\ G.\gamma_{\mathrm{Abst}} \Rightarrow_{\bullet} x s_{\mathsf{v}} * & \text{(abstract state)} \\ &\exists x s, x s_{\mathrm{queue}}, x s_{\mathrm{old}}, x_{\mathrm{head}}, x_{\mathrm{tail}}. & \text{(concrete state)} \\ &x s = x s_{\mathrm{queue}} +_{+} [x_{\mathrm{head}}] +_{+} x s_{\mathrm{old}} * \\ &\text{isLL}(x s) * & \\ &\text{projVal}(x s_{\mathrm{queue}}) = \mathsf{wrapSome}(x s_{\mathsf{v}}) * \end{split}
```

Invariant (Queue States)

- Idea: the enqueueing thread keeps half of tail pointer between invariant openings
- Guarantees that the pointer is not updated (full pointer needed for update)
- Similarly for the dequeueing thread
- Enqueue and Both also captures "gap" between adding x_{new} and swinging ℓ_{tail}
- Tokens used to reason about which state queue is in

Definition (Two-Lock M&S Queue HOCAP Invariant – continued)

- HOCAP-style specification requires the existence of a persistent queue predicate
- We define it in terms of our invariant

- HOCAP-style specification requires the existence of a persistent queue predicate
- We define it in terms of our invariant

Definition (Two-Lock M&S Queue - isQueue Predicate)

$$\begin{split} \mathsf{isQueue}(v_q,G) \triangleq & \exists \ell_{\mathrm{queue}}, \ell_{\mathrm{head}}, \ell_{\mathrm{tail}} \in \mathit{Loc}. \ \exists h_{\mathrm{lock}}, t_{\mathrm{lock}} \in \mathit{Val}. \\ & v_q = \ell_{\mathrm{queue}} * \ell_{\mathrm{queue}} \mapsto^{\square} \big(\big(\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}\big), \big(h_{\mathrm{lock}}, t_{\mathrm{lock}}\big) \big) * \\ & \boxed{|_{\mathsf{TLH}}(\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}, G)|}^{\mathcal{N}.\mathit{queue}} * \\ & \mathsf{isLock}(G.\gamma_{\mathrm{Hlock}}, h_{\mathrm{lock}}, \mathsf{TokD} \ G) * \\ & \mathsf{isLock}(G.\gamma_{\mathrm{Tlock}}, t_{\mathrm{lock}}, \mathsf{TokE} \ G) \end{split}$$

- HOCAP-style specification requires the existence of a persistent queue predicate
- We define it in terms of our invariant

Definition (Two-Lock M&S Queue - isQueue Predicate)

$$\begin{split} \mathsf{isQueue}(v_q,G) \triangleq & \exists \ell_{\mathrm{queue}}, \ell_{\mathrm{head}}, \ell_{\mathrm{tail}} \in \mathit{Loc}. \ \exists h_{\mathrm{lock}}, t_{\mathrm{lock}} \in \mathit{Val}. \\ & v_q = \ell_{\mathrm{queue}} * \ell_{\mathrm{queue}} \mapsto^{\square} \big((\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}), (h_{\mathrm{lock}}, t_{\mathrm{lock}}) \big) * \\ & \boxed{\mathsf{ITLH}(\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}, G)}^{\mathcal{N}.\mathit{queue}} * \\ & \mathsf{isLock}(G.\gamma_{\mathrm{Hlock}}, h_{\mathrm{lock}}, \mathsf{TokD} \ G) * \\ & \mathsf{isLock}(G.\gamma_{\mathrm{Tlock}}, t_{\mathrm{lock}}, \mathsf{TokE} \ G) \end{split}$$

■ The queue predicate is persistent, as all its constituents are

- HOCAP-style specification requires the existence of a persistent queue predicate
- We define it in terms of our invariant

Definition (Two-Lock M&S Queue - isQueue Predicate)

$$\begin{split} \mathsf{isQueue}(v_q,G) \triangleq & \exists \ell_{\mathrm{queue}}, \ell_{\mathrm{head}}, \ell_{\mathrm{tail}} \in \mathit{Loc}. \ \exists h_{\mathrm{lock}}, t_{\mathrm{lock}} \in \mathit{Val}. \\ & v_q = \ell_{\mathrm{queue}} * \ell_{\mathrm{queue}} \mapsto^{\square} ((\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}), (h_{\mathrm{lock}}, t_{\mathrm{lock}})) * \\ & \boxed{\mathsf{IT_{LH}}(\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}, G)}^{\mathcal{N}.\mathit{queue}} * \\ & \mathsf{isLock}(G.\gamma_{\mathrm{Hlock}}, h_{\mathrm{lock}}, \mathsf{TokD} \ G) * \\ & \mathsf{isLock}(G.\gamma_{\mathrm{Tlock}}, t_{\mathrm{lock}}, \mathsf{TokE} \ G) \end{split}$$

- The queue predicate is persistent, as all its constituents are
- Proving that Two-Lock M&S Queue satisfies the HOCAP-style specification then consists of proving the Hoare triples for initialize, enqueue, and dequeue
- We here focus on enqueue

$$\forall v_q, v, G, P, Q. \quad (\forall xs_v. \ G.\gamma_{\mathrm{Abst}} \mapsto_{\bullet} xs_v * P \Rightarrow_{\mathcal{E} \setminus \mathcal{N}.i\uparrow} \triangleright G.\gamma_{\mathrm{Abst}} \mapsto_{\bullet} (v :: xs_v) * Q) \twoheadrightarrow \{ \mathrm{isQueue}(v_q, G) * P \} \text{ enqueue } v_q \ v \ \{w.Q\}$$

```
\forall v_q, v, G, P, Q. \quad (\forall xs_v. \ G.\gamma_{Abst} \Rightarrow_{\bullet} xs_v * P \Rightarrow_{\mathcal{E} \setminus \mathcal{N}.i\uparrow} \triangleright G.\gamma_{Abst} \Rightarrow_{\bullet} (v :: xs_v) * Q) \twoheadrightarrow
                                      \{isQueue(v_a, G) * P\} enqueue v_a v \{w.Q\}
```

- $\begin{array}{l} \bullet \ \ v_q = \ell_{\mathrm{queue}} * \ell_{\mathrm{queue}} \mapsto^{\Box} ((\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}), (\mathit{h}_{\mathrm{lock}}, \mathit{t}_{\mathrm{lock}})) \\ \bullet \ \ [\mathsf{I}_{\mathsf{TLH}}(\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}, \mathcal{G})]^{\mathcal{N}.\mathit{queue}} \end{array}$
- isLock($G.\gamma_{Tlock}$, t_{lock} , TokE G)

```
\forall v_q, v, G, P, Q. \ (\forall xs_v. G. \gamma_{Abst} \Rightarrow_{\bullet} xs_v * P \Rightarrow_{\mathcal{E} \setminus \mathcal{N}, j\uparrow} \triangleright G. \gamma_{Abst} \Rightarrow_{\bullet} (v :: xs_v) * Q) \twoheadrightarrow
                                    {isQueue(v_a, G) * P} enqueue v_a v {w.Q}
(Proof)
```

- $\begin{array}{l} \bullet \ \ v_q = \ell_{\mathrm{queue}} * \ell_{\mathrm{queue}} \mapsto^{\Box} ((\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}), (\mathit{h}_{\mathrm{lock}}, \mathit{t}_{\mathrm{lock}})) \\ \bullet \ \ [\mathsf{I}_{\mathsf{TLH}}(\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}, \mathcal{G})]^{\mathcal{N}.\mathit{queue}} \end{array}$
- isLock($G.\gamma_{Tlock}$, t_{lock} , TokE G)

```
{P}
  let node = ref (Some v, ref (None)) in
  acquire(snd(snd(! v_a)));
  e_t = !(\operatorname{snd}(\operatorname{fst}(! v_a)))
  snd(!(e_t)) \leftarrow node;
  snd(fst(! v_a)) \leftarrow node;
  release(snd(snd(! v_a)))
{Q}
```

```
\forall v_q, v, G, P, Q. \ (\forall xs_v. G. \gamma_{Abst} \Rightarrow_{\bullet} xs_v * P \Rightarrow_{\mathcal{E} \setminus \mathcal{N}, j\uparrow} \triangleright G. \gamma_{Abst} \Rightarrow_{\bullet} (v :: xs_v) * Q) \twoheadrightarrow
                                    {isQueue(v_a, G) * P} enqueue v_a v {w.Q}
(Proof)
```

- $\begin{array}{l} \bullet \ \ v_q = \ell_{\mathrm{queue}} * \ell_{\mathrm{queue}} \mapsto^{\square} ((\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}), (\mathit{h}_{\mathrm{lock}}, \mathit{t}_{\mathrm{lock}})) \\ \bullet \ \ \left[\mathsf{I}_{\mathsf{TLH}}(\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}, \mathit{G})\right]^{\mathcal{N}.\mathit{queue}} \end{array}$
- isLock($G.\gamma_{Tlock}$, t_{lock} , TokE G)

```
{P}
  let node = ref(Some v, ref(None)) in (create node x_{new})
\{P * \operatorname{out}(x_{\text{new}}) \mapsto \operatorname{None}\}
  acquire(snd(snd(! v_a)));
  e_t = !(\operatorname{snd}(\operatorname{fst}(! v_a)))
  snd(!(e_t)) \leftarrow node;
  snd(fst(! v_a)) \leftarrow node;
  release(snd(snd(! v_a)))
{Q}
```

```
\forall v_q, v, G, P, Q. \quad (\forall xs_v. \ G.\gamma_{Abst} \Rightarrow_{\bullet} xs_v * P \Rightarrow_{\mathcal{E} \setminus \mathcal{N}.i\uparrow} \triangleright G.\gamma_{Abst} \Rightarrow_{\bullet} (v :: xs_v) * Q) \twoheadrightarrow
                                     {isQueue(v_a, G) * P} enqueue v_a v {w.Q}
(Proof)
```

- $\begin{array}{l} \bullet \ \ v_q = \ell_{\mathrm{queue}} * \ell_{\mathrm{queue}} \mapsto^{\square} ((\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}), (\mathit{h}_{\mathrm{lock}}, \mathit{t}_{\mathrm{lock}})) \\ \bullet \ \ \left[\mathsf{I}_{\mathsf{TLH}}(\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}, \mathit{G})\right]^{\mathcal{N}.\mathit{queue}} \end{array}$
- isLock($G.\gamma_{Tlock}$, t_{lock} , TokE G)

```
{P}
   let node = ref(Some v, ref(None)) in (create node x_{new})
\{P * \operatorname{out}(x_{\text{new}}) \mapsto \operatorname{None}\}
   acquire(snd(snd(!v_a))); (acquire tail lock)
\{P * \operatorname{out}(x_{\text{new}}) \mapsto \operatorname{None} * \operatorname{TokE} G\}
   e_t = !(\operatorname{snd}(\operatorname{fst}(! v_a)))
   snd(!(e_t)) \leftarrow node;
   snd(fst(! v_a)) \leftarrow node;
   release(snd(snd(! v_a)))
{Q}
```

```
\forall v_q, v, G, P, Q. \ (\forall xs_v. G. \gamma_{Abst} \Rightarrow_{\bullet} xs_v * P \Rightarrow_{\mathcal{E} \setminus \mathcal{N}, j\uparrow} \triangleright G. \gamma_{Abst} \Rightarrow_{\bullet} (v :: xs_v) * Q) \twoheadrightarrow
                                    {isQueue(v_a, G) * P} enqueue v_a v {w.Q}
(Proof)
```

- $\begin{array}{l} \bullet \ \ v_q = \ell_{\mathrm{queue}} * \ell_{\mathrm{queue}} \mapsto^{\square} ((\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}), (\mathit{h}_{\mathrm{lock}}, \mathit{t}_{\mathrm{lock}})) \\ \bullet \ \ \left[\mathsf{I}_{\mathsf{TLH}}(\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}, \mathit{G})\right]^{\mathcal{N}.\mathit{queue}} \end{array}$
- isLock($G.\gamma_{Tlock}$, t_{lock} , TokE G)

```
{P}
   let node = ref(Some v, ref(None)) in (create node x_{new})
\{P * \operatorname{out}(x_{\text{new}}) \mapsto \operatorname{None}\}
   acquire(snd(snd(!v_a))); (acquire tail lock)
\{P * \operatorname{out}(x_{\text{new}}) \mapsto \operatorname{None} * \operatorname{TokE} G\}
   e_t = !(snd(fst(!v_q))) (find current tail, x_{tail}. I_{TLH}: Static/Dequeue \rightarrow Enqueue/Both (before))
\{P * \operatorname{out}(x_{\text{new}}) \mapsto \operatorname{None} * \ell_{\text{tail}} \mapsto \frac{1}{2} \operatorname{in}(x_{\text{tail}}) * \operatorname{TokNE} G * \operatorname{TokAfter} G\}
   snd(!(e_t)) \leftarrow node;
   snd(fst(! v_a)) \leftarrow node;
   release(snd(snd(! v_a)))
{Q}
```

```
\forall v_q, v, G, P, Q. \quad (\forall x s_v. \ G. \gamma_{Abst} \Rightarrow_{\bullet} x s_v * P \Rightarrow_{\mathcal{E} \setminus \mathcal{N}.i \uparrow} \triangleright G. \gamma_{Abst} \Rightarrow_{\bullet} (v :: x s_v) * Q) \twoheadrightarrow
                                     {isQueue(v_a, G) * P} enqueue v_a v {w.Q}
(Proof)
```

- $\begin{array}{l} \bullet \ \ v_q = \ell_{\mathrm{queue}} * \ell_{\mathrm{queue}} \mapsto^{\square} ((\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}), (\mathit{h}_{\mathrm{lock}}, \mathit{t}_{\mathrm{lock}})) \\ \bullet \ \ \left[\mathsf{I}_{\mathsf{TLH}}(\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}, \mathit{G})\right]^{\mathcal{N}.\mathit{queue}} \end{array}$
- isLock($G.\gamma_{Tlock}$, t_{lock} , TokE G)

```
{P}
   let node = ref(Some v, ref(None)) in (create node x_{new})
\{P * \operatorname{out}(x_{\text{new}}) \mapsto \operatorname{None}\}
   acquire(snd(snd(!v_a))); (acquire tail lock)
\{P * \operatorname{out}(x_{\text{new}}) \mapsto \operatorname{None} * \operatorname{TokE} G\}
    e_t = !(snd(fst(!v_q))) (find current tail, x_{tail}. I_{TLH}: Static/Dequeue \rightarrow Enqueue/Both (before))
\{P * \operatorname{out}(x_{\text{new}}) \mapsto \operatorname{None} * \ell_{\text{tail}} \mapsto \frac{1}{2} \operatorname{in}(x_{\text{tail}}) * \operatorname{TokNE} G * \operatorname{TokAfter} G\}
   snd(!(e_t)) \leftarrow \textit{node}; \pmod{x_{tail}} \text{ point to } x_{new}. \text{ } I_{TLH}: \text{ Enqueue/Both (before)} \rightarrow \text{ Enqueue/Both (after))}
\{Q * \ell_{tail} \mapsto \frac{1}{2} \operatorname{in}(x_{tail}) * \operatorname{TokNE} G * \operatorname{TokBefore} G\}
   snd(fst(! v_a)) \leftarrow node;
   release(snd(snd(! v_a)))
{Q}
```

```
\forall v_q, v, G, P, Q. \quad (\forall x s_v. \ G. \gamma_{Abst} \Rightarrow_{\bullet} x s_v * P \Rightarrow_{\mathcal{E} \setminus \mathcal{N}.i \uparrow} \triangleright G. \gamma_{Abst} \Rightarrow_{\bullet} (v :: x s_v) * Q) \twoheadrightarrow
                                     {isQueue(v_a, G) * P} enqueue v_a v {w.Q}
(Proof)
```

- $\begin{array}{l} \bullet \ \ v_q = \ell_{\mathrm{queue}} * \ell_{\mathrm{queue}} \mapsto^{\square} ((\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}), (\mathit{h}_{\mathrm{lock}}, \mathit{t}_{\mathrm{lock}})) \\ \bullet \ \ \left[\mathsf{I}_{\mathsf{TLH}}(\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}, \mathit{G})\right]^{\mathcal{N}.\mathit{queue}} \end{array}$
- isLock($G.\gamma_{Tlock}$, t_{lock} , TokE G)

```
{P}
   let node = ref(Some v, ref(None)) in (create node x_{new})
\{P * \operatorname{out}(x_{\text{new}}) \mapsto \operatorname{None}\}
   acquire(snd(snd(!v_a))); (acquire tail lock)
\{P * \operatorname{out}(x_{\text{new}}) \mapsto \operatorname{None} * \operatorname{TokE} G\}
   e_t = !(snd(fst(!v_q))) (find current tail, x_{tail}. I_{TLH}: Static/Dequeue \rightarrow Enqueue/Both (before))
\{P * \operatorname{out}(x_{\text{new}}) \mapsto \operatorname{None} * \ell_{\text{tail}} \mapsto \frac{1}{2} \operatorname{in}(x_{\text{tail}}) * \operatorname{TokNE} G * \operatorname{TokAfter} G\}
   snd(!(e_t)) \leftarrow \textit{node}; \pmod{x_{tail}} \text{ point to } x_{new}. \text{ } I_{TLH}: \text{ Enqueue/Both (before)} \rightarrow \text{ Enqueue/Both (after))}
\{Q * \ell_{tail} \mapsto \overset{1}{2} in(x_{tail}) * TokNE G * TokBefore G\}
   snd(fst(!v_a)) \leftarrow node; (swing tail pointer to x_{new}. I_{TLH}: Enqueue/Both (after) \rightarrow Static/Dequeue)
\{Q * TokE G\}
   release(snd(snd(! v_a)))
\{Q\}
```

```
\forall v_q, v, G, P, Q. \quad (\forall x s_v. \ G. \gamma_{Abst} \Rightarrow_{\bullet} x s_v * P \Rightarrow_{\mathcal{E} \setminus \mathcal{N}.i \uparrow} \triangleright G. \gamma_{Abst} \Rightarrow_{\bullet} (v :: x s_v) * Q) \twoheadrightarrow
                                     {isQueue(v_a, G) * P} enqueue v_a v {w.Q}
(Proof)
```

- $\begin{array}{l} \bullet \ \ v_q = \ell_{\mathrm{queue}} * \ell_{\mathrm{queue}} \mapsto^{\square} ((\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}), (\mathit{h}_{\mathrm{lock}}, \mathit{t}_{\mathrm{lock}})) \\ \bullet \ \ \left[\mathsf{I}_{\mathsf{TLH}}(\ell_{\mathrm{head}}, \ell_{\mathrm{tail}}, \mathit{G})\right]^{\mathcal{N}.\mathit{queue}} \end{array}$
- isLock($G.\gamma_{Tlock}$, t_{lock} , TokE G)

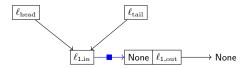
```
{P}
  let node = ref(Some v, ref(None)) in (create node x_{new})
\{P * \operatorname{out}(x_{\text{new}}) \mapsto \operatorname{None}\}
  acquire(snd(snd(!v_a))); (acquire tail lock)
\{P * \operatorname{out}(x_{\text{new}}) \mapsto \operatorname{None} * \operatorname{TokE} G\}
   e_t = !(snd(fst(!v_q))) (find current tail, x_{tail}. I_{TLH}: Static/Dequeue \rightarrow Enqueue/Both (before))
\{P * \operatorname{out}(x_{\text{new}}) \mapsto \operatorname{None} * \ell_{\text{tail}} \mapsto \frac{1}{2} \operatorname{in}(x_{\text{tail}}) * \operatorname{TokNE} G * \operatorname{TokAfter} G\}
  snd(!(e_t)) \leftarrow \textit{node}; \pmod{x_{tail}} \text{ point to } x_{new}. \text{ } I_{TLH}: \text{ Enqueue/Both (before)} \rightarrow \text{ Enqueue/Both (after))}
\{Q * \ell_{tail} \mapsto \overset{1}{2} in(x_{tail}) * TokNE G * TokBefore G\}
  snd(fst(!v_q)) \leftarrow node; (swing tail pointer to x_{new}. I_{TLH}: Enqueue/Both (after) \rightarrow Static/Dequeue)
\{Q * TokE G\}
  release(snd(snd(!v_a))) (release tail lock)
{ Q }
```

The Lock-Free Michael-Scott Queue

Implementation: initialize

- Queue data structure is still a linked list
- The lock-free versions of initialize, enqueue, and dequeue perform the same manipulations of the linked list as two-lock versions
- Difference is how the manipulations take place now with CAS instructions
- No longer need locks

```
initialize \triangleq
let node = ref(None, ref(None)) in
ref(ref(node), ref(node))
```

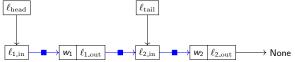


- Appending x_{new} to linked list is now done with CAS
- Ensures that no other thread has performed an enqueue during own enqueue
 - Otherwise, we might "overwrite" another threads enqueued node
- Swinging tail to x_{new} might fail another thread has helped us

```
enqueue Q \ value \triangleq

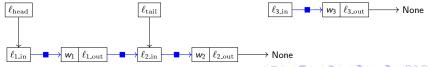
let node = ref (Some \ value, ref (None)) in 
(rec loop_-=

let tail = !(snd(!\ Q)) in 
let next = !(snd(!\ tail)) in 
if tail = !(snd(!\ Q)) then 
if next = None then 
if CAS (snd(!\ tail)) next node then 
CAS (snd(!\ tail)) tail node 
else loop () 
else CAS (snd(!\ Q)) tail next; loop () 
else loop ()
```



- Appending x_{new} to linked list is now done with CAS
- Ensures that no other thread has performed an enqueue during own enqueue
 - Otherwise, we might "overwrite" another threads enqueued node
- Swinging tail to x_{new} might fail another thread has helped us

```
enqueue Q value \triangleq
let node = ref (Some value, ref (None)) in
(rec loop_=
let tail = !(snd(! Q)) in
let next = !(snd(! tail)) in
if tail = !(snd(! Q)) then
if next = None then
if CAS (snd(! tail)) next node then
CAS (snd(! Q)) tail node
else loop ()
else CAS (snd(! Q)) tail next; loop ()
else loop ()
) ()
```



- Appending x_{new} to linked list is now done with CAS
- Ensures that no other thread has performed an enqueue during own enqueue
 - Otherwise, we might "overwrite" another threads enqueued node
- Swinging tail to x_{new} might fail another thread has helped us

```
enqueue Q value \triangleq

let node = ref(Some \, value, \, ref(None)) in

(rec loop_- =

let tail = !(snd(! \, Q)) in

let next = !(snd(! \, tail)) in

if tail = !(snd(! \, tail)) in

if tail = !(snd(! \, tail)) next node then

if CAS (snd(! \, tail)) next \, node then

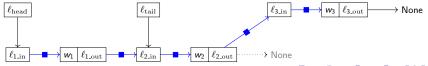
CAS (snd(! \, Q)) tail \, node

else loop ()

else CAS (snd(! \, Q)) tail \, next; loop ()

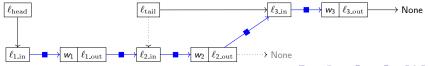
else loop ()

) ()
```



- Appending x_{new} to linked list is now done with CAS
- Ensures that no other thread has performed an enqueue during own enqueue
 - Otherwise, we might "overwrite" another threads enqueued node
- Swinging tail to x_{new} might fail another thread has helped us

```
enqueue Q value \triangleq
let node = ref (Some value, ref (None)) in
(rec loop_=
let tail = !(snd(! Q)) in
let next = !(snd(! tail)) in
if tail = !(sn
```



- Head now swung with CAS instruction
- Ensures that no other thread has dequeued the element we are trying to dequeue

```
dequeue Q \triangleq
  (rec loop_ =
     let head = !(fst(! Q)) in
     let tail = !(snd(! Q)) in
    let p = \text{newproph in}
    let next = !(snd(! head)) in
    if head = Resolve(!(fst(!Q)), p, ()) then
       if head = tail then
          if next = None then
            None
          else
            CAS(snd(! Q)) tail next; loop ()
        else
          let value = fst(! next) in
          if CAS (fst(! Q)) head next then
            value
          else loop ()
     else loop ()
     )()
```



- Head now swung with CAS instruction
- Ensures that no other thread has dequeued the element we are trying to dequeue

```
dequeue Q \triangleq
  (rec loop_ =
     let head = !(fst(! Q)) in
     let tail = !(snd(! Q)) in
    let p = \text{newproph in}
    let next = !(snd(! head)) in
    if head = Resolve(!(fst(!Q)), p, ()) then
       if head = tail then
          if next = None then
            None
          else
            CAS(snd(! Q)) tail next; loop ()
        else
          let value = fst(! next) in
          if CAS (fst(! Q)) head next then
            value
          else loop ()
     else loop ()
     )()
```



■ Proving adherence to HOCAP-style specification requires applying the view-shift at some point (must update P to Q)

```
| let p = newproph in | let next = !(snd(! head)) in | if head = Resolve!(fst(! Q)), p, ()) then | if head = tail then | if next = None then | None | ... | else loop () | ...
```

- Proving adherence to HOCAP-style specification requires applying the view-shift at some point (must update P to Q)
- View-shift is applied at Linearisation Points points where the effect of the function takes place

```
| let p = newproph in |
|let next = !(snd(! head)) in |
|if head = Resolve(!(fst(! Q)), p, ()) then |
|if head = tail then |
|if next = None then |
|None |
| ... |
|else loop () |
```

- Proving adherence to HOCAP-style specification requires applying the view-shift at some point (must update P to Q)
- View-shift is applied at Linearisation Points points where the effect of the function takes place
- When the queue is empty, the linearisation point is when reading next (specifically, the dereference instruction)
- We deduce that at exactly that read, the queue was empty

```
| let p = newproph in |
|let next = !(snd(! head)) in |
|if head = Resolve(!(fst(! Q)), p, ()) then |
|if head = tail then |
|if next = None then |
|None |
| ... |
|else loop () |
```

- Proving adherence to HOCAP-style specification requires applying the view-shift at some point (must update P to Q)
- View-shift is applied at Linearisation Points points where the effect of the function takes place
- When the queue is empty, the linearisation point is when reading *next* (specifically, the dereference instruction)
- We deduce that at exactly that read, the queue was empty
- But we only conclude the queue is empty if consistency check on next line succeeds
- The dereference is only the linearisation point if consistency check succeeds

```
...

let p = newproph in

let next = !(snd(! head)) in

if head = Resolve(!(fst(! Q)), p, ()) then

if head = tail then

if next = None then

None

...

else loop ()

...
```

- Proving adherence to HOCAP-style specification requires applying the view-shift at some point (must update P to Q)
- View-shift is applied at Linearisation Points points where the effect of the function takes place
- When the queue is empty, the linearisation point is when reading *next* (specifically, the dereference instruction)
- We deduce that at exactly that read, the queue was empty
- But we only conclude the queue is empty if consistency check on next line succeeds
- The dereference is only the linearisation point if consistency check succeeds
- Prophecies: reason about future computations (e.g. the consistency check)
 - !(fst(!Q)) will evaluate to some v_p (later proof obligation)
 - Before reading next, reason about whether $head = v_p$

The Lock-and-CC-Free Michael-Scott Queue

■ Reason for consistency checks: ABA problem in original implementation

The Lock-and-CC-Free Michael-Scott Queue

- Reason for consistency checks: ABA problem in original implementation
- HeapLang is garbage collected language, so we can remove consistency checks
- Can also remove prophecy in dequeue
 - When we read next, we know immediately whether dequeue will conclude empty queue
 - both head and tail are already fixed

```
dequeue Q \triangleq
  (rec\ loop_{-} =
     let head = !(fst(! Q)) in
     let tail = !(snd(!Q)) in
     let next = !(snd(! head)) in
     if head = tail then
       if next = None then
          None
        else
          CAS(snd(!Q)) tail next; loop ()
     else
       let value = fst(! next) in
       if CAS (fst(! Q)) head next then
          value
        else loop ()
     )()
```

The Lock-and-CC-Free Michael-Scott Queue

- Reason for consistency checks: ABA problem in original implementation
- HeapLang is garbage collected language, so we can remove consistency checks
- Can also remove prophecy in dequeue
 - When we read next, we know immediately whether dequeue will conclude empty queue
 - both head and tail are already fixed
- Correctness: both versions shown to satisfy HOCAP-style specification...

```
initialize \triangleq
let node = ref(None, ref(None)) in ref(ref(node), ref(node))
enqueue Q value \triangleq
let node = ref(Some value, ref(None)) in (rec\ loop_= =
let tail = !(snd(!\ Q)) in let next = !(snd(!\ tail)) in if next = None then if CAS (snd(!\ tail)) next node then CAS (snd(!\ Q)) tail node else loop() else CAS (snd(!\ Q)) tail next; loop()
```

```
dequeue Q \triangleq
  (rec\ loop_{-} =
     let head = !(fst(! Q)) in
     let tail = !(snd(!Q)) in
     let next = !(snd(! head)) in
     if head = tail then
       if next = None then
          None
        else
          CAS(snd(!Q)) tail next; loop ()
     else
       let value = fst(! next) in
       if CAS (fst(! Q)) head next then
          value
        else loop ()
     )()
```

Proving that the Lock-and-CC-Free Michael-Scott Queue Satisfies the HOCAP-style Specification

- The queue relies on some important properties to function correctly:
 - The set of nodes reachable from a particular node only grows
 - The head and tail are only moved forward in the linked list
 - The tail cannot lag behind the head (unlike in the two-lock version)

- The queue relies on some important properties to function correctly:
 - The set of nodes reachable from a particular node only grows
 - The head and tail are only moved forward in the linked list
 - The tail cannot lag behind the head (unlike in the two-lock version)
- We capture all these properties with a notion of *reachability*
- Consists of a concrete and abstract version of reachability

- The queue relies on some important properties to function correctly:
 - The set of nodes reachable from a particular node only grows
 - The head and tail are only moved forward in the linked list
 - The tail cannot lag behind the head (unlike in the two-lock version)
- We capture all these properties with a notion of *reachability*
- Consists of a concrete and abstract version of reachability

Concrete Reachability

■ Concrete reachability essentially captures a section of the linked list (á la isLL)

- The queue relies on some important properties to function correctly:
 - The set of nodes reachable from a particular node only grows
 - The head and tail are only moved forward in the linked list
 - The tail cannot lag behind the head (unlike in the two-lock version)
- We capture all these properties with a notion of *reachability*
- Consists of a concrete and abstract version of reachability

Concrete Reachability

- Concrete reachability essentially captures a section of the linked list (á la isLL)
- The proposition $x_n \rightsquigarrow x_m$ asserts that x_n can reach x_m through the linked list

- The queue relies on some important properties to function correctly:
 - The set of nodes reachable from a particular node only grows
 - The head and tail are only moved forward in the linked list
 - The tail cannot lag behind the head (unlike in the two-lock version)
- We capture all these properties with a notion of *reachability*
- Consists of a concrete and abstract version of reachability

Concrete Reachability

- Concrete reachability essentially captures a section of the linked list (á la isLL)
- The proposition $x_n \rightsquigarrow x_m$ asserts that x_n can reach x_m through the linked list
- Defined inductively as follows

$$x_n \rightsquigarrow x_m \stackrel{\triangle}{=} \mathsf{in}(x_n) \mapsto^{\square} (\mathsf{val}(x_n), \mathsf{out}(x_n)) * (x_n = x_m \vee \exists x_p. \mathsf{out}(x_n) \mapsto^{\square} \mathsf{in}(x_p) * x_p \rightsquigarrow x_m)$$

- The queue relies on some important properties to function correctly:
 - The set of nodes reachable from a particular node only grows
 - The head and tail are only moved forward in the linked list
 - The tail cannot lag behind the head (unlike in the two-lock version)
- We capture all these properties with a notion of *reachability*
- Consists of a concrete and abstract version of reachability

Concrete Reachability

- Concrete reachability essentially captures a section of the linked list (á la isLL)
- The proposition $x_n \rightsquigarrow x_m$ asserts that x_n can reach x_m through the linked list
- Defined inductively as follows

$$x_n \rightsquigarrow x_m \triangleq \mathsf{in}(x_n) \mapsto^{\square} (\mathsf{val}(x_n), \mathsf{out}(x_n)) * (x_n = x_m \lor \exists x_p. \mathsf{out}(x_n) \mapsto^{\square} \mathsf{in}(x_p) * x_p \rightsquigarrow x_m)$$

Concrete reachability is reflexive and transitive

Abstract Reachability

Abstract reachability is concerned with tracking specific types of nodes, such as the head node, the tail node, and the last node

Abstract Reachability

- Abstract reachability is concerned with tracking specific types of nodes, such as the head node, the tail node, and the last node
- lacktriangle Tracked using ghost names, e.g. γ_{Head} , γ_{Tail} , and γ_{Last}
 - lacksquare Implemented using the resource algebra $\operatorname{AUTH}(\mathcal{P}(\textit{Node}))$

Abstract Reachability

- Abstract reachability is concerned with tracking specific types of nodes, such as the head node, the tail node, and the last node
- Tracked using ghost names, e.g. $\gamma_{\rm Head}$, $\gamma_{\rm Tail}$, and $\gamma_{\rm Last}$ Implemented using the resource algebra ${\rm AUTH}(\mathcal{P}(\textit{Node}))$
- Defined in two parts: Abstract Points-to $(\gamma \rightarrowtail x)$ and Abstract Reach $(x \dashrightarrow \gamma)$
- For instance, $\gamma_{Tail} \rightarrow x_n$ means that the current tail node is x_n
- And $x_m \dashrightarrow \gamma_{Tail}$ means that node x_m can always reach the tail node

Abstract Reachability

- Abstract reachability is concerned with tracking specific types of nodes, such as the head node, the tail node, and the last node
- Tracked using ghost names, e.g. $\gamma_{\rm Head}$, $\gamma_{\rm Tail}$, and $\gamma_{\rm Last}$ Implemented using the resource algebra ${
 m AUTH}({\cal P}({\it Node}))$
- Defined in two parts: Abstract Points-to $(\gamma \rightarrowtail x)$ and Abstract Reach $(x \dashrightarrow \gamma)$
- For instance, $\gamma_{Tail} \rightarrow x_n$ means that the current tail node is x_n
- And $x_m \dashrightarrow \gamma_{\mathrm{Tail}}$ means that node x_m can always reach the tail node

Lemmas for Reachability (simplified)

$$x \rightsquigarrow x \Rrightarrow \exists \gamma. \gamma \rightarrowtail x$$
 (Abs Reach Alloc)
 $x_n \dashrightarrow \gamma_m * \gamma_m \rightarrowtail x_m \twoheadrightarrow x_n \leadsto x_m$ (Abs Reach Concr)
 $x_n \leadsto x_m * \gamma_m \rightarrowtail x_m \Rrightarrow x_n \dashrightarrow \gamma_m$ (Abs Reach Abs)
 $\gamma_m \rightarrowtail x_m * x_m \leadsto x_o \Rrightarrow \gamma_m \rightarrowtail x_o$ (Abs Reach Advance)

