

Master's Thesis Exam

Verification of the Blocking and Non-Blocking Michael-Scott Queue Algorithms

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Overview of the Project and Contributions

- Initial goal was to prove soundness of the two M&S Queues
- The project later generalised the results to apply to queues in general
- In particular, three different specifications for queues were given
 - Sequential specification
 - Useful for sequential clients
 - Concurrent specification
 - Proves soundness of concurrent queues
 - Useful for some concurrent clients
 - HOCAP-style specification
 - Stronger specification, useful for more complex clients
 - Demonstrated with a specific queue client (`queueAdd`)
- It was demonstrated that the HOCAP-style specification derives the other two specifications
- Implementations of the M&S Queues in HeapLang were proven to meet the three specifications
 - In particular, both version are sound
- All proofs have been mechanised in the Coq proof assistant

- 1 Queue Specifications
- 2 The Two-Lock Michael-Scott Queue
- 3 Proving that the Two-Lock Michael-Scott Queue Satisfies the HOCAP-style Specification
- 4 The Lock-Free Michael-Scott Queue
- 5 Proving that the Lock-and-CC-Free Michael-Scott Queue Satisfies the HOCAP-style Specification

Queue Specifications

Specifications for Queues

Assumptions on Queues

- Queues consists of initialize, enqueue, and dequeue
- initialize creates an empty queue: $[]$
- enqueue adds a value, v , to the beginning of the queue $xs_v: v :: xs_v$
- dequeue depends on whether queue is empty:
 - If non-empty, $xs_v ++ v$, remove v and return $\text{Some } v$
 - If empty, $[]$, return None

Nature of Specifications

- Specifications written in Iris, a higher order CSL
- Expressed in terms of *Hoare triples*: $\{P\} e \{v.\Phi \ v\}$
- Hoare triples prove partial correctness of programs, e
- In particular: safety
- Idea: clients can use Hoare triples to prove results about their own code

Sequential Specification

Definition (Sequential Specification)

$\exists \text{isQueue}_S : \text{Val} \rightarrow \text{List Val} \rightarrow \text{SeqQgnames} \rightarrow \text{Prop.}$

$\{\text{True}\} \text{ initialize } () \{v_q. \exists G. \text{isQueue}_S(v_q, [], G)\}$

$\wedge \quad \forall v_q, v, xs_v, G. \{\text{isQueue}_S(v_q, xs_v, G)\} \text{ enqueue } v_q \ v \{w. \text{isQueue}_S(v_q, (v :: xs_v), G)\}$

$\wedge \quad \forall v_q, xs_v, G. \{\text{isQueue}_S(v_q, xs_v, G)\}$

$\text{ dequeue } v_q$

$\left\{ w. \begin{array}{l} (xs_v = [] * w = \text{None} * \text{isQueue}_S(v_q, xs_v, G)) \vee \\ (\exists v, xs'_v. xs_v = xs'_v ++ [v] * w = \text{Some } v * \text{isQueue}_S(v_q, xs'_v, G)) \end{array} \right\}$

- The proposition $\text{isQueue}_S(v_q, xs_v, G)$, states that value v_q represents the queue, which contains elements xs_v
- $G \in \text{SeqQgnames}$ is a collection of ghost names (depends on specific queue)
- Specification consists of three Hoare triples – one for each queue function
- Important: isQueue_S not required to be persistent!

Concurrent Specification

- To support concurrent clients, we shall require the queue predicate be persistent
- Tracking the contents of queue in the way that the sequential specification did doesn't work
- Threads will start disagreeing on contents of queue, as they have only local view of contents
- Give up on tracking contents for now
- Instead, promise that all elements satisfy client-defined predicate, Ψ

Definition (Concurrent Specification)

$\exists \text{isQueue}_C : (Val \rightarrow Prop) \rightarrow Val \rightarrow \text{ConcQnames} \rightarrow Prop.$

$\forall \Psi : Val \rightarrow Prop.$

$\forall v_q, G. \text{isQueue}_C(\Psi, v_q, G) \implies \Box \text{isQueue}_C(\Psi, v_q, G)$

$\wedge \{ \text{True} \} \text{ initialize } () \{ v_q. \exists G. \text{isQueue}_C(\Psi, v_q, G) \}$

$\wedge \forall v_q, v, G. \{ \text{isQueue}_C(\Psi, v_q, G) * \Psi(v) \} \text{ enqueue } v_q \ v \{ w. \text{True} \}$

$\wedge \forall v_q, G. \{ \text{isQueue}_C(\Psi, v_q, G) \} \text{ dequeue } v_q \{ w. w = \text{None} \vee (\exists v. w = \text{Some } v * \Psi(v)) \}$

HOCAP-style Specification - Abstract State RA

- ► Introduce Auth and Frag predicates for tracking abstract state ◀
- ► Show Resource Algebra ◀

Lemmas on the Abstract State RA

$\vdash \models \exists \gamma. \gamma \models_{\bullet} xs_v * \gamma \models_{\circ} xs_v$ (Abstract State Alloc)

$\gamma \models_{\bullet} xs'_v * \gamma \models_{\circ} xs_v \vdash xs_v = xs'_v$ (Abstract State Agree)

$\gamma \models_{\bullet} xs'_v * \gamma \models_{\circ} xs_v \Rightarrow \gamma \models_{\bullet} xs''_v * \gamma \models_{\circ} xs_v$ (Abstract State Update)

HOCAP-style Specification

Definition (HOCAP Specification)

$\exists \text{isQueue} : \text{Val} \rightarrow \text{Qgnames} \rightarrow \text{Prop}.$

$\forall v_q, G. \text{isQueue}(v_q, G) \implies \Box \text{isQueue}(v_q, G)$

$\wedge \{ \text{True} \} \text{ initialize } () \{ v_q. \exists G. \text{isQueue}(v_q, G) * G.\gamma_{\text{Abst}} \mapsto_\bullet [] \}$

$\wedge \forall v_q, v, G, P, Q. \left(\forall x_{s_v}. G.\gamma_{\text{Abst}} \mapsto_\bullet x_{s_v} * P \Rightarrow_{\mathcal{E} \setminus \mathcal{N}.i \uparrow} \triangleright G.\gamma_{\text{Abst}} \mapsto_\bullet (v :: x_{s_v}) * Q \right) \multimap$
 $\{ \text{isQueue}(v_q, G) * P \} \text{ enqueue } v_q \ v \{ w.Q \}$

$\wedge \forall v_q, G, P, Q.$

$\left(\forall x_{s_v}. G.\gamma_{\text{Abst}} \mapsto_\bullet x_{s_v} * P \Rightarrow_{\mathcal{E} \setminus \mathcal{N}.i \uparrow} \triangleright \left(\begin{array}{l} (x_{s_v} = [] * G.\gamma_{\text{Abst}} \mapsto_\bullet x_{s_v} * Q(\text{None})) \\ \vee \left(\begin{array}{l} \exists v, x'_{s_v}. x_{s_v} = x'_{s_v} ++ [v] * \\ G.\gamma_{\text{Abst}} \mapsto_\bullet x'_{s_v} * Q(\text{Some } v) \end{array} \right) \end{array} \right) \right)$
 $\{ \text{isQueue}(v_q, G) * P \} \text{ dequeue } v_q \{ w.Q(w) \}$

Queue Client - A PoC Client

- Idea: a minimal client complex enough to require HOCAP specification
- Uses parallel composition, so sequential specification insufficient
- Relies on dequeues not returning None, so concurrent specification insufficient
- HOCAP specification supports consistency and allows us to track queue contents, allowing us to exclude cases where dequeue returns None

```
unwrap w  $\triangleq$  match w with None  $\Rightarrow$  () () | Some v  $\Rightarrow$  v end
```

```
enqdeq vq c  $\triangleq$  enqueue vq c; unwrap(dequeue vq)
```

```
queueAdd a b  $\triangleq$   
  let vq = initialize () in  
  let p = (enqdeq vq a) || (enqdeq vq b) in  
  fst p + snd p
```

Queue Client - A PoC Client (continued)

Lemma (QueueAdd Specification)

$$\forall a, b \in \mathbb{Z}. \{True\} \text{ queueAdd } a \ b \{v.v = a + b\}$$

- Proof idea: Create invariant capturing possible states of queue contents
- Tokens are used to reason about which state we are in

Definition (Invariant for QueueAdd)

$$\begin{aligned} I_{QA}(G, Ga, a, b) \triangleq & G.\gamma_{\text{Abst}} \Rightarrow_{\circ} [] * \text{TokD1 } Ga * \text{TokD2 } Ga \vee \\ & G.\gamma_{\text{Abst}} \Rightarrow_{\circ} [a] * \text{TokA } Ga * (\text{TokD1 } Ga \vee \text{TokD2 } Ga) \vee \\ & G.\gamma_{\text{Abst}} \Rightarrow_{\circ} [b] * \text{TokB } Ga * (\text{TokD1 } Ga \vee \text{TokD2 } Ga) \vee \\ & G.\gamma_{\text{Abst}} \Rightarrow_{\circ} [a; b] * \text{TokA } Ga * \text{TokB } Ga \vee \\ & G.\gamma_{\text{Abst}} \Rightarrow_{\circ} [b; a] * \text{TokB } Ga * \text{TokA } Ga \vee \end{aligned}$$

The Two-Lock Michael-Scott Queue

Implementation: initialize

►format◄

- The data structure is a linked list
- A node x in the linked list is a triple, $x = (\ell_{\text{in}}, w, \ell_{\text{out}})$, with ℓ_{in} pointing to (w, ℓ_{out})
- We use the following notation for nodes

$$\text{in}(x) = \ell_{\text{in}}$$

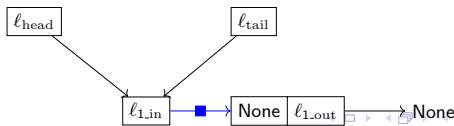
$$\text{val}(x) = w$$

$$\text{out}(x) = \ell_{\text{out}}$$

- The initialize function first creates an initial head node, x_{head}
- Then a lock protecting the head pointer, and a lock protecting the tail pointer
- Finally, it creates the head and tail pointers, ℓ_{head} and ℓ_{tail} , both pointing to x_{head}

initialize \triangleq

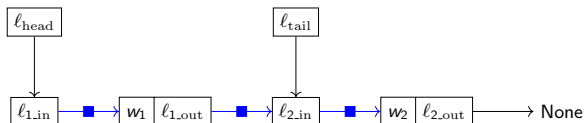
```
let node = ref (None, ref (None)) in
let H_lock = newLock() in
let T_lock = newLock() in
ref ((ref (node), ref (node)), (H_lock, T_lock))
```



Implementation: enqueue

- The enqueue function consists of the following steps
 - 1 Create a new node, x_{new} , containing value to be enqueued
 - 2 Acquire the tail lock
 - 3 Add x_{new} to linked list
 - 4 Swing tail pointer to x_{new}
 - 5 Release the tail lock

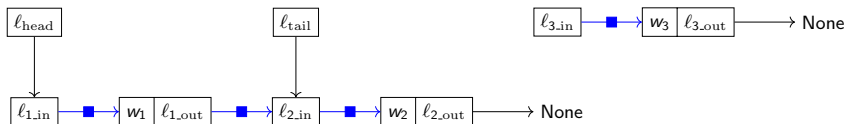
```
enqueue  $Q$  value  $\triangleq$   
  let node = ref (Some value, ref (None)) in  
  acquire(snd(snd(! Q)));  
  snd(!(! (snd(fst(! Q)))))  $\leftarrow$  node;  
  snd(fst(! Q))  $\leftarrow$  node;  
  release(snd(snd(! Q)))
```



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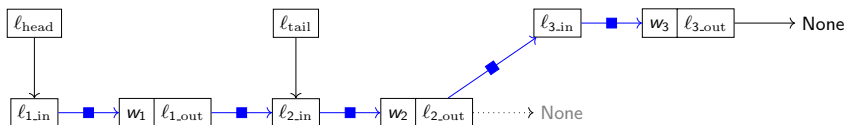
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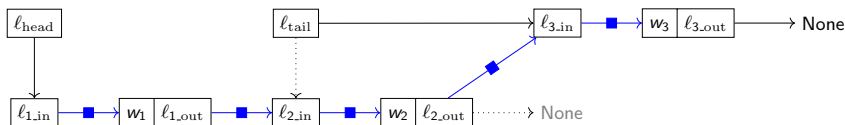
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  snd(fst(! Q))  $\leftarrow$  node;  
  release(snd(snd(! Q)))
```

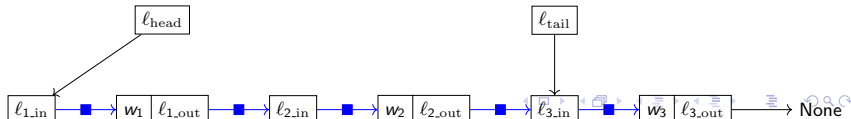


Implementation: dequeue

►format◀

- The dequeue function checks if queue is empty
 - If empty, return *None*
 - Else, swing head pointer to new head, and return dequeued value

```
dequeue  $Q \triangleq$   
  acquire(fst(snd(! Q)));  
  let node = !(fst(fst(! Q))) in  
  let new_head = !(snd(! node)) in  
  if new_head = None then  
    release(fst(snd(! Q)));  
    None  
  else  
    let value = fst(! new_head) in  
    fst(fst(! Q))  $\leftarrow$  new_head;  
    release(fst(snd(! Q)));  
    value
```

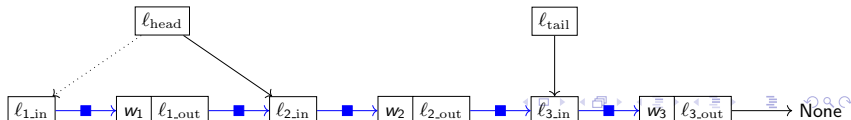


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  acquire(fst(snd(! Q)));  
  let node = !(fst(fst(! Q))) in  
  let new_head = !(snd(! node)) in  
  if new_head = None then  
    release(fst(snd(! Q)));  
    None  
  else  
    let value = fst(! new_head) in  
    fst(fst(! Q))  $\leftarrow$  new_head;  
    release(fst(snd(! Q)));  
    value
```



Observations on Behaviour of the Two-Lock M&S Queue

►format and simplify◄

- 1 The tail node is always either the last or second last node in the linked list.
- 2 All but the last pointer in the linked list (the pointer to None) never change.
- 3 Nodes in the linked list are never deleted. Hence, the linked list only ever grows.
- 4 The tail can lag one node behind the head.
- 5 At any given time, the queue is in one of four states:
 - 1 No threads are interacting with the queue (**Static**).
 - 2 A thread is enqueueing (**Enqueue**).
 - 3 A thread is dequeueing (**Dequeue**).
 - 4 A thread is enqueueing and a thread is dequeueing (**Both**).

Proving that the Two-Lock Michael-Scott Queue Satisfies the HOCAP-style Specification

The isLL Predicate

►format slide◄

- Idea: express the structure of the linked list in terms of points-to predicates
- Also captures persistent and non-persistent parts of the linked list

Definition (Linked List Chain Predicate)

$$\text{isLL_chain}([]) \triangleq \text{True}$$

$$\text{isLL_chain}([x]) \triangleq \text{in}(x) \mapsto^{\square} (\text{val}(x), \text{out}(x))$$

$$\text{isLL_chain}(x :: x' :: xs) \triangleq \text{in}(x) \mapsto^{\square} (\text{val}(x), \text{out}(x)) * \text{out}(x') \mapsto^{\square} \text{in}(x') * \text{isLL_chain}(x' :: xs)$$

Definition (Linked List Predicate)

$$\text{isLL}([]) \triangleq \text{True}$$

$$\text{isLL}(x :: xs) \triangleq \text{out}(x) \mapsto \text{None} * \text{isLL_chain}(x :: xs)$$

Example

Consider the list $[x_0, x_1, \dots, x_n]$ where x_i is a node in the list.

Invariant

►format slide◄

- Queue predicate must be persistent (according to specification)
- The queue relies on non-persistent resources (e.g. $\ell_{\text{head}} \mapsto \ell_{\text{in}}$)
- Solution: identify a *queue invariant*, describing the resources
- Invariants are persistent in Iris

Definition (Two-Lock M&S Queue HOCAP Invariant)

$$\begin{aligned} I_{\text{TLH}}(\ell_{\text{head}}, \ell_{\text{tail}}, G) &\triangleq \\ \exists x_{\text{sv}}. G. \gamma_{\text{Abst}} \mapsto_{\bullet} x_{\text{sv}} * \\ \exists xs, xs_{\text{queue}}, xs_{\text{old}}, x_{\text{head}}, x_{\text{tail}}. \\ xs &= xs_{\text{queue}} ++ [x_{\text{head}}] ++ xs_{\text{old}} * \\ \text{isLL}(xs) * \\ \text{projVal}(xs_{\text{queue}}) &= \text{wrapSome}(x_{\text{sv}}) * \\ (\\ \ell_{\text{head}} \mapsto \text{in}(x_{\text{head}}) * \ell_{\text{tail}} \mapsto \text{in}(x_{\text{tail}}) * \text{isLast}(x_{\text{tail}}, xs) * & \text{(Static)} \\ \text{TokNE } G * \text{TokND } G * \text{TokUpdated } G \end{aligned}$$

Queue Predicate

- HOCAP-style specification requires the existence of a persistent queue predicate
- We define it in terms of our invariant

Definition (Two-Lock M&S Queue - isQueue Predicate)

$$\begin{aligned} \text{isQueue}(v_q, G) &\triangleq \exists \ell_{\text{queue}}, \ell_{\text{head}}, \ell_{\text{tail}} \in \text{Loc}. \exists h_{\text{lock}}, t_{\text{lock}} \in \text{Val}. \\ &v_q = \ell_{\text{queue}} * \ell_{\text{queue}} \mapsto^{\square} ((\ell_{\text{head}}, \ell_{\text{tail}}), (h_{\text{lock}}, t_{\text{lock}})) * \\ &\boxed{\text{TLH}(\ell_{\text{head}}, \ell_{\text{tail}}, G)}^{\mathcal{N}.\text{queue}} * \\ &\text{isLock}(G.\gamma_{\text{Hlock}}, h_{\text{lock}}, \text{TokD } G) * \\ &\text{isLock}(G.\gamma_{\text{Tlock}}, t_{\text{lock}}, \text{TokE } G) \end{aligned}$$

- The queue predicate is persistent, as all its constituents are
- Proving that TLMSQ satisfies the HOCAP-style specification then consists of proving the Hoare triples for initialize, enqueue, and dequeue
- We here focus on enqueue

Proof Sketch of the Hoare triple for enqueue

► **format** ◀ Must prove:

$$\forall v_q, v, G, P, Q. \quad (\forall x_{sv}. G.\gamma_{\text{Abst}} \mapsto_{\bullet} x_{sv} * P \Rightarrow_{\varepsilon \setminus \mathcal{N}.i\uparrow} \triangleright G.\gamma_{\text{Abst}} \mapsto_{\bullet} (v :: x_{sv}) * Q) \multimap \{ \text{isQueue}(v_q, G) * P \} \text{ enqueue } v_q \ v \{ w.Q \}$$

Assume the view-shift, and the persistent information in $\text{isQueue}(v_q, Qgnames)$:

$v_q = \ell_{\text{queue}} * \ell_{\text{queue}} \mapsto^{\square} ((\ell_{\text{head}}, \ell_{\text{tail}}), (h_{\text{lock}}, t_{\text{lock}}))$, the invariant

$\boxed{\text{TLH}(\ell_{\text{head}}, \ell_{\text{tail}}, G)}^{\mathcal{N}.queue}$, and $\text{isLock}(G.\gamma_{\text{Tlock}}, t_{\text{lock}}, \text{TokE } G)$

```
{P}
  let node = ref (Some v, ref (None)) in   (create node xnew)
{P * out(xnew) ↦ None}
  acquire(snd(snd(!vq)));   (acquire tail lock)
{P * out(xnew) ↦ None * TokE G}
  et = !(snd(fst(!vq)))   (find current tail, xtail. ITLH: Static/Dequeue → Enqueue/Both (before))
{P * out(xnew) ↦ None * ℓtail ↦ 1/2 in(xtail) * TokNE G * TokAfter G}
  snd(!et) ← node;   (make xtail point to xnew. ITLH: Enqueue/Both (before) → Enqueue/Both (after))
{Q * ℓtail ↦ 1/2 in(xtail) * TokNE G * TokBefore G}
  snd(fst(!vq)) ← node;   (swing tail pointer to xnew. ITLH: Enqueue/Both (after) → Static/Dequeue)
{Q * TokE G}
  release(snd(snd(!vq)))   (release tail lock)
{Q}
```

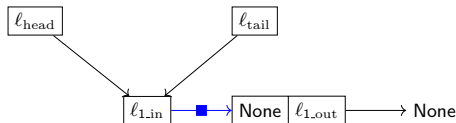
The Lock-Free Michael-Scott Queue

Implementation: initialize

- Queue data structure is still a linked list
- The lock-free versions of initialize, enqueue, and dequeue perform the same manipulations of the linked list as two-lock versions
- Difference is how the manipulations take place: CAS
- No longer need locks

initialize \triangleq

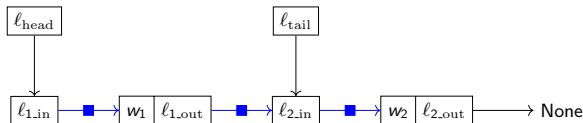
```
let node = ref (None, ref (None)) in  
ref (ref (node), ref (node))
```



Implementation: enqueue

- Appending x_{new} to linked list is now done with CAS
- Ensures that no other thread has performed an enqueue while we have been working
- Swinging tail to x_{new} might fail: another thread has helped us

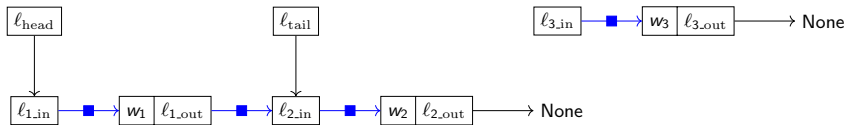
```
enqueue Q value  $\triangleq$   
  let node = ref (Some value, ref (None)) in  
  (rec loop_ =  
    let tail = !(snd(! Q)) in  
    let next = !(snd(! tail)) in  
    if tail = !(snd(! Q)) then  
      if next = None then  
        if CAS (snd(! tail)) next node then  
          CAS (snd(! Q)) tail node  
        else loop ()  
      else CAS (snd(! Q)) tail next; loop ()  
    else loop ()  
  ) ()
```



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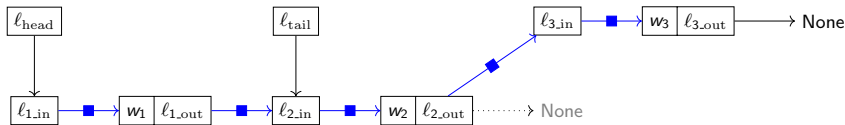
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    let next = !(snd(! tail)) in  
    if tail = !(snd(! Q)) then  
      if next = None then  
        if CAS (snd(! tail)) next node then  
          CAS (snd(! Q)) tail node  
        else loop ()  
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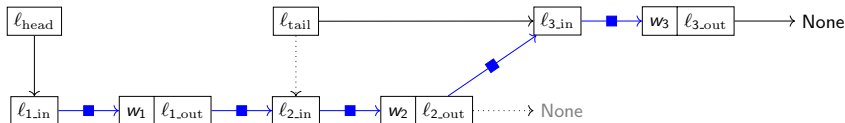
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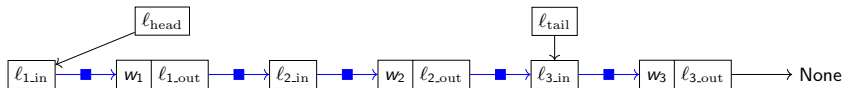
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          CAS (snd(! Q)) tail node  
        else loop ()  
      else CAS (snd(! Q)) tail next; loop ()  
    else loop ()  
  ) ()
```



Implementation: dequeue

- Head now swung with CAS
- Ensures that another thread hasn't dequeued the element we are trying to dequeue

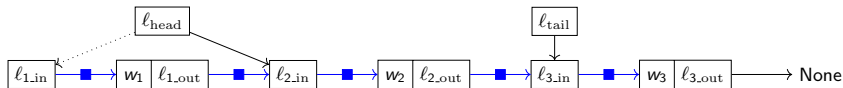
```
dequeue Q  $\triangleq$   
(rec loop_ =  
  let head = !(fst(! Q)) in  
  let tail = !(snd(! Q)) in  
  let p = newproph in  
  let next = !(snd(! head)) in  
  if head = Resolve(!(fst(! Q)), p, ()) then  
    if head = tail then  
      if next = None then  
        None  
      else  
        CAS(snd(! Q)) tail next; loop ()  
    else  
      let value = fst(! next) in  
      if CAS (fst(! Q)) head next then  
        value  
      else loop ()  
  else loop ()  
)()
```



Implementation: dequeue

- Head now swung with CAS
- Ensures that another thread hasn't dequeued the element we are trying to dequeue

```
dequeue Q  $\triangleq$   
(rec loop_ =  
  let head = !(fst(! Q)) in  
  let tail = !(snd(! Q)) in  
  let p = newproph in  
  let next = !(snd(! head)) in  
  if head = Resolve(!(fst(! Q)), p, ()) then  
    if head = tail then  
      if next = None then  
        None  
      else  
        CAS(snd(! Q)) tail next; loop ()  
    else  
      let value = fst(! next) in  
      if CAS (fst(! Q)) head next then  
        value  
      else loop ()  
  else loop ()  
)()
```



Prophecies

► create slide◄

The Lock-and-CC-Free Michael-Scott Queue

- Consistency checks and associated loops gone
- Can also remove prophecy in dequeue
 - When we read *next*, we know immediately whether dequeue will conclude empty queue
 - both *head* and *tail* are already fixed

```
initialize  $\triangleq$   
  let node = ref (None, ref (None)) in  
  ref (ref (node), ref (node))  
  
enqueue Q value  $\triangleq$   
  let node = ref (Some value, ref (None)) in  
  (rec loop_ =  
    let tail = !(snd(! Q)) in  
    let next = !(snd(! tail)) in  
    if next = None then  
      if CAS (snd(! tail)) next node then  
        CAS (snd(! Q)) tail node  
      else loop ()  
    else CAS (snd(! Q)) tail next; loop ()  
  ) ()
```

```
dequeue Q  $\triangleq$   
  (rec loop_ =  
    let head = !(fst(! Q)) in  
    let tail = !(snd(! Q)) in  
    let next = !(snd(! head)) in  
    if head = tail then  
      if next = None then  
        None  
      else  
        CAS (snd(! Q)) tail next; loop ()  
    else  
      let value = fst(! next) in  
      if CAS (fst(! Q)) head next then  
        value  
      else loop ()  
  )()
```

Proving that the Lock-and-CC-Free Michael-Scott Queue Satisfies the HOCAP-style Specification

Reachability

- The queue relies on some important properties to function correctly:
 - The set of nodes reachable from a particular node only grows
 - The head and tail are only moved forward in the linked list
 - The tail cannot lag behind the head (unlike in the two-lock version)
- We capture all these properties with a notion of *reachability*
- Consists of a concrete and abstract version of reachability

Concrete Reachability

- Concrete reachability essentially captures a section of the linked list (à la isLL)
- The proposition $x_n \rightsquigarrow x_m$ asserts that x_n can reach x_m through the linked list
- Defined inductively as follows

$$x_n \rightsquigarrow x_m \triangleq \text{in}(x_n) \mapsto^{\square} (\text{val}(x_n), \text{out}(x_n)) * (x_n = x_m \vee \exists x_p. \text{out}(x_n) \mapsto^{\square} \text{in}(x_p) * x_p \rightsquigarrow x_m)$$

- Concrete reachability is reflexive and transitive

Reachability (continued)

Abstract Reachability

- Abstract reachability is concerned with tracking specific *types* of nodes, such as the head node, the tail node, and the last node
- Tracked using ghost names, e.g. γ_{Head} , γ_{Tail} , and γ_{Last}
 - Implemented using the resource algebra $\text{AUTH}(\mathcal{P}(\text{Node}))$
- Defined in two parts: Abstract Points-to ($\gamma \multimap x$) and Abstract Reach ($x \dashrightarrow \gamma$)
- For instance, $\gamma_{\text{Tail}} \multimap x_n$ means that the current tail node is x_n
- And $x_m \dashrightarrow \gamma_{\text{Tail}}$ means that node x_m can always reach the tail node

Lemmas for Reachability (simplified)

$$x \leadsto x \Rightarrow \exists \gamma. \gamma \multimap x \quad (\text{Abs Reach Alloc})$$

$$x_n \dashrightarrow \gamma_m * \gamma_m \multimap x_m \multimap x_n \leadsto x_m \quad (\text{Abs Reach Concr})$$

$$x_n \leadsto x_m * \gamma_m \multimap x_m \Rightarrow x_n \dashrightarrow \gamma_m \quad (\text{Abs Reach Abs})$$

$$\gamma_m \multimap x_m * x_m \leadsto x_o \Rightarrow \gamma_m \multimap x_o \quad (\text{Abs Reach Advance})$$

In Coq!

