

## **Application: Number systems and circuits for addition**


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1


# Binary number system

Positional system: multiply each digit by its place value

- Decimal notation:


$$4268_{10} = 4 \cdot 10^3 + 2 \cdot 10^2 + 6 \cdot 10^1 + 8 \cdot 10^0$$

- Binary notation


$$\begin{aligned} 11000111_2 &= 1 \cdot 2^7 + 1 \cdot 2^6 + 0 \cdot 2^5 + 0 \cdot 2^4 + 0 \cdot 2^3 + 1 \cdot 2^2 + 1 \cdot 2^1 + 1 \cdot 2^0 \\ &= 128 + 64 + 0 + 0 + 0 + 4 + 2 + 1 = 199_{10} \end{aligned}$$

Here indices 10 and 2 are used to highlight the *base* of the number system

## Convert decimal numbers to binaries: divide by 2

**Rule:** divide repeatedly by 2, writing down the remainder from each stage from right to left.

Example:

$$533/2 = 266$$

$$\text{remainder} = 1$$

lsb

$$266/2 = 133$$

$$\text{remainder} = 0$$

$$133/2 = 66$$

$$\text{remainder} = 1$$

$$66/2 = 33$$

$$\text{remainder} = 0$$

$$33/2 = 16$$

$$\text{remainder} = 1$$

$$16/2 = 8$$

$$\text{remainder} = 0$$

$$8/2 = 4$$

$$\text{remainder} = 0$$

$$4/2 = 2$$

$$\text{remainder} = 0$$

$$2/2 = 1$$

$$\text{remainder} = 0$$

$$1/2 = 0$$

$$\text{remainder} = 1$$

msb

$$533_{10} = \underline{1000010101_2}$$

$$57 = 2 \cdot 28 + 1$$

28		1	
14		0	
7		0	
3		1	
1		1	↑
0		1	

$$57_{10} = 111001_2$$

## Alternative method

- If you know powers of 2, continually subtract largest power value from the number

$$\begin{aligned}123_{10} &= 64 + (123 - 64) = 64 + 59 \\&= 64 + 32 + (59 - 32) = 64 + 32 + 27 \\&= 64 + 32 + 16 + (27 - 16) = 64 + 32 + 16 + 11 \\&= 64 + 32 + 16 + 8 + (11 - 8) = 64 + 32 + 16 + 8 + 3 = \\&= 64 + 32 + 16 + 8 + 2 + (3 - 2) \\&= \underline{64 + 32 + 16 + 8 + 2 + 1} = \\&= 1 \cdot 2^6 + 1 \cdot 2^5 + 1 \cdot 2^4 + 1 \cdot 2^3 + 0 \cdot 2^2 + 1 \cdot 2^1 + 1 \cdot 2^0 = \\&= 1111011_2\end{aligned}$$

## Binary addition

$$0_2 + 0_2 = 0_2$$

$$0_2 + 1_2 = 1_2$$

$$1_2 + 0_2 = 1_2$$

$$1_2 + 1_2 = 10_2$$

$$\begin{array}{r} 1 \\ 27 \\ + 59 \\ \hline 86 \end{array}$$

$$\begin{array}{r} 1 \quad 1 \\ 1 \quad 1 \\ + \quad 1 \quad 0 \\ \hline 1 \quad 1 \quad 0 \quad 1 \quad 0 \end{array}$$

Handwritten green annotations show a carry of 1 from the second column to the first, and a carry of 1 from the third column to the second.

$$\begin{array}{r}
 1010 \\
 1001 \\
 \hline
 10011
 \end{array}$$

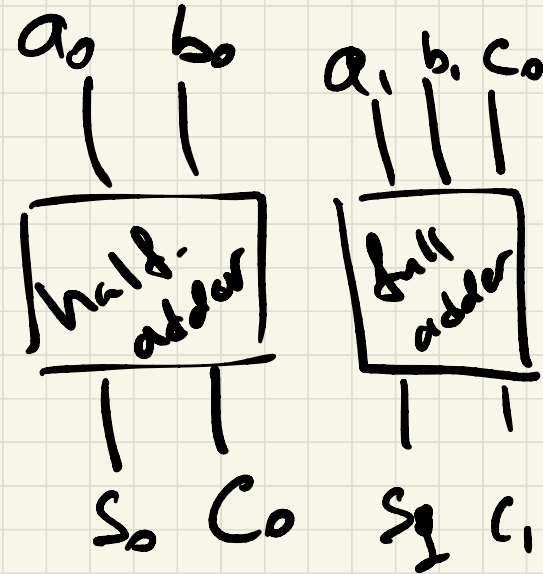
$$\begin{array}{r}
 1111 \\
 1111 \\
 1111 \\
 \hline
 11110
 \end{array}$$

$a$   
 $b$   
 $s$

$$\begin{array}{r}
 1111 \\
 1111 \\
 0001 \\
 \hline
 10000
 \end{array}$$

$c_1$  (circled)  
 $c_{out}$  (circled)  
 $s_1$  (circled)

$a$     1 1 1 1  
 $b$     0 0 0 1

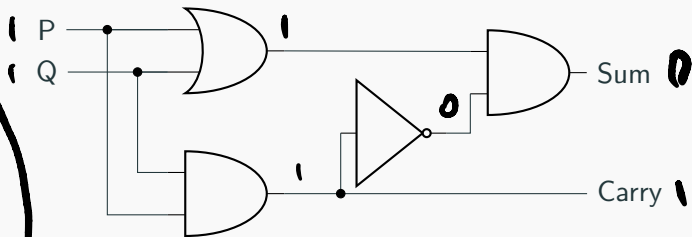




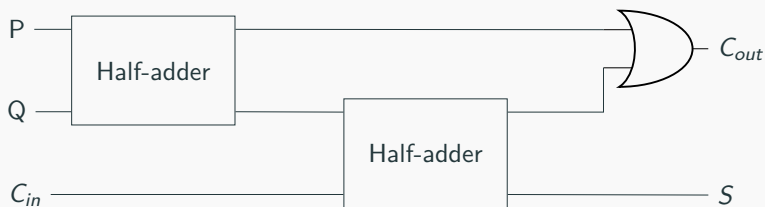
# Half-adder

$$\begin{array}{r} \phantom{+} \phantom{1} \phantom{1} \phantom{1} \phantom{1} \\ \phantom{+} \phantom{1} \phantom{1} \phantom{1} \phantom{1} \\ + \phantom{1} \phantom{1} \phantom{1} \phantom{1} \phantom{1} \\ \hline 1 \phantom{1} \phantom{1} \phantom{1} \phantom{1} \phantom{1} \end{array}$$

P	Q	Carry	Sum
1	1	1	0
1	0	0	1
0	1	0	1
0	0	0	0

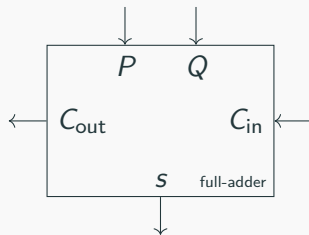
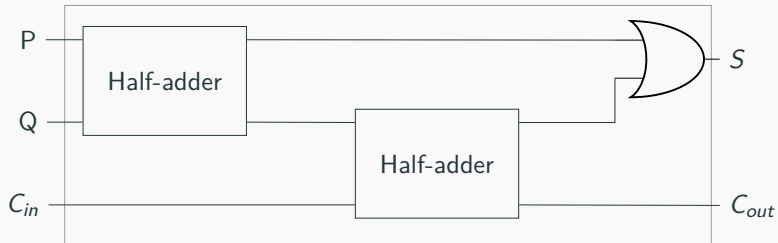


# Full-adder



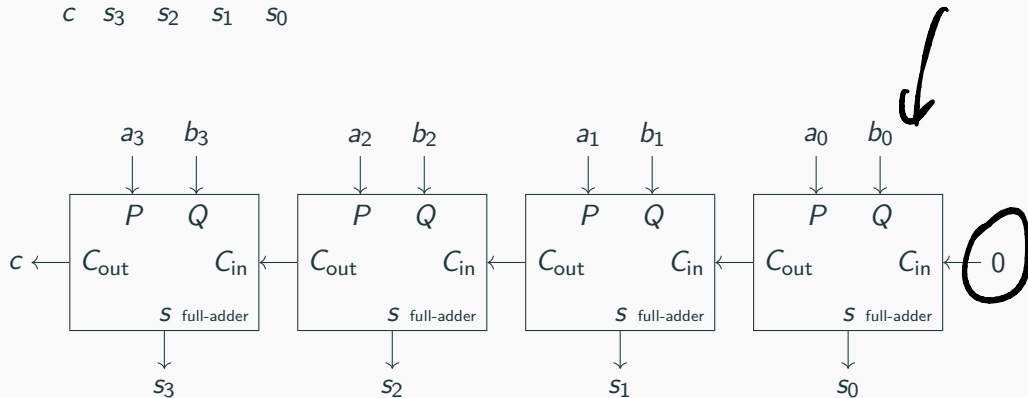
$P$	$Q$	$C_{in}$	$C_{out}$	$S$
1	1	1	1	1
1	1	0	1	0
1	0	1	1	0
1	0	0	0	1
0	1	1	1	0
0	1	0	0	1
0	0	1	0	1
0	0	0	0	0

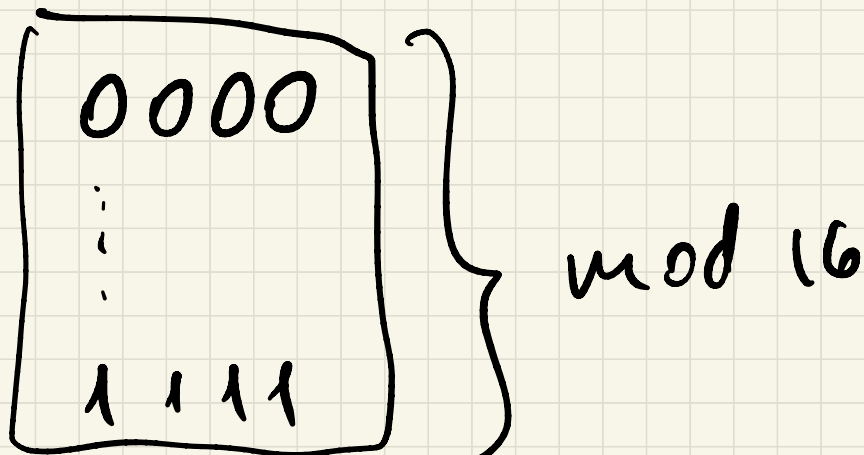
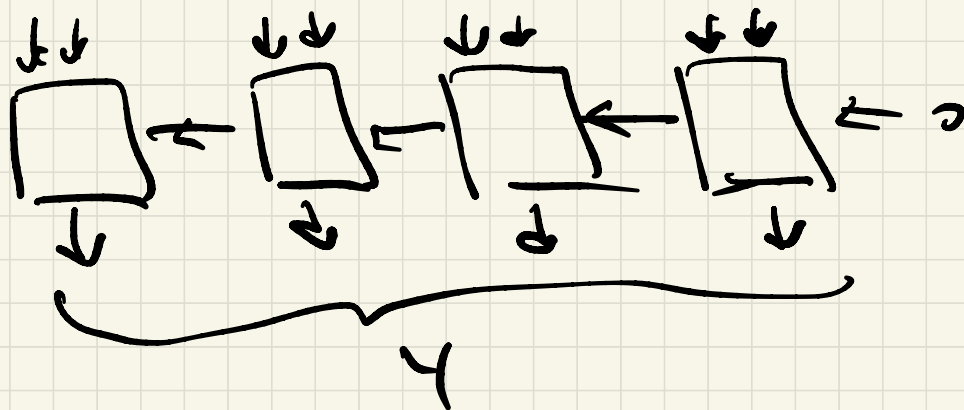
## 'Black box' notation



## 4-bit adder

$$\begin{array}{r} \phantom{+} \phantom{c} \phantom{s_3} \phantom{s_2} \phantom{s_1} \phantom{s_0} \\ \phantom{+} \phantom{c} \phantom{s_3} \phantom{s_2} \phantom{s_1} \phantom{s_0} \\ + \phantom{c} \phantom{s_3} \phantom{s_2} \phantom{s_1} \phantom{s_0} \\ \hline c \phantom{s_3} \phantom{s_2} \phantom{s_1} \phantom{s_0} \end{array}$$





$$2^3 + 2^2 + 2^1 + 2^0 = 8 + 4 + 2 + 1 = 15$$



-17

$$\boxed{0111 = 7_{10}}$$

$$1111 = -7_{10}$$

$$0000 = 0$$

$$\boxed{1000 = -0}$$

mod 16

# Computer representation of negative integers

- Typically a fixed number of bits is used to represent integers:  
8, 16, 32 or 64 bits

- **Unsigned** integer can take all space available

- Signed integers

- **Leading sign**

but then

$$0\ 000\ 0001_2 = 1_{10}$$

$$1\ 000\ 0001_2 = -1_{10}$$

$$1\ 000\ 0000_2 = -0_{10} \text{ (?)}$$

- **Two's complement:**

given a positive integer  $a$ , the **two's complement** of  $a$  relative to a fixed **bit length**  $n$  is the binary representation of

$$2^n - a.$$

$$n=4$$

## Example: 4-bit two's complement ( $n=4$ )

- $a = 1$ , two's complement:  $2^4 - 1 = 15 = 1111_2 = -1$
- $a = 2$ , two's complement:  $2^4 - 2 = 14 = 1110_2 = -2$
- $a = 3$ , two's complement:  $2^4 - 3 = 13 = 1101_2 = -3$
- ...
- $a = 8$ , two's complement:  $2^4 - 8 = 8 = 1000_2 = -8$

"normal" binary

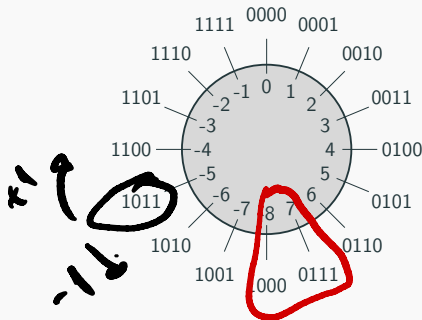
2's complement



# Properties

- Positive numbers start with 0, negative numbers start with 1
- 0 is always represented as a string of zeros
- $-1$  is always represented as a string of ones

Example: 4-bits



- The number range is split unevenly between +ve and -ve numbers
- The range of numbers we can represent in  $n$  bits is  $-2^{n-1}$  to  $2^{n-1} - 1$