

Practical 3

Matthew Rothenburg
EEE3096S
University of Cape Town
South Africa
RTHMAT003

September 23, 2024

Abstract - This report covers the implementation of practical 3 on the STM32 development board. A LED was blinked at a certain frequency using timer 6, this frequency could be switched between two value using a push-button. The push-button made use of hardware interrupts in order to switch frequencies. The ADC was used in order to read a potentiometer, and a PWM signal relative to the potentiometer was sent to a LED in order to dim it. Various value were written to the EEPROM using SPI and were then repeatedly fetched and displayed to the LCD.

1 Introduction

There are several goals of this practical. The first is to too blink an LED with a variable frequency, this frequency can be switched between two values through use of a push button. The second goal is too dim a second LED in accordance with a potentiometer. The final goal of this practical is too communicate with the on-board EEPROM over SPI, this is done too store and load values. These value are then to be displayed on the LCD

2 Method

2.1 Software Implementation

In order to blink the on board LED at a certain frequency TIM6 was used. The ARR and prescaler were both configured to result in an interrupt being triggered at the frequency of 2Hz. During the interrupt the LED is toggled, thus causing the LED to flash with a frequency of 2Hz.

In order to change the frequency of the pulsing LED a push button was used. This push-button was configured in interrupt mode. Upon the push button being pressed the interrupt service routine would be called, during this ser-

vice routine TIM6 was paused its prescaler was changed to result in a frequency of 1 Hz and then restarted. This cause the pulsing LEDs frequency to change as well. Upon multiple presses the frequency would toggle between 1 Hz and 2 Hz. The push button was also debounced using HAL getTick() to stop interrupts caused by component tolerances.

The potentiometer was read through the ADC. TIM3 was setup to send a PWM signal to a LED. This PWM signal dims the LED in relation to the potentiometers reading. The STM32 has a 12 bit ADC while TIM3 is setup in 16 bit mode. This causes issues as the potentiometer readings range from 0 - 4095 and TIM3 using values ranging from 0 - 4799. To solve this a formula was used map potentiomter to TIM3.

$$PWM = \frac{ADC \times 4799}{4096}$$

Using the on board EEPROM allows for value to be retained even between power cycles. Values were stored and retrieved through the use of the SPI communication protocol. Several different binary values were stored on the EEPROM. These values were then fetched one by one, converted to decimal numbers and then displayed to the LCD screen. This was done continuously, producing a pattern of displayed number on the screen.

3 Conclusion

This practical successfully demonstrated the use of various peripherals on the STM32 development board, including timers, ADC, PWM, and SPI communication. Each of the requirements laid out were completed successfully.

```

1  /* USER CODE BEGIN Header */
2  /**
3   * *****
4   * @file           : main.c
5   * @brief          : Main program body
6   * *****
7   * @attention
8   *
9   * Copyright (c) 2023 STMicroelectronics.
10  * All rights reserved.
11  *
12  * This software is licensed under terms that can be found in the LICENSE file
13  * in the root directory of this software component.
14  * If no LICENSE file comes with this software, it is provided AS-IS.
15  *
16  * *****
17  */
18  /* USER CODE END Header */
19  /* Includes -----*/
20  #include "main.h"
21
22  /* Private includes -----*/
23  /* USER CODE BEGIN Includes */
24  #include <stdio.h>
25  #include "stm32f0xx.h"
26  #include <lcd_stm32f0.c>
27  /* USER CODE END Includes */
28
29  /* Private typedef -----*/
30  /* USER CODE BEGIN PTD */
31
32  /* USER CODE END PTD */
33
34  /* Private define -----*/
35  /* USER CODE BEGIN PD */
36
37  // Definitions for SPI usage
38  #define MEM_SIZE 8192 // bytes
39  #define WREN 0b00000110 // enable writing
40  #define WRDI 0b00000100 // disable writing
41  #define RDSR 0b00000101 // read status register
42  #define WRSR 0b00000001 // write status register
43  #define READ 0b00000011
44  #define WRITE 0b00000010
45  /* USER CODE END PD */
46
47  /* Private macro -----*/
48  /* USER CODE BEGIN PM */
49
50  /* USER CODE END PM */
51
52  /* Private variables -----*/
53  ADC_HandleTypeDef hadc;
54
55  TIM_HandleTypeDef htim3;
56  TIM_HandleTypeDef htim6;
57  TIM_HandleTypeDef htim16;
58
59  /* USER CODE BEGIN PV */
60
61  // TODO: Define input variables
62  uint8_t LED7_Freq = 0;
63  uint8_t debounceDelay = 100;
64  int lastTick = 0;
65  int currentTick = 0;
66  uint16_t currentAddress = 0;
67
68  uint8_t bitVals[6] = {
69      0b10101010,
70      0b01010101,
71      0b11001100,
72      0b00110011,
73      0b11110000,
74      0b00001111

```

```

75 };
76
77
78 /* USER CODE END PV */
79
80 /* Private function prototypes -----*/
81 void SystemClock_Config(void);
82 static void MX_GPIO_Init(void);
83 static void MX_ADC_Init(void);
84 static void MX_TIM3_Init(void);
85 static void MX_TIM16_Init(void);
86 static void MX_TIM6_Init(void);
87 /* USER CODE BEGIN PFP */
88 void EXTI0_1_IRQHandler(void);
89 void TIM16_IRQHandler(void);
90 void writeLCD(char *char_in);
91
92 // ADC functions
93 uint32_t pollADC(void);
94 uint32_t ADCtoCCR(uint32_t adc_val);
95
96 // SPI functions
97 static void init_spi(void);
98 static void write_to_address(uint16_t address, uint8_t data);
99 static uint8_t read_from_address(uint16_t address);
100 static void spi_delay(uint32_t delay_in_us);
101 /* USER CODE END PFP */
102
103 /* Private user code -----*/
104 /* USER CODE BEGIN 0 */
105
106 /* USER CODE END 0 */
107
108 /**
109  * @brief The application entry point.
110  * @retval int
111  */
112 int main(void)
113 {
114
115     /* USER CODE BEGIN 1 */
116     /* USER CODE END 1 */
117
118     /* MCU Configuration-----*/
119
120     /* Reset of all peripherals, Initializes the Flash interface and the Systick. */
121     HAL_Init();
122
123     /* USER CODE BEGIN Init */
124     /* USER CODE END Init */
125
126     /* Configure the system clock */
127     SystemClock_Config();
128
129     /* USER CODE BEGIN SysInit */
130     /* USER CODE END SysInit */
131
132     /* Initialize all configured peripherals */
133     init_spi();
134     MX_GPIO_Init();
135     MX_ADC_Init();
136     MX_TIM3_Init();
137     MX_TIM16_Init();
138     MX_TIM6_Init();
139     /* USER CODE BEGIN 2 */
140
141     // Initialise LCD
142     init_LCD();
143     lcd_command(TWOLINE_MODE);
144     lcd_putstr("EEPROM byte:");
145
146     // Start timers
147     HAL_TIM_Base_Start_IT(&htim6);
148     HAL_TIM_Base_Start_IT(&htim16);
149

```

```

150 // PWM setup
151 uint32_t CCR = 0;
152 HAL_TIM_PWM_Start(&htim3, TIM_CHANNEL_3); // Start PWM on TIM3 Channel 3
153
154 // TODO: Write all bytes to EEPROM using "write_to_address"
155 uint16_t startAddress = 0;
156 for(int i = 0; i < 6; i++){
157     write_to_address(startAddress + i, bitVals[i]);
158 }
159
160 /* USER CODE END 2 */
161
162 /* Infinite loop */
163 /* USER CODE BEGIN WHILE */
164 while (1)
165 {
166
167     // TODO: Poll ADC
168     uint32_t adcVal = pollADC();
169     // TODO: Get CRR
170     uint32_t brightness = ADCtoCCR(adcVal);
171
172     // Update PWM value
173     __HAL_TIM_SetCompare(&htim3, TIM_CHANNEL_3, brightness);
174
175     /* USER CODE END WHILE */
176
177     /* USER CODE BEGIN 3 */
178 }
179 /* USER CODE END 3 */
180 }
181
182 /**
183  * @brief System Clock Configuration
184  * @retval None
185  */
186 void SystemClock_Config(void)
187 {
188     LL_FLASH_SetLatency(LL_FLASH_LATENCY_0);
189     while(LL_FLASH_GetLatency() != LL_FLASH_LATENCY_0)
190     {
191     }
192     LL_RCC_HSI_Enable();
193
194     /* Wait till HSI is ready */
195     while(LL_RCC_HSI_IsReady() != 1)
196     {
197
198     }
199     LL_RCC_HSI_SetCalibTrimming(16);
200     LL_RCC_HSI14_Enable();
201
202     /* Wait till HSI14 is ready */
203     while(LL_RCC_HSI14_IsReady() != 1)
204     {
205
206     }
207     LL_RCC_HSI14_SetCalibTrimming(16);
208     LL_RCC_SetAHBPrescaler(LL_RCC_SYSCLK_DIV_1);
209     LL_RCC_SetAPB1Prescaler(LL_RCC_APB1_DIV_1);
210     LL_RCC_SetSysClkSource(LL_RCC_SYS_CLKSOURCE_HSI);
211
212     /* Wait till System clock is ready */
213     while(LL_RCC_GetSysClkSource() != LL_RCC_SYS_CLKSOURCE_STATUS_HSI)
214     {
215
216     }
217     LL_SetSystemCoreClock(8000000);
218
219     /* Update the time base */
220     if (HAL_InitTick (TICK_INT_PRIORITY) != HAL_OK)
221     {
222         Error_Handler();
223     }
224     LL_RCC_HSI14_EnableADCControl();

```

```

225 }
226
227 /**
228  * @brief ADC Initialization Function
229  * @param None
230  * @retval None
231  */
232 static void MX_ADC_Init(void)
233 {
234
235     /* USER CODE BEGIN ADC_Init 0 */
236     /* USER CODE END ADC_Init 0 */
237
238     ADC_ChannelConfTypeDef sConfig = {0};
239
240     /* USER CODE BEGIN ADC_Init 1 */
241
242     /* USER CODE END ADC_Init 1 */
243
244     /** Configure the global features of the ADC (Clock, Resolution, Data Alignment
245         and number of conversion)
246     */
247     hadc.Instance = ADC1;
248     hadc.Init.ClockPrescaler = ADC_CLOCK_ASYNC_DIV1;
249     hadc.Init.Resolution = ADC_RESOLUTION_12B;
250     hadc.Init.DataAlign = ADC_DATAALIGN_RIGHT;
251     hadc.Init.ScanConvMode = ADC_SCAN_DIRECTION_FORWARD;
252     hadc.Init.EOCSelection = ADC_EOC_SINGLE_CONV;
253     hadc.Init.LowPowerAutoWait = DISABLE;
254     hadc.Init.LowPowerAutoPowerOff = DISABLE;
255     hadc.Init.ContinuousConvMode = DISABLE;
256     hadc.Init.DiscontinuousConvMode = DISABLE;
257     hadc.Init.ExternalTrigConv = ADC_SOFTWARE_START;
258     hadc.Init.ExternalTrigConvEdge = ADC_EXTERNALTRIGCONVEDGE_NONE;
259     hadc.Init.DMAContinuousRequests = DISABLE;
260     hadc.Init.Overrun = ADC_OVR_DATA_PRESERVED;
261     if (HAL_ADC_Init(&hadc) != HAL_OK)
262     {
263         Error_Handler();
264     }
265
266     /** Configure for the selected ADC regular channel to be converted.
267     */
268     sConfig.Channel = ADC_CHANNEL_6;
269     sConfig.Rank = ADC_RANK_CHANNEL_NUMBER;
270     sConfig.SamplingTime = ADC_SAMPLETIME_1CYCLE_5;
271     if (HAL_ADC_ConfigChannel(&hadc, &sConfig) != HAL_OK)
272     {
273         Error_Handler();
274     }
275
276     /* USER CODE BEGIN ADC_Init 2 */
277     ADC1->CR |= ADC_CR_ADSCAL;
278     while(ADC1->CR & ADC_CR_ADSCAL); // Calibrate the ADC
279     ADC1->CR |= (1 << 0); // Enable ADC
280     while((ADC1->ISR & (1 << 0)) == 0); // Wait for ADC ready
281     /* USER CODE END ADC_Init 2 */
282 }
283
284 /**
285  * @brief TIM3 Initialization Function
286  * @param None
287  * @retval None
288  */
289 static void MX_TIM3_Init(void)
290 {
291
292     /* USER CODE BEGIN TIM3_Init 0 */
293
294     /* USER CODE END TIM3_Init 0 */
295
296     TIM_ClockConfigTypeDef sClockSourceConfig = {0};
297     TIM_MasterConfigTypeDef sMasterConfig = {0};
298     TIM_OC_InitTypeDef sConfigOC = {0};

```

```

299  /* USER CODE BEGIN TIM3_Init 1 */
300
301  /* USER CODE END TIM3_Init 1 */
302  htim3.Instance = TIM3;
303  htim3.Init.Prescaler = 0;
304  htim3.Init.CounterMode = TIM_COUNTERMODE_UP;
305  htim3.Init.Period = 47999;
306  htim3.Init.ClockDivision = TIM_CLOCKDIVISION_DIV1;
307  htim3.Init.AutoReloadPreload = TIM_AUTORELOAD_PRELOAD_DISABLE;
308  if (HAL_TIM_Base_Init(&htim3) != HAL_OK)
309  {
310      Error_Handler();
311  }
312  sClockSourceConfig.ClockSource = TIM_CLOCKSOURCE_INTERNAL;
313  if (HAL_TIM_ConfigClockSource(&htim3, &sClockSourceConfig) != HAL_OK)
314  {
315      Error_Handler();
316  }
317  if (HAL_TIM_PWM_Init(&htim3) != HAL_OK)
318  {
319      Error_Handler();
320  }
321  sMasterConfig.MasterOutputTrigger = TIM_TRGO_RESET;
322  sMasterConfig.MasterSlaveMode = TIM_MASTERSLAVEMODE_DISABLE;
323  if (HAL_TIMEx_MasterConfigSynchronization(&htim3, &sMasterConfig) != HAL_OK)
324  {
325      Error_Handler();
326  }
327  sConfigOC.OCMode = TIM_OCMODE_PWM1;
328  sConfigOC.Pulse = 0;
329  sConfigOC.OCpolarity = TIM_OCPOLARITY_HIGH;
330  sConfigOC.OCFastMode = TIM_OCFAST_DISABLE;
331  if (HAL_TIM_PWM_ConfigChannel(&htim3, &sConfigOC, TIM_CHANNEL_3) != HAL_OK)
332  {
333      Error_Handler();
334  }
335  /* USER CODE BEGIN TIM3_Init 2 */
336
337  /* USER CODE END TIM3_Init 2 */
338  HAL_TIM_MspPostInit(&htim3);
339
340 }
341
342 /**
343  * @brief TIM6 Initialization Function
344  * @param None
345  * @retval None
346  */
347 static void MX_TIM6_Init(void)
348 {
349
350  /* USER CODE BEGIN TIM6_Init 0 */
351
352  /* USER CODE END TIM6_Init 0 */
353
354  TIM_MasterConfigTypeDef sMasterConfig = {0};
355
356  /* USER CODE BEGIN TIM6_Init 1 */
357
358  /* USER CODE END TIM6_Init 1 */
359  htim6.Instance = TIM6;
360  htim6.Init.Prescaler = 8000-1;
361  htim6.Init.CounterMode = TIM_COUNTERMODE_UP;
362  htim6.Init.Period = 500-1;
363  htim6.Init.AutoReloadPreload = TIM_AUTORELOAD_PRELOAD_ENABLE;
364  if (HAL_TIM_Base_Init(&htim6) != HAL_OK)
365  {
366      Error_Handler();
367  }
368  sMasterConfig.MasterOutputTrigger = TIM_TRGO_RESET;
369  sMasterConfig.MasterSlaveMode = TIM_MASTERSLAVEMODE_DISABLE;
370  if (HAL_TIMEx_MasterConfigSynchronization(&htim6, &sMasterConfig) != HAL_OK)
371  {
372      Error_Handler();
373  }

```

```

374  /* USER CODE BEGIN TIM6_Init 2 */
375  NVIC_EnableIRQ(TIM6_IRQn);
376  /* USER CODE END TIM6_Init 2 */
377
378  }
379
380  /**
381   * @brief TIM16 Initialization Function
382   * @param None
383   * @retval None
384   */
385  static void MX_TIM16_Init(void)
386  {
387
388      /* USER CODE BEGIN TIM16_Init 0 */
389
390      /* USER CODE END TIM16_Init 0 */
391
392      /* USER CODE BEGIN TIM16_Init 1 */
393
394      /* USER CODE END TIM16_Init 1 */
395      htim16.Instance = TIM16;
396      htim16.Init.Prescaler = 8000-1;
397      htim16.Init.CounterMode = TIM_COUNTERMODE_UP;
398      htim16.Init.Period = 1000-1;
399      htim16.Init.ClockDivision = TIM_CLOCKDIVISION_DIV1;
400      htim16.Init.RepetitionCounter = 0;
401      htim16.Init.AutoReloadPreload = TIM_AUTORELOAD_PRELOAD_ENABLE;
402      if (HAL_TIM_Base_Init(&htim16) != HAL_OK)
403      {
404          Error_Handler();
405      }
406      /* USER CODE BEGIN TIM16_Init 2 */
407      NVIC_EnableIRQ(TIM16_IRQn);
408      /* USER CODE END TIM16_Init 2 */
409
410  }
411
412  /**
413   * @brief GPIO Initialization Function
414   * @param None
415   * @retval None
416   */
417  static void MX_GPIO_Init(void)
418  {
419      LL_EXTI_InitTypeDef EXTI_InitStruct = {0};
420      LL_GPIO_InitTypeDef GPIO_InitStruct = {0};
421      /* USER CODE BEGIN MX_GPIO_Init_1 */
422      /* USER CODE END MX_GPIO_Init_1 */
423
424      /* GPIO Ports Clock Enable */
425      LL_AHB1_GRP1_EnableClock(LL_AHB1_GRP1_PERIPH_GPIOF);
426      LL_AHB1_GRP1_EnableClock(LL_AHB1_GRP1_PERIPH_GPIOA);
427      LL_AHB1_GRP1_EnableClock(LL_AHB1_GRP1_PERIPH_GPIOB);
428
429      /**/
430      LL_GPIO_ResetOutputPin(LED7_GPIO_Port, LED7_Pin);
431
432      /**/
433      LL_SYSCFG_SetEXTISource(LL_SYSCFG_EXTI_PORTA, LL_SYSCFG_EXTI_LINE0);
434
435      /**/
436      LL_GPIO_SetPinPull(Button0_GPIO_Port, Button0_Pin, LL_GPIO_PULL_UP);
437
438      /**/
439      LL_GPIO_SetPinMode(Button0_GPIO_Port, Button0_Pin, LL_GPIO_MODE_INPUT);
440
441      /**/
442      EXTI_InitStruct.Line_0_31 = LL_EXTI_LINE_0;
443      EXTI_InitStruct.LineCommand = ENABLE;
444      EXTI_InitStruct.Mode = LL_EXTI_MODE_IT;
445      EXTI_InitStruct.Trigger = LL_EXTI_TRIGGER_RISING;
446      LL_EXTI_Init(&EXTI_InitStruct);
447
448      /**/

```

```

449 GPIO_InitStruct.Pin = LED7_Pin;
450 GPIO_InitStruct.Mode = LL_GPIO_MODE_OUTPUT;
451 GPIO_InitStruct.Speed = LL_GPIO_SPEED_FREQ_LOW;
452 GPIO_InitStruct.OutputType = LL_GPIO_OUTPUT_PUSHPULL;
453 GPIO_InitStruct.Pull = LL_GPIO_PULL_NO;
454 LL_GPIO_Init(LED7_GPIO_Port, &GPIO_InitStruct);
455
456 /* USER CODE BEGIN MX_GPIO_Init_2 */
457 HAL_NVIC_SetPriority(EXTIO_1_IRQn, 0, 0);
458 HAL_NVIC_EnableIRQ(EXTIO_1_IRQn);
459 /* USER CODE END MX_GPIO_Init_2 */
460 }
461
462 /* USER CODE BEGIN 4 */
463 void EXTIO_1_IRQHandler(void)
464 {
465     // TODO: Add code to switch LED7 delay frequency
466     currentTick = HAL_GetTick();
467     if(currentTick - lastTick > 150){
468         lastTick = currentTick;
469         HAL_TIM_Base_Stop(&htim6);
470         if(LED7_Freq%2 == 0){
471             htim6.Init.Period = 1000-1;
472         }
473         else{
474             htim6.Init.Period = 500-1;
475         }
476         HAL_TIM_Base_Init(&htim6);
477         HAL_TIM_Base_Start(&htim6);
478         LED7_Freq++;
479     }
480     HAL_GPIO_EXTI_IRQHandler(Button0_Pin); // Clear interrupt flags
481 }
482
483 void TIM6_IRQHandler(void)
484 {
485     // Acknowledge interrupt
486     HAL_TIM_IRQHandler(&htim6);
487
488     // Toggle LED7
489     HAL_GPIO_TogglePin(GPIOB, LED7_Pin);
490 }
491
492 void TIM16_IRQHandler(void)
493 {
494     // Acknowledge interrupt
495     HAL_TIM_IRQHandler(&htim16);
496
497     // TODO: Initialise a string to output second line on LCD
498     uint8_t number = read_from_address(currentAddress);
499     char stringToWrite[3]; // Ensure this buffer is large enough for the largest
                          // expected number
500     sprintf(stringToWrite, "%d", number);
501     lcd_command(0xC0);
502     for (int i = 0; i < 3; i++) { // Assuming a 16x2 LCD
503         lcd_putchar(' '); // Write a space character
504     }
505     if(number != bitVals[currentAddress]){
506         lcd_command(0xC0);
507         lcd_putstring("SPI ERROR!");
508     }
509     else{
510         writeLCD(stringToWrite);
511         // TODO: Change LED pattern; output 0x01 if the read SPI data is incorrect
512     }
513     currentAddress ++;
514     currentAddress = currentAddress % 6;
515 }
516
517 // TODO: Complete the writeLCD function
518 void writeLCD(char *char_in){
519     lcd_command(0xC0); // 0xC0 is the command to set the cursor to the beginning of
                          // Line 2
520     // Write each character of the string to the LCD
521     while (*char_in) {

```



```

522     lcd_putchar(*char_in); // Write each character to LCD
523     char_in++;
524 }
525 delay(3000);
526 }
527
528 // Get ADC value
529 uint32_t pollADC(void){
530     HAL_ADC_Start(&hadc); // start the adc
531     HAL_ADC_PollForConversion(&hadc, 100); // poll for conversion
532     uint32_t val = HAL_ADC_GetValue(&hadc); // get the adc value
533     HAL_ADC_Stop(&hadc); // stop adc
534     return val;
535 }
536
537 // Calculate PWM CCR value
538 uint32_t ADCToCCR(uint32_t adc_val){
539     // TODO: Calculate CCR value (val) using an appropriate equation
540     uint32_t brightness = (adc_val * 47999) / 4096;
541     //return val;
542     return brightness;
543 }
544
545 void ADC1_COMP_IRQHandler(void)
546 {
547     //adc_val = HAL_ADC_GetValue(&hadc); // read adc value
548     HAL_ADC_IRQHandler(&hadc); //Clear flags
549 }
550
551 // Initialise SPI
552 static void init_spi(void) {
553
554     // Clock to PB
555     RCC->AHBENR |= RCC_AHBENR_GPIOBEN; // Enable clock for SPI port
556
557     // Set pin modes
558     GPIOB->MODER |= GPIO_MODER_MODER13_1; // Set pin SCK (PB13) to Alternate
559         Function
560     GPIOB->MODER |= GPIO_MODER_MODER14_1; // Set pin MISO (PB14) to Alternate
561         Function
562     GPIOB->MODER |= GPIO_MODER_MODER15_1; // Set pin MOSI (PB15) to Alternate
563         Function
564     GPIOB->MODER |= GPIO_MODER_MODER12_0; // Set pin CS (PB12) to output push-pull
565     GPIOB->BSRR |= GPIO_BSRR_BS_12; // Pull CS high
566
567     // Clock enable to SPI
568     RCC->APB1ENR |= RCC_APB1ENR_SPI2EN;
569     SPI2->CR1 |= SPI_CR1_BIDIOE; // Enable output
570     SPI2->CR1 |= (SPI_CR1_BR_0 | SPI_CR1_BR_1); // Set Baud to fpclock / 16
571     SPI2->CR1 |= SPI_CR1_MSTR; // Set to master mode
572     SPI2->CR2 |= SPI_CR2_FRXTH; // Set RX threshold to be 8 bits
573     SPI2->CR2 |= SPI_CR2_SSOE; // Enable slave output to work in
574         master mode
575     SPI2->CR2 |= (SPI_CR2_DS_0 | SPI_CR2_DS_1 | SPI_CR2_DS_2); // Set to 8-bit mode
576     SPI2->CR1 |= SPI_CR1_SPE; // Enable the SPI peripheral
577 }
578
579 // Implements a delay in microseconds
580 static void spi_delay(uint32_t delay_in_us) {
581     volatile uint32_t counter = 0;
582     delay_in_us *= 3;
583     for(; counter < delay_in_us; counter++) {
584         __asm("nop");
585         __asm("nop");
586     }
587 }
588
589 // Write to EEPROM address using SPI
590 static void write_to_address(uint16_t address, uint8_t data) {
591
592     uint8_t dummy; // Junk from the DR
593
594     // Set the Write Enable latch
595     GPIOB->BSRR |= GPIO_BSRR_BR_12; // Pull CS low
596     spi_delay(1);

```

```

593 *((uint8_t*)&SPI2->DR)) = WREN;
594 while ((SPI2->SR & SPI_SR_RXNE) == 0); // Hang while RX is empty
595 dummy = SPI2->DR;
596 GPIOB->BSRR |= GPIO_BSRR_BS_12; // Pull CS high
597 spi_delay(5000);
598
599 // Send write instruction
600 GPIOB->BSRR |= GPIO_BSRR_BR_12; // Pull CS low
601 spi_delay(1);
602 *((uint8_t*)&SPI2->DR)) = WRITE;
603 while ((SPI2->SR & SPI_SR_RXNE) == 0); // Hang while RX is empty
604 dummy = SPI2->DR;
605
606 // Send 16-bit address
607 *((uint8_t*)&SPI2->DR)) = (address >> 8); // Address MSB
608 while ((SPI2->SR & SPI_SR_RXNE) == 0); // Hang while RX is empty
609 dummy = SPI2->DR;
610 *((uint8_t*)&SPI2->DR)) = (address); // Address LSB
611 while ((SPI2->SR & SPI_SR_RXNE) == 0); // Hang while RX is empty
612 dummy = SPI2->DR;
613
614 // Send the data
615 *((uint8_t*)&SPI2->DR)) = data;
616 while ((SPI2->SR & SPI_SR_RXNE) == 0); // Hang while RX is empty
617 dummy = SPI2->DR;
618 GPIOB->BSRR |= GPIO_BSRR_BS_12; // Pull CS high
619 spi_delay(5000);
620 }
621
622 // Read from EEPROM address using SPI
623 static uint8_t read_from_address(uint16_t address) {
624
625     uint8_t dummy; // Junk from the DR
626
627     // Send the read instruction
628     GPIOB->BSRR |= GPIO_BSRR_BR_12; // Pull CS low
629     spi_delay(1);
630     *((uint8_t*)&SPI2->DR)) = READ;
631     while ((SPI2->SR & SPI_SR_RXNE) == 0); // Hang while RX is empty
632     dummy = SPI2->DR;
633
634     // Send 16-bit address
635     *((uint8_t*)&SPI2->DR)) = (address >> 8); // Address MSB
636     while ((SPI2->SR & SPI_SR_RXNE) == 0); // Hang while RX is empty
637     dummy = SPI2->DR;
638     *((uint8_t*)&SPI2->DR)) = (address); // Address LSB
639     while ((SPI2->SR & SPI_SR_RXNE) == 0); // Hang while RX is empty
640     dummy = SPI2->DR;
641
642     // Clock in the data
643     *((uint8_t*)&SPI2->DR)) = 0x42; // Clock out some junk data
644     while ((SPI2->SR & SPI_SR_RXNE) == 0); // Hang while RX is empty
645     dummy = SPI2->DR;
646     GPIOB->BSRR |= GPIO_BSRR_BS_12; // Pull CS high
647     spi_delay(5000);
648
649     return dummy; // Return read data
650 }
651 /* USER CODE END 4 */
652
653 /**
654  * @brief This function is executed in case of error occurrence.
655  * @retval None
656  */
657 void Error_Handler(void)
658 {
659     /* USER CODE BEGIN Error_Handler_Debug */
660     /* User can add his own implementation to report the HAL error return state */
661     __disable_irq();
662     while (1)
663     {
664     }
665     /* USER CODE END Error_Handler_Debug */
666 }
667

```

```

668 #ifdef USE_FULL_ASSERT
669 /**
670  * @brief Reports the name of the source file and the source line number
671  *        where the assert_param error has occurred.
672  * @param file: pointer to the source file name
673  * @param line: assert_param error line source number
674  * @retval None
675  */
676 void assert_failed(uint8_t *file, uint32_t line)
677 {
678     /* USER CODE BEGIN 6 */
679     /* User can add his own implementation to report the file name and line number,
680      ex: printf("Wrong parameters value: file %s on line %d\r\n", file, line) */
681     /* USER CODE END 6 */
682 }
683 #endif /* USE_FULL_ASSERT */

```



EEE3095S/EEE3096S Practical 3 Demonstrations/Solutions
2024

Total Marks Available: 15

Group No.	30	
	Stn 1	Stn2
Student no.	PTHMAT003	
Name	Matthew Rothenburg	
Signature		

NB Please take a photo of this mark sheet and submit it with your report!

Action + Mark Allocation	Mark
Pressing PA0 should toggle the flashing frequency of LED PB7 from 0.5 seconds to 1 second, or from 1 second back to 0.5 seconds.	2 /2
The LCD should display the "EEPROM byte" with the correct formatting. This should vary between the values 10101010, 01010101, 11001100, 00110011, 11110000, and 00001111 — changing every 1 second.	4 /4
Check code: SPI must be used for this; if not, student gets zero for this task.	
The brightness of LED PB0 should vary based on the current value being read from POT1, i.e., off when POT1 is turned fully anticlockwise and maximum brightness when POT1 is turned fully clockwise.	3 /3
Check code: PA0 should have some form of debouncing enabled (see Marking Notes).	1 /1
Check code: an EXTI interrupt is used to handle PA0 presses.	1 /1
Check code: CRR is calculated correctly (see Marking Notes).	2 /2
Check code: "pollADC" and "writeLCD" functions are correctly implemented and used.	2 /2

Tutor Name:	TYRAN
Tutor Signature:	