Current Implementation

Master Cores

M0 M1 M2 M3 M4 M5

time=0

Core Queues

CQ0 CQ1 CQ2 CQ3 CQ4 CQ5   8 requests

Arbitration (QoS, Round Robin), Expansion of requests

Bank Queues

BQ0 BQ1 BQ2 BQ3 BQ4 BQ5 BQ6 BQ7   10